



**R.M.D. ENGINEERING COLLEGE**  
**(AN AUTONOMOUS INSTITUTION)**  
R.S.M. NAGAR, KAVARAIPETTAI – 601206



**Mandatory Disclosure (As on 27.02.2023)**

**Annexure 10**

**Mandatory Disclosure:**

**27<sup>th</sup> February 2023**

AICTE File No

F.No. Southern/1-10974588840/2022/EOA

Date & Period of last approval

03-Jul-2021, 2022-23

UGC Autonomous Approval

File No. F.22-1/2017(AC) Dated 22.12.2020

Anna University Autonomous Approval

Letter No. 3490/AU/CAC/Autonomous/2021  
Dated 01.03.2021

**1. Name of the Institution**

**R.M.D. Engineering College (Autonomous)**

Address of the Institution

R.S.M. Nagar

Kavaraipettai

Gummidipoondi Taluk

Thiruvallur District

601 206

Tamil Nadu

Phone number with STD code

044-67919191; 044-33303030

FAX number with STD code

044-67919190

E-Mail

principal@rmd.ac.in

Website

[www.rmd.ac.in](http://www.rmd.ac.in)

**2. Name and Address of the Trust**

Name of the Trust

**Sri Swaminatha Naidu Educational Trust**

Address of the Trust

Plot No. 2981, "Z" Block, 1<sup>st</sup> Street,

13<sup>th</sup> Main Road

Anna Nagar, Chennai – 600 040.

Phone number with STD code

044-26211504

### 3. Name and Address of the Principal

Name of Principal

**Dr. ANBUCHEZHIAN N**

Phone number with STD code

044-67919104; 33303031

FAX number with STD code

044-67919190

E-Mail

principal@rmd.ac.in

### 4. Name of the affiliating University

Address

**Anna University Chennai**

Chennai – 600 025

Website

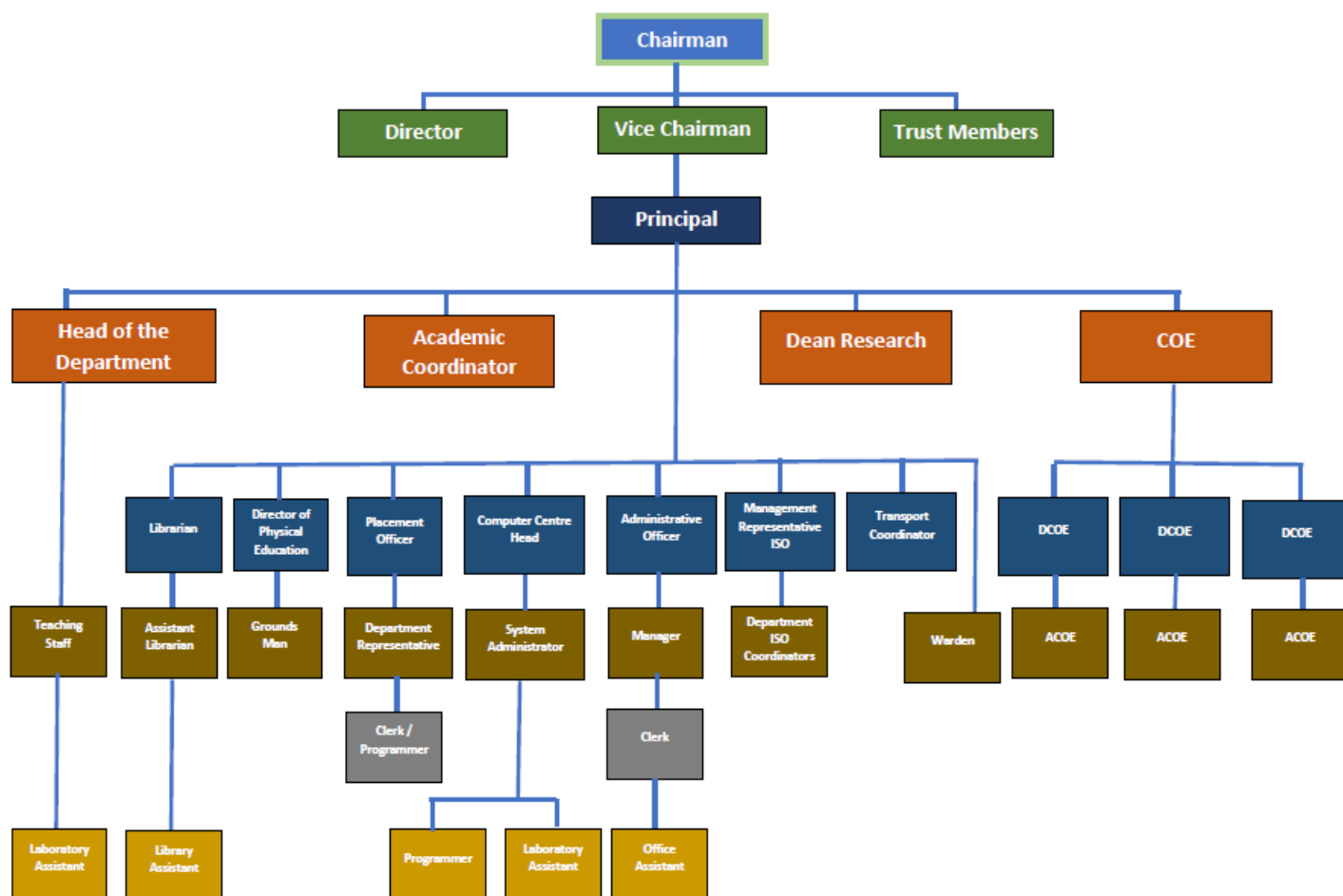
www.annauniv.edu.in

Latest affiliation period

2022-2023

## 5. Governance

### Organization chart and processes



## **Grievance redressal mechanism for faculty, staff and students**

### **Complaint and Grievance Procedures**

The purpose of this procedure is to establish a process for students to express and resolve misunderstandings, concerns, or grievances they have with any college employee in a prompt, fair and equitable manner. This procedure emphasizes an informal resolution.

- to protect each student's freedom of expression in the classroom
- to protect each student from prejudice or arbitrary and capricious academic evaluation as evidenced by the student's final course grade.
- to protect each student against improper disclosure of the student's views, beliefs and political associations.
- to protect a student's right to a learning environment that is free from unlawful discrimination.

### **Withdrawal of grievance**

At any time during the grievance procedure the student may withdraw the grievance.

### **Time limits on filing a grievance.**

A student must file a grievance by the last day of the academic semester following the action that gives rise to the grievance. Principal may suspend this rule under exceptional circumstances. All grievance records will be held in the Office of the Convenor for a period of one year.

### **Composition of the Grievance Redressal Committee**

Members of the Grievance Redressal Committee shall be assigned as follows,

- One (1) administrator, assigned by the college Principal. The administrator will act as the committee chair.
- Two (2) faculty members, appointed by the Principal two (2) students, appointed by the Concerned department HOD.

### **Always follow appropriate procedures in pursuing any grievance. College rules prohibit the following:**

- Obstruction or disruption of teaching, research, administration, disciplinary procedures, or other college activities. Disruption or obstruction mean conduct which disturbs, interferes with, or prevents normal campus functions and activities. Examples include creating a disturbance by yelling, using profanity, or verbally intimidating or abusing others; or making excessive or unreasonable demands for immediate action, such as demanding an appointment or a response to a grievance on the spot.
- Failure to comply with directions of a college official or resisting or obstructing such officials in the performance of their duties.

### **Scope and coverage**

A grievance may arise when a graduate student believes that his/her status as a graduate student, or University appointment based on student status, has been adversely affected by an incorrect or inappropriate decision or behavior. Examples include, but are not limited to the following:

1. Inappropriate application of a **department/unit** or University policy.
2. Being unfairly assessed on a preliminary examination.
3. Being improperly terminated from a program.
4. Being required to perform personal services unrelated to academic or assistantship duties.

Being the subject of professional misconduct by a student's graduate supervisor or other faculty or staff member

### **Establishment of Anti Ragging Committee:**

<b>Members</b>	<b>Designation / Dept</b>	<b>Designation</b>	<b>Contact Nos.</b>	<b>E-Mail ID</b>
Dr.Anbuezhian N	Principal	Chairman	9655566926	<a href="mailto:principal@rmd.ac.in">principal@rmd.ac.in</a>
Dr.K.K.Thyagarajan	Dean-Research	Member	9444112551	<a href="mailto:acdean@rmd.ac.in">acdean@rmd.ac.in</a>
Dr.K.HelenPrabha	HOD - ECE	Member	9345036915	<a href="mailto:hod.ece@rmd.ac.in">hod.ece@rmd.ac.in</a>
Dr.A.Chilambuchelvan	Controller of Examinations	Member	9443217864	<a href="mailto:coe@rmd.ac.in">coe@rmd.ac.in</a>
Mr.R.Ganesan	Administrative Officer	Member	9789534121	<a href="mailto:ao@rmd.ac.in">ao@rmd.ac.in</a>
Mr.Arul Jeshin (111519205001)	Student – Rep	Member	7708406726	<a href="mailto:uit19101@rmd.ac.in">uit19101@rmd.ac.in</a>
Ms.B.Sowmiya (111519106151)	Student-Rep	Member	6382347427	<a href="mailto:uec19416@rmd.ac.in">uec19416@rmd.ac.in</a>
Mr.K.Balachander	Parent - Rep	Member	9381039278	<a href="mailto:rkbchander@gmail.com">rkbchander@gmail.com</a>
Mr.R.Krishnasamy	Advocate / High Court	Member	9444454884	<a href="mailto:rathanaanush@gmail.com">rathanaanush@gmail.com</a>
Dr.Venkata Subbu Raju	Medical Officer	Member	9443242357	<a href="mailto:drvsmbbs@gmail.com">drvsmbbs@gmail.com</a>
Dr.PavaiMadheswari	NGO - REP	Member	8754033336	<a href="mailto:ac@rmkec.ac.in">ac@rmkec.ac.in</a>
Revenue Divisional Officer	RDO, Ponneri	Member	9445000410	<a href="mailto:pnirido.tntlr@nic.in">pnirido.tntlr@nic.in</a>
Inspector of Police	Police / F4, Police Station, Kavaraipettai	Member	044-27925561	<a href="mailto:tnpudhayap@gmail.com">tnpudhayap@gmail.com</a>

### **Establishment of Online Grievance Redressal Mechanism:**

<https://rmd.ac.in/Grievance/index.html>

## Establishment of Grievance redressal Committee in the Institution and the Appointment of OBUDSMAN by the University

Name	Designation / Department	Position in the Committee	Contact Nos.
<b>Dr.Anbuchezhian N</b>	<b>Principal</b>	<b>Chairman</b>	<b>044 - 67919104</b>
Dr. Balasubadra K	Professor and Head-IT Dept	Member	9865041012
Dr.Chilambuchelvan A	Professor and COE	Member	9443217864
Dr.Tamil Selvi V	Professor and Head – S&H Dept	Member	9444551371
Ms.B.Sowmiya	IV Year, B.E. Electronics & Commn Engg.	Special Invitee	044-67919191

**\*Ombudsman :** The Director, Centre for Student Affairs, AU as appointed by the Anna University

## Establishment of Internal Complaints Committee (ICC)

Members	Designation / Dept	Designation	Contact Nos.	E-Mail ID
Dr.K. Balasubadra	Professor & HOD/IT	Presiding Officer	9865041012	<a href="mailto:hodit@rmd.ac.in">hodit@rmd.ac.in</a>
Dr.R. Priya	Professor	Member	9444765290	<a href="mailto:drpriya.snh@rmd.ac.in">drpriya.snh@rmd.ac.in</a>
Dr.A. Sumaya Begum	Associate Professor	Member	9790996037	<a href="mailto:asb.ece@rmd.ac.in">asb.ece@rmd.ac.in</a>
Mr.R.Ganesan	Administrative Officer	Member	9789534121	<a href="mailto:ao@rmd.ac.in">ao@rmd.ac.in</a>
Mrs. Maria Antony Swapna	Placement Officer	Member	9940561176	<a href="mailto:tnp@rmd.ac.in">tnp@rmd.ac.in</a>
Mr. Arul Jeshin	Student – Rep. (IV - IT )	Member	7708406726	<a href="mailto:uit19101@rmd.ac.in">uit19101@rmd.ac.in</a>
Ms. B. Sowmiya	Student-Rep (IV - ECE)	Member	6382347427	<a href="mailto:uec19416@rmd.ac.in">uec19416@rmd.ac.in</a>
Ms. S. Manasa	Student – Rep. (IV - IT )	Member	9940491777	<a href="mailto:uit19126@rmd.ac.in">uit19126@rmd.ac.in</a>
Mr.R.Krishnasamy	Advocate / High Court	Member	9444454884	<a href="mailto:rathanaanush@gmail.com">rathanaanush@gmail.com</a>

## Establishment of Committee for SC/ST

Members	Designation / Dept	Coordinator/ Member	Contact Nos.	E-Mail ID
Dr.Anbuezhian N	Principal	Convenor	9655566926	<a href="mailto:principal@rmd.ac.in">principal@rmd.ac.in</a>
Dr.K.HelenPrabha	Professor & HOD-ECE	Member / Registry Incharge	9345036915	<a href="mailto:hod.ece@rmd.ac.in">hod.ece@rmd.ac.in</a>
Dr.K. Balasubadra	Professor	Member	9865041012	<a href="mailto:hodit@rmd.ac.in">hodit@rmd.ac.in</a>
Mrs. P. Poonkuzhali	Associate Professor	Member	9444060143	<a href="mailto:poonkuzhali.ece@rmd.ac.in">poonkuzhali.ece@rmd.ac.in</a>
Dr.PavaiMadheswari	NGO	Member	8754033336	<a href="mailto:ac@rmkec.ac.in">ac@rmkec.ac.in</a>
Mr.R.Ganesan	Administrative Officer	Member	9789534121	<a href="mailto:ao@rmd.ac.in">ao@rmd.ac.in</a>
Mr.R.Krishnasamy	Advocate /High Court	Member	9444454884	<a href="mailto:rathanaanush@gmail.com">rathanaanush@gmail.com</a>

## Internal Quality Assurance Cell (IQAC)

S.No.	Name of the Faculty	Designation	Composition criteria specified by NAAC	Position in IQAC
1.	Thiru R.S. Munirathinam	Founder Chairman	Management Member	Member
2.	Shri. R.M. Kishore	Vice Chairman	Management Member	Member
3.	Thiru. R. Jothi Naidu	Managing Director, Sri Ganapathy Bricks Industries, Thiruvalluvar colony, Anna Nagar, Chennai-40	Industrialist	Member
4.	Dr.M.S.Palanichamy	Former Vice Chancellor, Tamil Nadu Open University	Educationist & Local Society	Member
5.	Mr. S. Krithivasan	Head – Strategy, Partnership and Engagement – University Hiring, Virtusa Consultancy Services Pvt. Ltd, Chennai.	Employer	Member
6.	Dr. S. Venkateswari	Department of CSE, L.N Government Arts College, Ponneri	Stake Holder-Parent	Member
7.	Dr.K.K.Thyagarajan	Dean-Research	Senior Faculty	Member
8.	Dr.P.Ezhumalai	Professor & Head / CSE	Senior Faculty	Member
9.	Dr. K. HelenPrabha	Professor & Head / ECE	Senior Faculty	Member
10.	Dr. G. Amudha	Professor & Head / CSBS	Senior Faculty	Member
11.	Dr. C S Anita	Professor & Head / AI&ML	Senior Faculty	Member
12.	Dr. V. Tamilselvi	Professor & Head / EEE	Senior Faculty	Member
13.	Dr.A. Chilambuchelvan	Professor & Head / EIE & Controller of Examinations	Senior Faculty	Member
14.	Dr.D.RukmaniDevi	Professor / ECE	Senior Faculty	Member
15.	Dr. R. Priya	Professor/ S&H	Senior Faculty	Member
16.	Dr.S.Muthusundari	Associate Professor / Dept. IQAC Coordinator/ CSE (ISO-MR)	Faculty	Member
17.	Dr. C. Shobana Nageswari	Associate Professor / Dept. IQAC Coordinator/ ECE	Faculty	Member
18.	Ms.J.Sumitra	Associate Professor / Dept. IQAC Coordinator/ EEE	Faculty	Member
19.	Mrs, P. Aileen Sonia Dhas	Assistant Professor / Dept. IQAC Coordinator/ EIE	Faculty	Member

<b>S.No.</b>	<b>Name of the Faculty</b>	<b>Designation</b>	<b>Composition criteria specified by NAAC</b>	<b>Position in IQAC</b>
20.	Dr.R.Jothilakshmi	Associate Professor / Dept. IQAC Coordinator/IT	Faculty	Member
21.	Dr.G.Ganapathy	Assistant Professor / Dept. IQAC Coordinator/ S& H	Faculty	Member
22.	Dr. S. Shalini	Assistant Professor / Dept. IQAC Coordinator/ S& H	Faculty	Member
23.	Dr. N. Padmavathy	Assistant Professor / Dept. IQAC Coordinator/ S& H	Faculty	Member
24.	Dr. G. Gayathri Devi	Assistant Professor / S&H Covener- Higher Education Cell	Faculty	Member
25.	Dr. C. Benniala Thangammal	Professor/ECE, Converner- Institutions Innovation Council & Business Incubator	Faculty	Member
26.	Mr.R.Ganesan	Administrative Officer	Senior Administrative Officer	Member
27.	Ms.Maria Swapna Antony	Placement Officer	Administration	Member
28.	Ms. S. Sharmila	II Year Student / AI&ML	Student Nominee	Member
29.	Ms. B. Praveena	Assistant Professor RMKEC / EIE	Alumni Nominee	Member
30.	Dr. K. Balasubadra	Professor & Head / IT	Senior Faculty	Coordinator
31.	Dr. N.Anbuezhian	Principal	Head of the Institution	Chairperson



## 6. Programmes

### Name of Programmes Approved by AICTE

Sl.No.	Degree	Name of the Programme
1.	B.E.	Computer Science and Engineering
2.	B.E.	Electrical and Electronics Engineering **
3.	B.E.	Electronics and Communication Engineering
4.	B.E.	Electronics and Instrumentation Engineering *
5.	B.Tech.	Information Technology
6.	B.Tech.	Computer Science and Business Systems
7.	B.Tech.	Artificial Intelligence and Machine Learning

\* Course Closed from 2020-21

\*\* Course Closed from 2021-22

### Name of Programmes Accredited by NBA

Sl.No.	Degree	Name of the Programme
1.	B.E.	Computer Science and Engineering
2.	B.E.	Electrical and Electronics Engineering
3.	B.E.	Electronics and Communication Engineering
4.	B.E.	Electronics and Instrumentation Engineering
5.	B.Tech.	Information Technology

## Status of NBA Accreditation of the Courses

Total no. Of Courses : **07**

Total no. Of Accredited Courses: **05 (All the eligible U.G Courses are accredited)**

Course	Year of Introduction	Accreditation status	Period of accreditation	Letter No. and Date
B.E. Computer Science & Engineering	2001	Accredited	2020-2021 to 2022-2023 i.e., upto 30.06.2023	F.No.33-243/2010-NBA Dated 05.03.2020
B.E. Electrical & Electronics Engineering	2001			
B.E. Electronics & Communication Engineering	2001			
B.E. Electronics & Instrumentation Engineering	2009			
B.Tech. Information Technology	2001			
B.Tech Computer Science and Business Systems	2020	Not Eligible for Accreditation	-	-
B.Tech Artificial Intelligence and Machine Learning	2021	Not Eligible for Accreditation	-	-

## Status of NAAC Accreditation

Status of Accreditation	Accreditation Grade
Accredited	B++

## Name / No. Of Seats / Duration of the programmes

Sl. No.	Degree	Name of the Programme	Sanctioned intake in the academic year 2022 -2023	Duration of the Course (In years)
1.	B.E.	Computer Science and Engineering	180	4
2.	B.E.	Electronics and Communication Engineering	180	4
3.	B.Tech.	Information Technology	60	4
4.	B.Tech.	Computer Science and Business Systems	60	4
5.	B.Tech.	Artificial Intelligence and Machine Learning	60	4

**Cut off marks/rank of admission during the last three years.**

Sl.No.	Degree	Name of the Programme	Academic Year		
			2022-2023	2021-2022	2020-2021
1	B.E.	Computer Science and Engineering	196.00 -136.00	190.66 - 140.00	190.00 - 120.66
2	B.E.	Electronics and Communication Engineering	186.66 - 134.00	192.00 - 128.00	177.34 - 102.66
3	B.Tech.	Information Technology	188.00 -149.34	188.00 - 159.34	185.34 - 98.36
4	B.Tech.	Computer Science and Business Systems	182.66 - 146.00	186.00 - 158.00	168.00 - 87.34
5	B.Tech.	Artificial Intelligence and Machine Learning	183.34 - 116.00	187.34 - 137.34	NA

**Fee details during last three years**

Sl.No.	Degree	Name of the Programme	Fee in Rs. (As per Govt Norms)		
			2022-2023	2021-2022	2020-2021
1.	B.E.	Computer Science and Engineering	Rs. 55,000/- (All Accredited Courses)	Rs. 55,000/- (All Accredited Courses)	Rs. 55,000/- (All Accredited Courses)
2.	B.E.	Electrical and Electronics Engineering			
3.	B.E.	Electronics and Communication Engineering			
4.	B.Tech.	Information Technology			
5.	B. Tech.	Computer Science and Business System	Rs.50,000/-	Rs.50,000/-	Rs.50,000/-
6	B. Tech.	Artificial Intelligence and Machine Learning	Rs.50,000/-	Rs.50,000/-	NA

## Placement Facilities

- The department of Training and Placement plays a key role in training and guiding the students for their successful career in core, product, and service industries.
- There is an extensive industry collaboration leading to academic excellence, recruitments, value addition courses and research.
- A signature Programme of R.M.D. Engineering College is its Industry specific training programmes.
- Well-structured training programmes are conducted in phases by leading corporate trainer's right from the first semester to improve students' technical. Interpersonal, aptitude and communication skills.
- Eminent personalities offer their support through career counselling and motivation programmes.
- Skilled personalities organize personality development workshops.
- Mock interviews are arranged by experienced senior professors and industry-expert's alumnus.
- Internships provide practical exposure through industry projects.
- Students with innovative ideas are encouraged to participate in corporate contests.
- Various business unit hiring requirements and training implemented on industry-framed curriculum are identified to make students job-ready. Placement process has been digitized.

## Campus placement in last three years with minimum salary, maximum salary and average salary

Batch	No. of Students Eligible	No. of Students Placed	Placement %	Min. Salary (Lakhs)	Max. Salary (Lakhs)	Avg. Salary (Lakhs)
2019-23	415	380	91.57	2.50 Lakhs	8.40 Lakhs	4.81 Lakhs
2018-22	391	376	96.16	1.90 Lakhs	8.40 Lakhs	4.50 Lakhs
2017-21	399	350	87.72	1.80 Lakhs	10.00 Lakhs	4.46 Lakhs
2016-20	355	322	90.70	1.44 Lakhs	14.00 Lakhs	4.32 Lakhs

**\*Placement is in progress**

## 7.Faculty

### Branch wise list of Faculty Members

#### DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

S. No	Name of the Faculty	Designation
	<b>B.E.-CSE</b>	
1	Dr. P. Ezhumalai	Professor and Head
2	Dr. R. Sasikumar	Professor
3	Dr. S. Srinivasan	Professor
4	Dr. P. Shobha Rani	Professor
5	Dr.P.M.Joe Prathap	Professor
6	Dr.V.Prasanna Srinivasan	Professor
7	Dr. M.A. Berlin	Professor
8	Dr. S. Muthusundari	Associate Professor
9	Dr. A. Gnanasekar	Associate Professor
10	Dr. D. Rajalakshmi	Associate Professor
11	Dr. M. Vedaraj	Associate Professor
12	Dr. S. Murugesan	Associate Professor
13	Dr. N. Ramshankar	Associate Professor
14	Dr. A.K. Jaithunbi	Associate Professor
15	Mr.K.Mohanasundaram	Assistant Professor
16	Ms. D.Vishnu Sakthi	Assistant Professor
17	Mr. D. Jaya kumar	Assistant Professor
18	Ms. J. Geethapriya	Assistant Professor
19	Ms. K. Balasaranya	Assistant Professor
20	Ms. K.Roslin Dayana	Assistant Professor
21	Mrs.A.Tamizharasi	Assistant Professor
22	Ms. K. Padmapriya	Assistant Professor
23	Dr. G.Nirmala	Assistant Professor
24	Ms. P.Rajeshwari	Assistant Professor
25	Ms. S. Logeswari	Assistant Professor
26	Ms. V. Sharmila	Assistant Professor
27	Ms. G. Manisha	Assistant Professor
28	Ms. L. Sherin Beevi	Assistant Professor
29	Ms. J. Sherine Glory	Assistant Professor
30	Ms. E. Nalina	Assistant Professor
31	Ms. S. Geetha Priya	Assistant Professor
32	Mr. G. Shankar	Assistant Professor
33	Ms. M. Rangini	Assistant Professor
34	Ms. S. Swetha	Assistant Professor
35	Ms. D. Sterlin Rani	Assistant Professor

<b>S. No</b>	<b>Name of the Faculty</b>	<b>Designation</b>
36	Mr. P. Jagadeesan	Assistant Professor
37	Ms. D.M. Kalai Selvi	Assistant Professor
38	Ms. S. Suhasini	Assistant Professor
39	Mr. S. Karthick Murugan	Assistant Professor
40	Mr. K.E. Krishna	Assistant Professor

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

<b>S. No</b>	<b>Name of the Faculty</b>	<b>Designation</b>
1	Dr.P.Usha Rani	Professor
2	Ms.J.C.Vinitha	Associate Professor
3	Ms.M.Uma Maheswari	Assistant professor
4	Ms.S.Karkuzhali	Assistant professor
5	Ms.R.Dhanalakshmi	Assistant professor
6	Ms.R.Vanitha	Assistant professor
7	Ms. P. Priyadharshini	Assistant professor

**DEPARTMENT OF ELECTRONICS AND COMMUNICATON ENGINEERING**

<b>S. No</b>	<b>Name of the Faculty</b>	<b>Designation</b>
1	Dr.K.K.Thyagarajan	Professor & Dean (Research)
2	Dr. K. Helenprabha	Professor & Head
3	Dr.D.Rukmani Devi	Professor
4	Dr.A.Chilambuchelvan	Professor
5	Dr.C.Bennila Thangammal	Professor
6	Dr. A. Sumaiya Begum	Associate Professor
7	Dr.J.Jayaudhaya	Associate Professor
8	Dr. C.Shobana Nageswari	Associate Professor
9	Mrs. P. Poonkuzhali	Associate Professor
10	Mr.S.Balasubramani	Associate Professor
11	Dr.N.Vini Antony Grace	Associate Professor
12	Ms.J.Sumithra	Associate Professor
13	Mrs. R. Aarthi	Assistant Professor
14	Mrs.M.Shakunthala	Assistant Professor
15	Mr.M.Jyothi Prasad	Assistant Professor
16	Mr.P.Arul	Assistant Professor
17	Mrs.R.Hemalatha	Assistant Professor
18	Mr.V.Kumaravel	Assistant Professor

<b>S. No</b>	<b>Name of the Faculty</b>	<b>Designation</b>
19	Mr.B.Jaiganesh	Assistant Professor
20	Mr.D. Bharathi Dhasan	Assistant Professor
21	Mrs.P.Santhoshini	Assistant Professor
22	Mr.Prabhu.V.S	Assistant Professor
23	Mr.Karuppaiah.S	Assistant Professor
24	Mr.J.Jagan Babu	Assistant Professor
25	Mrs.G.Karthika	Assistant Professor
26	Ms. S. Gayathri Priya	Assistant Professor
27	Ms. S. Jayanthi	Assistant Professor
28	Ms. G. Preethi	Assistant Professor
29	Ms. G. Anitha	Assistant Professor
30	Dr. S.G. Hymlin Rose	Assistant Professor
31	Ms. M. Ayesha Nasreen	Assistant Professor
32	Ms. S. Indumathi	Assistant Professor
33	Ms. R.M. Senthil Priya	Assistant Professor
34	Ms. M. Amalasweena	Assistant Professor

#### **DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION ENGINEERING**

<b>S. No</b>	<b>Name of the Faculty</b>	<b>Designation</b>
1.	Dr.G.Nalinashini	Professor
2.	Mr.M.Aravindan	Assistant Professor
3.	Ms.Aileen Sonia Dhas	Assistant professor
4.	Ms.L.Aquiline Lydia	Assistant Professor

#### **DEPARTMENT OF INFORMATION TECHNOLOGY**

<b>S. No</b>	<b>Name of the Faculty</b>	<b>Designation</b>
1	Dr.K.Balasubadra	Professor and Head
2	Dr.R.Jothilakshmi	Associate Professor
3	Dr. B.Kalpana	Associate Professor
4	Dr. D.Praveena	Associate Professor
5	Dr.K.Saravanan	Associate Professor
6	Ms. M. Radhika	Assistant Professor
7	Ms. N. Abirami	Assistant Professor
8	Ms. R. Nikitha	Assistant Professor
9	Ms. G. Janani	Assistant Professor
10	Ms. N. Arockia Rosy	Assistant Professor
11	Mr. S. Velmurugan	Assistant Professor
12	Ms. Ravilla Pavithra	Assistant Professor

**DEPARTMENT OF COMPUTER SCIENCE AND BUSINESS SYSTEMS**

<b>S. No</b>	<b>Name of the Faculty</b>	<b>Designation</b>
1	Dr. G. Amudha	Professor and Head
2	Ms.CH.Srilakshmi	Assistant Professor
3	Ms. S. Anitha Rajathi	Assistant Professor
4	Ms. R. Monica Lakshmi	Assistant Professor
5	Ms. S. Deepa	Assistant Professor
6	Ms. K. Sasirekha	Assistant Professor
7	Ms. K. Sudha	Assistant Professor
8	Mr. N. Muthuvairavan Pillai	Assistant Professor

**DEPARTMENT OF ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING**

<b>S. No</b>	<b>Name of the Faculty</b>	<b>Designation</b>
1	Dr. C.S. Anita	Professor and Head
2	Ms. Remya Rose	Assistant Professor
3	Mr. N. Sathish Kumar	Assistant Professor
4	Ms. Badi Alekhya	Assistant Professor

**DEPARTMENT OF SCIENCE AND HUMANITIES**

<b>S. No</b>	<b>Name of the Faculty</b>	<b>Designation</b>
1	Dr. N. Anbuezhian	Principal
	<b>MATHEMATICS</b>	
2	Dr. P. Venkata Mohan Reddy	Associate Professor
3	Dr. S. Saravanan	Associate Professor
4	Ms. K. Sumathy	Associate Professor
5	Dr. S. Rajakumari	Assistant Professor
6	Dr. V. Choudri	Assistant Professor
7	Mr. T. Venkatesan	Assistant Professor
8	Dr. M. Suresh	Assistant Professor
9	Dr. G. Ganapathy	Assistant Professor
10	Dr. A. Jyothi Bala	Assistant Professor
11	Mr. S. Sathishkumar	Assistant Professor
12	Dr. S. Celine Prabha	Assistant Professor
13	Dr. M. Suresh	Associate Professor
14	Dr. N. Prabakaran	Assistant Professor
	<b>PHYSICS</b>	
15	Dr. R. Priya	Professor
16	Dr. K. Amudha	Associate Professor



S. No	Name of the Faculty	Designation
17	Dr. S. Shalini	Assistant Professor
18	Dr. R. Ranjani	Assistant Professor
	<b>CHEMISTRY</b>	
19	Dr. K.S. Radha	Associate Professor
20	Dr. S. Rekha	Associate Professor
21	Dr. V. Subha	Assistant Professor
22	Dr. S. Ramesh	Assistant Professor
23	Dr. P. Kamalarajan	Assistant Professor
	<b>ENGLISH</b>	
24	Dr. G. Gayathiri Devi	Associate Professor
25	Dr. K. Krishna Veni	Assistant Professor
26	Ms. M.V. Vani	Assistant Professor
27	Ms. M.G. Meedphin Arasi	Assistant Professor
	<b>GENERAL ENGINEERING</b>	
28	Dr.V.Tamil Selvi / EEE	Professor & Head
29	Dr. T.A. Sundara Vadivel / Mech.	Assistant Professor
30	Mr. G. Arul Jothi / Mech.	Assistant Professor
31	Mr. M. Nandakumar / Mech.	Assistant Professor
32	Dr. N.Padmavathi / EIE	Associate Professor
33	Dr.U.Nagabalan / EEE	Assistant professor
34	Dr. R. Ashok Kumar / Mech.	Assistant professor

**Permanent Faculty: Student Ratio:** 1:13.4

## 8. Profile of Principal

<b>Name</b>	Dr. ANBU CHEZHIAN N
<b>Date of birth / Age</b>	08-10-1965 / 57
<b>Uniu e id</b>	1-21838 44008
<b>Father Name</b>	Mr. NATTAPPAN P
<b>Date of joining</b>	29-11-2017
<b>Experience (Academic)</b>	35 Years
<b>Telephone number - Office</b>	044 - 67919104
<b>Telephone number - Residence</b>	044 - 29815033
<b>Fax number</b>	044 - 67919190
<b>Mobile number</b>	9790670444
<b>E-mail</b>	principal@rmd.ac.in

<b>Residential Address Line 1</b>	FLAT NO. B 306, RMK CHOLA GARDENS, SUNDARACHOLAVARAM ROAD,		
<b>Line 2</b>	TIRUVERKADU, AYAPPAKKAM, 600077		
<b>District</b>	Tiruvallur		
<b>Educational Qualification</b>	<b>Degree</b>	<b>Specialization</b>	<b>Class</b>
	B.E.	Mechanical Engineering	First Class
	M.E.	Engineering Design	First Class
	Ph.D.	Faculty of Mechanical Engineering	Others-AWARDED
	M.B.A.	Master of Business Administration	First Class
<b>Title of the Ph.D. Thesis</b>	CONJOINTANALYSISFORPRODUCTDEVELOPMENTAPPLIED TO DOMESTIC SOLAR WATERHEATERS		

## 9.Fee

**Details of Fee, as approved by State Fee Committee, for the institution.**

Sl.No.	Degree	Name of the Programme	Fee in Rs. (As per Govt Norms) *		
			2022-2023	2021-2022	2020-2021
1.	B.E.	Computer Science and Engineering	Rs. 55,000/- (All Accredited Courses)	Rs. 55,000/- (All Accredited Courses)	Rs. 55,000/- (All Accredited Courses)
2.	B.E.	Electrical and Electronics Engineering			
3.	B.E.	Electronics and Communication Engineering			
4.	B.Tech.	Information Technology			
5.	B.Tech.	Computer Science and Business Systems	Rs.50,000/-	Rs.50,000/-	Rs.50,000/-
6.	B.Tech.	Artificial Intelligence and Machine Learning	Rs.50,000/-	Rs.50,000/-	NA

- Fees for Government Quota

## Proceedings of the Fee committee ( 06.09.2021)

### **PROCEEDINGS OF THE COMMITTEE ON FIXATION OF FEE IN RESPECT OF SELF FINANCING PROFESSIONAL COLLEGES.**

Proceedings No. CFF/ UG - Engineering /Fee /2021 Dated: 06.09.2021

**Hon'ble Mr. Justice K.VENKATARAMAN**  
Chairman

**Thiru. D. Karthikeyan, I.A.S.,**  
Principal Secretary to Government  
Higher Education Department  
Member-Secretary

**Thiru. A. Kirshnamoorthy,**  
Member (Chartered Accountant of repute)

**Shri. M. Sundresan,**  
Member (Representative of AICTE)

**Prof. Dr. Esther Anlin Kala James,**  
Member  
(Independent person of repute in the field of Education)

**Tmt. K. LaxmiPriya I.A.S.,**  
Director of Technical Education  
Special Invitee

#### **ORDER:**

R.M.K Engineering College, RSM Nagar, Kavaraipettai 601 206, Gummudipoondi Tk, Thiruvallur District has sought permission to collect the fee for the new courses namely B.Tech – Computer Science and Business Systems, B.Tech – Artificial Intelligence and Data Science and B.E – Computer Science and Design.

Likewise R.M.D Engineering College, RSM Nagar, Kavaraipettai 601 206, Gummudipoondi Tk, Thiruvallur District sought permission to fix the fees for the following courses B.Tech – Computer Science and Business Systems, B.Tech – Artificial Intelligence and Machine Learning.

Also R.M.K College of Engineering and Technology, RSM Nagar, Puduvoyal - 601 206 Gummudipoondi Tk, Thiruvallur District sought for fixation of fee for the following course B.Tech – Artificial Intelligence and Data Science.

The Committee earlier considered the request made by the institutions by an order dated 30-11-2020. The following maximum fee structure was fixed for the academic year 2020 -2021, by the said order

Sl. No.	Name of the Course		Fee fixed by the Committee Rs.	Remarks
1.	B.E.- B.Tech/B.Arch. (Category-I) (Govt. Quota)	Non-Accredited Courses	50,000/Annum	The fee is an all inclusive annual fee including various fees like Tuition fee, Admission fee, Special fee, Laboratory / Computer / Internet fee, Library fee, Sports fee, Maintenance and Amenities fee, Extracurricular activities fee and other recurring expenditure
		Accredited Courses	55,000/ Annum	
2.	B.E.- B.Tech/B.Arch. (Category-II) (Seats retained by the Colleges)	Non-Accredited Courses	1,40,000/Annum	
		Accredited Courses	1,45,000/Annum	

Considering the said facts and circumstances we are of the view the following maximum fee structure will take effect for the students admitted in the above referred course in the colleges mentioned above for the academic year 2021 -2022.

Sl. No.	Name of the Course		Fee fixed by the Committee Rs.	Remarks
1.	B.Tech -Computer Science and Business Systems, B.Tech - Artificial Intelligence and Data Science B.Tech - Artificial Intelligence and Machine Learning B.E - Computer Science and Design (Category-I) <b>(Govt. Quota)</b>	Non-Accredited Courses	<b>50,000/Annum</b>	The fee is an all inclusive annual fee including various fees like Tuition fee, Admission fee, Special fee, Laboratory / Computer / Internet fee, Library fee, Sports fee, Maintenance and Amenities fee,
		Accredited Courses	<b>55,000/ Annum</b>	
2.	B.Tech - Computer Science and Business Systems, B.Tech - Artificial Intelligence and Data Science B.Tech - Artificial Intelligence and Machine Learning B.E - Computer Science and Design (Category-II) <b>(Seats retained by the Colleges)</b>	Non-Accredited Courses	<b>1,40,000/Annum</b>	Extracurricular activities fee and other recurring expenditure
		Accredited Courses	<b>1,45,000/Annum</b>	

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## Proceedings of the Fee committee ( 30.11.2020)

### PROCEEDINGS OF THE COMMITTEE ON FIXATION OF FEE IN RESPECT OF SELF FINANCING PROFESSIONAL COLLEGES.

Proceedings No. CFF/ UG - Engineering /Fee /2020 Dated :30.11.2020

**Hon'ble Mr. Justice K.VENKATARAMAN**  
Chairman

**Selvi. Apoorva, I.A.S.,**  
Principal Secretary to Government  
Higher Education Department  
Member-Secretary

**Thiru. A. Kirshnamoorthy,**  
Member (Chartered Accountant of repute)

**Shri. M. Sundresan,**  
Member (Representative of AICTE)

**Prof. Dr. Esther Anlin Kala James,**  
Member  
(Independent person of repute in the field of Education)

**Thiru K. Vivekanadan, I.A.S.,**  
Commissioner of Technical Education  
Special Invitee

#### **ORDER:**

1. The Committee was constituted for fixation of fee in respect of Self-Financing Professional colleges under the Chairmanship of Hon'ble Mr. Justice K. VENKATARAMAN.
2. Most of the Engineering Colleges have submitted their proposal along with documents for revising / increasing the fees payable by the students.

- 3 -

#### **(3) Fee Reviewed and fixed during 2019-2020 for 24 Colleges**

**(a) Government Quota** Non Accredited Courses Rs.50,000/-  
Accredited Courses Rs.55,000/-

**(b) Management Quota** Non Accredited Courses Rs.1,20,000/-  
Accredited Courses Rs. 1,25,000/-

#### **(c) Apart from the Above Fee:-**

Rs.15,000/- Fixed for Placement and Training

Rs. 5,000/- Development Charges

8. Though based on the available materials, the fees has to be revised / increased, due to COVID -19 it has been felt by the Committee that the parents of the students will find it very difficult to pay the fees. However, reasonable increase of fees has to be made considering the facilities available in the Engineering Colleges and income and expenditure.
9. The following factors were considered by the Committee
  - i. Principal adopted by Sri Krishna Committee
  - ii. The expenditure details submitted by the colleges in proposals
  - iii. Request submitted by the representatives of the Colleges regarding Skill Development and Placement Training expenditure
  - iv. Seat sharing for admission of the students in Self Financing Engineering Colleges.
10. Keeping in mind all the above factors, which have been set above, the Committee is of the view, that the fee prescribed for the students admitted in the Government Quota need no change for the academic year 2020 -2021. However a sum of Rs. 20,000/- could be increased as tuition

## **Proceedings of the Fee committee ( 28.05.2019)**

### **PROCEEDINGS OF THE COMMITTEE ON FIXATION OF FEE IN RESPECT OF SELF FINANCING PROFESSIONAL COLLEGES**

**PROC. NO.CFF/ UG - Engineering/ Fees/ 029 / 2019, Dated:28.05.2019**

**Hon'ble Mr. Justice N.V. Balasubramanian  
Chairman**

**Thiru. Mangat Ram Sharma, IAS  
Principal Secretary to Government  
Higher Education Department  
Member Secretary**

**Thiru. A. Kanagaraj  
Member (Chartered Accountant of repute)**

**Shri. M. Sundresan  
Member (representative of AICTE)**

**Prof. Dr. Esther Anlin Kala James,  
Member  
(Independent person of repute in the field of Education)**

**Thiru. K. Vivekanadan, I.A.S.,  
Commissioner of Technical Education  
Special Invitee**

- Ref: 1) G.O. Ms. No.226, Higher Education (J2) Department, dated 11-7-2007  
2) Minute of the meeting held on dated.28.02.2019  
3) Letter No. CFF / Fee / Engineering / 029 / 2019, dated: 07.03.2019  
4) Minutes of the meeting held on dated.14.03.2019  
5) Minutes of the meeting held on dated: 20.05.2019

#### **ORDER:**

1. With reference to G.O. 1<sup>st</sup> cited, the Committee was constituted for fixation of fee in respect of Self-Financing Professional Colleges under the Chairmanship of Hon'ble Mr. Justice N.V. Balasubramanian.

#### **ORDER:**

12. The following maximum fee structure will take effect for the batch of the students admitted for the academic year 2019-2020 Only.

Sl. No	Name of the Course		Fee fixed by the Committee Rs.	Remarks
1	B.E./B.Tech./B.Arch. (Category-I)	Non-Accredited Courses	50,000 / Annum	The fee is an all inclusive annual fee including various fees like Tuition fee, Admission fee, Special fee, Laboratory / Computer / Internet fee, Library fee, Sports fee, Maintenance and Amenities fee, Extracurricular activities fee and other recurring expenditure.
		Accredited Courses	55,000 / Annum	
2	B.E./B.Tech./B.Arch. (Category-II)	Non-Accredited Courses	1,20,000 /Annum	
		Accredited Courses	1,25,000/ Annum	

13. In addition to the above fees, the institution is permitted to collect an amount of Rs.5,000/- per student as development fee

14. In addition to the above recurring annual fee, the concerned Institutions are permitted to collect a refundable one time caution deposit not exceeding Rs.5, 000/- per student at the time of admission, to be refunded at the time the student leaving the institution.

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### **Time schedule for the payment of Fee for the entire Programme**

At the starting of each academic year and the schedule is flexible on student request.

**No. of fee waivers granted with amount and name of the students: (2022-2023)**

SL.NO	YEAR/ BRANCH	REG.NO	NAME	QUOTA	AMOUNT
1	I -CSE	111522102056	JAYASHREE K	GOVT	27500
2	I -CSE	111522102123	PRIYADHARCINI K	GOVT	27500
3	I -CSE	111522102085	LOKESHWARAN E	GOVT	27500
4	I -CSE	111522102147	SREEDEVI R M	GOVT	27500
5	I -CSE	111522102154	SUNIL R S	GOVT	27500
6	I -CSE	111522102038	DURGAA DEVI M	GOVT	27500
7	I -CSE	111522102032	DILIP KUMAR P L	GOVT	27500
8	I -ECE	111522104152	TAMIL CHELVAN M	GOVT	27500
9	I -ECE	111522104166	VIMAL RAJ A	GOVT	27500
10	I -ECE	111522104055	HARIHARAN S	GOVT	27500
11	I -IT	111522203032	MOHANA SHRI V	GOVT	27500
12	I -IT	111522203029	MANJUSHREE M	GOVT	27500
13	I -IT	111522203055	SRINITHI P	GOVT	27500
14	I- AIML	111522204013	DHARSHINI K	GOVT	25000
<b>TOTAL</b>					<b>382500</b>

**No. of scholarships offered by the institution, duration and amount (2022-2023)**

SL. NO	YEAR/ BRANCH	REG.NO	NAME	QUOTA	TOTAL
1	IV CSE	111519104134	SHARAN S T	MGT	42200
2	IV EEE	111519105015	GUNADHARSHINI C	GOVT	45300
3	IV ECE	111519106007	ASWITHA K G	GOVT	35750
4	"	111519106033	ENIYAN T S	GOVT	20000
5	IV EIE	111519107018	PRINCE KELWIN LAZAR M	GOVT	45300

SL. NO	YEAR/ BRANCH	REG.NO	NAME	QUOTA	TOTAL
6	III CSE	111520104005	ABINAYA LAKSHMI B G	MGT	17000
7	"	111520104149	SMITI E	GOVT	28000
8	"	111520104162	TAGORE E	GOVT	42200
9	III EEE	111520105019	MANIKANDAN M	MGT	45300
10	III ECE	111520106041	HARIPRIYA N	MGT	25000
11	III CSBS	111520244031	PRIYAA THARSHINI PA	GOVT	17000
12	II CSE	111521102053	HAREESH KANNA R	GOVT	45300
13	II ECE	111521104028	DIKSHITHA R	MGT	17000
14	"	111521104115	RACHITHA R	MGT	17000
15	"	111521104122	RAJINI VYSHNAVI R	MGT	17000
16	"	111521104142	SIVAKUMAR P	GOVT	17000
17	"	111521104143	SNEHA D	MGT	42200
18	II CSBS	111521202024	JUDSON SMITH J	MGT	42200
19	II IT	111521203026	KEERTHIKA M N	MGT	42200
20	I CSE	111522102116	PON MADHUMITHA A	GOVT	20000
21	I ECE	111522104148	SWATHI J	GOVT	17000
22	"	111522104112	PRADEEP N M	GOVT	20000
23	"	111522104010	AKSHAYA LAKSHMI M M	MGT	125750
24	I IT	111522203050	SHERLIN S	GOVT	42200
				<b>TOTAL</b>	<b>827900</b>



**2021-22**

S.No	Reg.No	Year	Branch	Name	Quota	Total
1	111518105052	IV	EEE	PULIMI HANISHA	MGT	25000
2	111518106059	IV	ECE	KANMANI M K	GOVT	19050
3	111518106117	IV	ECE	RAGUL S	GOVT	11050
4	111518205011	IV	IT	ELANGO VAN	MGT	118050
5	111519104134	III	CSE	SHARAN S T	MGT	32560
6	111519105015	III	EEE	GUNADHARSHINI C	GOVT	40400
7	111519106033	III	ECE	ENIYAN T S	GOVT	20000
8	111520104005	II	CSE	ABINAYA LAKSHMI B G	MGT	27600
9	111520104092	II	CSE	MALLU DHANUSH KUMAR	MGT	75000
10	111520104149	II	CSE	SMITI E	GOVT	21600
11	111520105019	II	EEE	MANIKANDAN M	MGT	35040
12	111520106041	II	ECE	HARIPRIYA N	MGT	25000
13	111521102053	I	CSE	HAREESH KANNA R	GOVT	40700
14	111521104028	I	ECE	DIKSHITHA R	MGT	40700
15	111521104115	I	ECE	RACHITHA R	MGT	40700
16	111521104122	I	ECE	RAJINI VYSHNAVI R	MGT	17000
17	111521104142	I	ECE	SIVAKUMAR P	GOVT	40700
18	111521104143	I	ECE	SNEHA D	MGT	40700
19	111521202024	I	CSBS	JUDSON SMITH J	MGT	34500
20	111521202051	I	CSBS	G SHAKTHI PRIYA	MGT	20000
21	111521203026	I	IT	KEERTHIKA M N	MGT	43800
<b>TOTAL</b>						<b>769150</b>

**Criteria for Fee waivers / Scholarship**

- As per Government Norms

**Estimated cost of Boarding and Lodging in Hostel: Rs 95000/-**

## 10. Admission

### No. of Seats sanctioned with the year of approval (2022-23)

Course	N. of. Seats Sanctioned	Accreditation status	Period of accreditation	Letter No. and Date
B.E. Computer Science & Engineering	180	NBA Accredited	2020-2021to	F.No.33-243/2010-NBA  Dated 05.03.2020
B.E. Electronics & Communication Engineering	180		2022-2023	
B.Tech. Information Technology	60		i.e., upto 30.06.2023	
B.Tech. Computer Science and Business Systems	60	Not Eligible for NBA Accreditation		
B.Tech. Artificial Intelligence and Machine Learning	60	Not Eligible for NBA Accreditation		

### No. of Students admitted under various categories each year in the last three years.

Dept	Academic year 2022-23				Academic year 2021-22				Academic year 2020-21			
	Sanctioned	Admitted			Sanctioned	Admitted			Sanctioned	Admitted		
		CAT-I	CAT-II	Total		CAT-I	CAT-II	Total		CAT-I	CAT-II	Total
CSE	180	83	104	187	180	69	107	176	180	65	115	180
ECE	180	79	104	183	180	71	105	176	180	72	108	180
EEE	-	-	-	-	-	-	-	-	60	20	8	28
IT	60	29	34	63	60	27	32	59	60	23	37	60
CSBS*	60	20	40	60	60	25	33	58	60	21	34	55
AIML**	60	28	33	61	60	25	33	58	-	-	-	-
TOTAL	540	239	315	554	540	217	310	527	540	201	302	503

\* CSBS started from 2020-21

\*\* AIML started from 2021-22

**No. of application received during last two years for admission under Management Quota and number admitted**

Particular	Academic Year 2021-22		Academic Year 2020-21	
	Management	Government	Management	Government
<b>No. of Application received</b>	322	217	315	201
<b>No. of students admitted</b>	310	217	302	201

## 11. Admission Procedure

Admissions are made as per State government norms through Single Window Counselling and Consortium of Self-financing Engineering Colleges as per the norms of Tamil Nadu Government.

**Entrance test / admission criteria** - As per Tamil Nadu Government Norms

### SELECTION OF STUDENTS

Admission to the Courses offered by the College is done under 2 categories:

- 1) Government Quota seats and
- 2) Management Quota seats.

#### **Government Quota Admission**

Seat sharing between these two categories is done on the basis of the existing Government norms for the purpose of Admission of the students into Engineering and Technology courses in the Self-Financing Colleges. R.M.D. Engineering College being the Linguistic Minority Institution is allowed to fill 50% of the total seats, branch wise, under the Management Quota and the other 50% of seats are surrendered to be filled under the Government Quota seats.

The Government Quota seats admission process is conducted and overseen by the Tamil Nadu Engineering Admissions (TNEA) through the Single Window Counseling system. Every year, the Tamil Nadu Engineering Admissions (TNEA) releases its Prospectus for public notice and calls for admission of students into B.E./B.TECH Courses under Government Quota seats. The admissions are done directly by the TNEA, and the selected candidates report to the College along with the Allotment Order issued by the TNEA for admission and based on the Allotment Order the College admits the students under Government Quota. The entire procedure followed by the TNEA including the Reservation Policy, Qualification and Eligibility Policy and the selection procedure is published in the Prospectus. TNEA prepares the Merit list of the students based on the marks obtained in the qualifying (PLUS 2) examinations for a cut off 200 and based on the cut off and choice of the students grants admission. The excerpts of the relevant pages from the TNEA Prospectus are attached herewith for ready reference.

A sample Allotment order of a candidate issued by the TNEA 2019 is attached herewith for your ready reference. The College being the most sought-after Engineering Institutions in the State, the students opting this College through Single Window Counseling of the TNEA are generally from the higher cut off Marks. The higher cut off marks, community wise, of the students admitted under Government Quota is attached which stand testimony to the Quality and merit of the students.

### **Management Quota Admission**

Admission of the students into Engineering and Technology courses in the Self-Financing Colleges under the Management Quota seats are governed by the Committee constituted by the Government of Tamil Nadu. The Committee known as “COMMITTEE TO REGULATE-MONITOR THE ADMISSIONS OF STUDENTS TO PROFESSIONAL COURSES BY SELF FINANCING PROFESSIONAL, ARTS AND SCIENCE COLLEGES” that regulates and monitors the admission of students made under:

- 1) Seats retained by the Institutions and the details of the students admitted under Management Quota Seats.
- 2) Details of students admitted under Government Quota lapsed seats.

R.M.D. Engineering College is a constituent member of the Consortium of Self-Financing Professional, Arts, and Science Colleges in Tamil Nadu. The consortium based on the orders and authorization issued by the “COMMITTEE TO REGULATE-MONITOR THE ADMISSIONS OF STUDENTS TO PROFESSIONAL COURSES BY SELF FINANCING PROFESSIONAL, ARTS AND SCIENCE COLLEGES” calls for applications from eligible candidates for admissions to Self-Financing Engineering Colleges and ranks the candidates based on the qualifying marks as in the TNEA Prospectus and issues allotment order based on the candidates’ choice of Institute.

Based on the allotment order and the consortium rank card, the college admits the candidates under Management Quota and submit the list of such admitted candidates to the COMMITTEE for scrutiny and approval.

Admissions to the PG course (M.E. Computer Science and Engineering) is done based on the scores obtained in the Tamil Nadu Common Entrance Test (TANCET) conducted by the Anna University Chennai / Marks scored in the Common Entrance Test conducted by the Consortium of Self-Financing Professional, Arts, and Science Colleges in Tamil Nadu and ranked in the order of merit.

Finally, the Directorate of Technical Education, Government of Tamil Nadu scrutinizes the list of students admitted; verifies the documents of qualification & Eligibility; reservation criteria and other relevant original documents. And subsequently the Directorate of Technical Education, Government of Tamil Nadu issues Admission Approval order, with the list of students, to the Colleges.

The College being the most sought-after Engineering Institutions in the State, the students Ranked and selected under Consortium Ranking are generally from the higher cut off Marks. The Ranks of the students admitted under Management Quota is attached which stand testimony to the Quality and merit of the students.

## **12.Criteria and Weightage for Admission**

- As per Government Norms

### 13.List of Applicants

After the admission process, the Directorate of Technical Education, Government of Tamil Nadu scrutinizes the list of students admitted; verifies the documents of qualification & Eligibility; reservation criteria and other relevant original documents.

And subsequently the Directorate of Technical Education, Government of Tamil Nadu issues Admission Approval order, with the list of students, to the Colleges. The list of approved students of 2021-2022 is attached. (2022-2023 approval awaited)

### 14.Results of Admission under Management Quota/ Vacant seats

Admission of the students into Engineering and Technology courses in the Self-Financing Colleges under the Management Quota seats are governed by the Committee constituted by the Government of Tamil Nadu. The Committee known as "COMMITTEE TO REGULATE-MONITOR THE ADMISSIONS OF STUDENTS TO PROFESSIONAL COURSES BY SELF FINANCING PROFESSIONAL, ARTS AND SCIENCE COLLEGES" that regulates and monitors the admission of students made under:

- 1) Seats retained by the Institutions and the details of the students admitted under Management Quota Seats.
- 2) Details of students admitted under Government Quota lapsed seats.

### 15.Information of Infrastructure and other Resources available

#### Number of Classrooms and size of each.

Sl.No.	Name of the Block	Area (Length *Width) in sq.m.	Number of Rooms	Type of roof	Capacit y
1	BIO TECHNICAL BLOCK	66	31	Permanent	1860
2	EEE BLOCK	66	10	Permanent	600
3	NM BLOCK	66	12	Permanent	720
4	ECE BLOCK	66	10	Permanent	600
5	MAIN BLOCK	66	10	Permanent	600

#### Number of Tutorial Rooms and size.

- 16 Rooms each with size of 33 sq.mtr

## Number of Laboratories and size of each

SL.No	Degree & Course	Laboratory/Workshop/Studio	Name of the Laboratory	Area of the Laboratory required(sq.m.)	Area of the Laboratory available(sq.m.)	Deficiency %
1	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	digital principles and system design laboratory	66	66	0
2	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS301 DIGITAL PRINCIPLES AND SYSTEM DESIGN LABINTEGRATED	66	66	0
3	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	PYTHON PROGRAMMING INTEGRATED LAB	66	66	0
4	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	DATA STRUCTURES LABORATORY	66	66	0
5	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22cs101 problem solving using c plusplus	66	66	0
6	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	CHEMISTRY LABORATORY	66	66	0
7	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	C PROGRAMMING LABORATORY	66	66	0
8	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	PHYSICS FOR COMPUTER SCIENCEAND INFORMATION TECHNOLOGY	66	66	0
9	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	DATABASE MANAGEMEN TSYSTEM LABORATOR Y	66	66	0
10	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21IT412 DATABASE MANAGEMENT SYSTEMS LABORATORY	66	66	0
11	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS311 OBJECT ORIENTED PROGRAMMIN G LABORATORY	66	66	0
12	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21EC441 MICROPROCESSO RS AND INTERFACING LAB INTEGRATED	66	66	0
13	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS412 OPERATING SYSTEMS LABORATORY	66	66	0

14	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS411 INTERNET PROGRAMMING LAB	66	66	0
15	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS511 NETWORKS LABORATORY	66	66	0
16	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS512 ARTIFICIAL INTELLIGENCE LABORATORY	66	66	0
17	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS502 OBJECT ORIENTED ANALYSIS AND DESIGN LAB INTEGRATED	66	66	0
18	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS601 COMPILER DESIGN LAB INTEGRATED	66	66	0
19	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS611 MOBILE APPLICATION DEVELOPMENT LABORATORY	66	66	0
20	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS612 SECURITY LABORATORY	66	66	0
21	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	IT8761 SECURITY LABORATORY	66	66	0
22	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	CS8711 CLOUD COMPUTING LABORATORY	66	66	0
23	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
24	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATION	66	66	0
25	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22cs201 data structures	66	66	0
26	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
27	B.E.-Electronics and Communication	Engg. & Tech. Laboratory	21EC412 LINEAR INTEGRATED CIRCUITS LABORATORY	66	66	0

	Engineering					
28	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	digital principles and system design laboratory	66	66	0
29	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATION	66	66	0
30	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	PHYSICS FOR ELECTRONICS ENGINEERING	66	66	0
31	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
32	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
33	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	Data structures laboratory	66	66	0
34	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8561 COMMUNICATION SYSTEMS LABORATORY	66	66	0
35	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21CS414 APTITUDE AND CODING SKILLS II	66	66	0
36	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC201	66	66	0
37	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC411 MICROPROCESSOR AND MICROCONTROLLER LABORATORY	66	66	0
38	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21CS313 APTITUDE AND CODING SKILLS I	66	66	0
39	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC312 FOUNDATION LAB ON IOT	66	66	0
40	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC311 ANALOG AND DIGITAL CIRCUITS LABORATORY	66	66	0



41	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22cs201 data structures	66	66	0
42	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22cs101 problem solving using cplusplus	66	66	0
43	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21ec311 analog and digital circuits laboratory	66	66	0
44	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	ec8761 ADVANCED COMMUNICATION LABORATORY	66	66	0
45	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8711 EMBEDDED SYSTEMS LABORATORY	66	66	0
46	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC312 FOUNDATION LAB ON IOT	66	66	0
47	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21CS313 APTITUDE AND CODING SKILLS I	66	66	0
48	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC411 MICROPROCESSOR AND MICROCONTROLLERS LABORATORY	66	66	0
49	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC412 LINEAR INTEGRATED CIRCUITS LABORATORY	66	66	0
50	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC413 MINIPROJECT AND INDUSTRIAL INTERNSHIP	66	66	0
51	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC511 COMMUNICATION NETWORKS AND DSPLABORATORY	66	66	0
52	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC512 COMMUNICATION SYSTEMS LABORATORY	66	66	0
53	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC513 FOUNDATION LAB ON MACHINE LEARNING	66	66	0
54	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC611 EMBEDDED SYSTEMS AND RTOS LABORATORY	66	66	0

55	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC612 VLSI DESIGN LABORATORY	66	66	0
56	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21CS512 ADVANCED APTITUDE CODING SKILLS I	66	66	0
57	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21CS614 ADVANCED APTITUDE AND CODING SKILLS II	66	66	0
58	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC613 INNOVATIVE AND MULTIDISCIPLINARY PROJECT	66	66	0
59	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	C PROGRAMMING LABORATORY	66	66	0
60	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8711 EMBEDDED LABORATORY	66	66	0
61	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8761 ADVANCED COMMUNICATION LABORATORY	66	66	0
62	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8661 VLSI DESIGN LABORATORY	66	66	0
63	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	CHEMISTRY LABORATORY	66	66	0
64	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8681 MICROPROCESSOR AND MICROCONTROLLER LABORATORY	66	66	0
65	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8563 COMMUNICATION NETWORKS LABORATORY	66	66	0
66	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8562 DIGITAL SIGNAL PROCESSING LABORATORY	66	66	0
67	B.E.-General Engineering	Engg. & Tech. Laboratory	physics laboratory	66	66	0
	B.E.-General		COMPUTER AIDED ENGINEERING			

68	Engineering	Engg. & Tech. Laboratory	GRAPHICS	66	66	0
69	B.E.- General Engineering	Engg. & Tech. Laboratory	data structures laboratory	66	66	0
70	B.E.- General Engineering	Engg. & Tech. Laboratory	C Programming laboratory	66	66	0
71	B.E.- General Engineering	Engg. & Tech. Workshop	PRODUCT DEVELOPMENT LAB 1	200	200	0
72	B.E.- General Engineering	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
73	B.E.- General Engineering	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
74	B.E.- General Engineering	Engg. & Tech. Laboratory	physics laboratory	66	66	0
75	B.E.- General Engineering	Engg. & Tech. Workshop	PRODUCT DEVELOPMENT LAB 2	200	200	0

76	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21IT501 WEB TECHNOLOGY FRAMEWORK	66	66	0
77	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21EC441 MICROPROCESS OR AND INTERFACING	66	66	0
78	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21CS511 NETWORKS LABORATORY	66	66	0
79	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21IT512 BIG DATA ANALYTICS LABORATORY	66	66	0
80	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21IT602 ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING	66	66	0
81	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21IT603 CYBER SECURITY	66	66	0
82	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21CS711 CLOUD COMPUTING LABORATORY	66	66	0

83	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21IT511 OBJECT ORIENTED SYSTEM DESIGN LABORATORY	66	66	0
84	B.Tech.- Information Technology	Engg. & Tech. Laboratory	CHEMISTRY LABORATORY	66	66	0
85	B.Tech.- Information Technology	Engg. & Tech. Laboratory	PHYSICS FOR COMPUTER SCIENCEAND INFORMATION TECHNOLOGY	66	66	0
86	B.Tech.- Information Technology	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATION	66	66	0
87	B.Tech.- Information Technology	Engg. & Tech. Laboratory	digital principles and system design laboratory	66	66	0
88	B.Tech.- Information Technology	Engg. & Tech. Laboratory	22cs101 problem solving using c plusplus	66	66	0
89	B.Tech.- Information Technology	Engg. & Tech. Laboratory	22cs201 data structures	66	66	0
90	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21GE111 C PROGRAMMIN G LABORATORY	66	66	0
91	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21CS211 DATA STRUCTURE S LABORATOR Y	66	66	0
92	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21CS202 PYTHON PROGRAMMING LABORATORY INTEGRATED COURSE	66	66	0
93	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21IT311 OBJECT ORIENTED PROGRAMMIN GPRINCIPLES LABORATORY	66	66	0
94	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21CS412 OPERATING SYSTEMS LABORATORY	66	66	0
95	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21CS301 DIGITAL PRINCIPLES ANDSYSTEM DESIGN LABORATORY INTEGRATED COURSE	66	66	0

96	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21IT401 SOFTWARE ENGINEERING LABORATORY INTEGRATED COURSE	66	66	0
97	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21IT411 WEB TECHNOLOGY LABORATORY	66	66	0
98	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21IT412 DATABASE MANAGEMENT SYSTEMS LABORATORY	66	66	0
99	B.Tech.- Information Technology	Engg. & Tech. Laboratory	IT8761 SECURITY LABORATORY	66	66	0
100	B.Tech.- Information Technology	Engg. & Tech. Laboratory	IT8711 FOSS AND CLOUD COMPUTING LABORATORY	66	66	0
101	B.Tech.- Information Technology	Engg. & Tech. Laboratory	21CS611 MOBILE APPLICATION DEVELOPMENT LABORATORY	66	66	0
102	B.Tech.- Information Technology	Engg. & Tech. Laboratory	C PROGRAMMING LABORATORY	66	66	0
103	B.Tech.- Information Technology	Engg. & Tech. Laboratory	DATA STRUCTURES LABORATORY	66	66	0
104	B.Tech.- Information Technology	Engg. & Tech. Laboratory	PYTHON PROGRAMMING INTEGRATED LAB	66	66	0
105	B.Tech.- Information Technology	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
106	B.Tech.- Information Technology	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
107	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB303 OBJECT ORIENTED PROGRAMMING LAB INTEGRATED	66	66	0
108	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22EC202 PRINCIPLES OF ELECTRONICS ENGINEERING LABORATORY INTEGRATED COURSE	66	66	0

109	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21EC241 PRINCIPLES OF ELECTRONICS ENGINEERING LAB INTEGRATED	66	66	0
110	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB201 DATA STRUCTURES AND ALGORITHMS LAB INTEGRATED	66	66	0
111	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	fundamentals of physics lab	66	66	0
112	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8411 COMPUTATIONAL STATISTICS LABORATORY	66	66	0
113	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CS8461 OPERATING SYSTEMS LABORATORY	66	66	0

114	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY	66	66	0
115	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	66	66	0
116	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	AD8261 DATA STRUCTURE DESIGN LABORATORY	66	66	0
117	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8512 SOFT SKILLS LABORATORY	66	66	0
118	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	COMPUTATIONAL STATISTICS LABORATORY	66	66	0
119	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8511 MINI PROJECT SOFTWARE SYSTEM DESIGN ARCHITECTURE END TO END	66	66	0
120	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	BUSINESS COMMUNICATION AND VALUE SCIENCES ii	66	66	0
121	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	Fundamentals of computer science integrated laboratory	66	66	0

122	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB903 MACHINE LEARNING LAB INTEGRATED	66	66	0
123	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB601 COMPUTER NETWORKS LAB INTEGRATED	66	66	0
124	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB602 INFORMATION SECURITY LAB INTEGRATED	66	66	0
125	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB603 ARTIFICIAL INTELLIGENCE LAB INTEGRATED	66	66	0
126	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB908 MODERN WEB APPLICATIONS LAB INTEGRATED	66	66	0
127	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB909 DATA MINING AND ANALYTICS LAB INTEGRATED	66	66	0
128	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB902 CLOUD MICRO SERVICES AND APPLICATION LAB INTEGRATED	66	66	0
129	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	FUNDAMENTALS OF PHYSICS	66	66	0
130	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB502 COMPILER DESIGN LAB INTEGRATED	66	66	0
131	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB405 OPERATIONAL RESEARCH LAB INTEGRATED	66	66	0

132	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB403 SOFTWARE DESIGN WITH UML LAB INTEGRATED	66	66	0
133	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB402 DATABASE MANAGEMENT SYSTEMS LAB INTEGRATED	66	66	0
134	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	DATA STRUCTURES AND ALGORITHMS INTEGRATED LABORATORY	66	66	0

135	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATION	66	66	0
136	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB401 OPERATING SYSTEMS LAB UNIX INTEGRATED	66	66	0
137	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB304 SOFTWARE ENGINEERING LAB INTEGRATED	66	66	0
138	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21MA304 COMPUTATION ALSTATISTICS LAB INTEGRATED	66	66	0
139	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cs101 problem solving using c plusplus	66	66	0
140	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21MA203 STATISTICAL METHODS LAB INTEGRATED	66	66	0
141	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8811 PROJECTWORK	66	66	0
142	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cb201 data structures and algorithms	66	66	0
143	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8712 MINI PROJECT	66	66	0
144	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	OPERATING SYSTEMSLAB	66	66	0
145	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	BUSINESS COMMUNICATION AND VALUE SCIENCES I	66	66	0
146	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	IT8511 WEB TECHNOLOGY LABORATORY	66	66	0
147	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8711 INFORMATIO NSECURITY LABORATOR Y	66	66	0
148	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8612 ARTIFICIAL INTELLIGENCE LABORATORY	66	66	0
149	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8611 BUSINESS ANALYTICS LABORATORY DESIGN ARCHITECTURE	66	66	0



			END TO END			
150	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22cs101 problem solving using c plusplus	66	66	0

151	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21am412 data analytics laboratory	66	66	0
152	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21am411 machine learning essentials laboratory	66	66	0
153	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	CHEMISTRY LABORATORY	66	66	0
154	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	PYTHON PROGRAMMING INTEGRATED LAB	66	66	0
155	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22cs201 data structures	66	66	0
156	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21IT411 DATABASE MANAGEMENT SYSTEMS LABORATORY	66	66	0
157	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	digital principles and system design laboratory	66	66	0
158	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATION	66	66	0
159	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	C programming laboratory	66	66	0
160	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
161	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21am404 operating systems fundamentals lab integrated	66	66	0
162	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
	B.Tech.-Artificial Intelligence and		21am302 principles of artificial intelligence			

163	Machine Learning	Engg. & Tech. Laboratory	lab integrated	66	66	0
164	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21AM601 PRINCIPLES AND PRACTICES IN DEEP LEARNING LAB INTEGRATED	66	66	0
165	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	MINI PROJECT AND DESIGN THINKING PRACTICES LABORATORY	66	66	0
166	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21AM511 NEURAL NETWORKS LABORATORY	66	66	0
167	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	PHYSICS FOR COMPUTER SCIENCE AND INFORMATION TECHNOLOGY	66	66	0
168	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	DATA STRUCTURES LABORATORY	66	66	0
169	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21AM502 DATA VISUALIZATION LAB INTEGRATED	66	66	0

170	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21CS311 OBJECT ORIENTED PROGRAMMING LABORATORY	66	66	0
171	B.Tech.-General Engineering	Engg. & Tech. Workshop	PRODUCT DEVELOPMENT LAB 2	200	200	0
172	B.Tech.-General Engineering	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
173	B.Tech.-General Engineering	Engg. & Tech. Workshop	PRODUCT DEVELOPMENT LAB 1	200	200	0
174	B.Tech.-General Engineering	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
175	B.Tech.-General Engineering	Engg. & Tech. Laboratory	COMPUTER AIDED ENGINEERING LABORATORY	66	66	0
176	B.Tech.-General Engineering	Engg. & Tech. Laboratory	physics laboratory	66	66	0

**Number of Drawing Halls with capacity of each.**

Degree	Number Available	Area of each drawing hall required (sq.m.)	Area of the drawing hall available (sq.m.)
B.E.	03	264	396

**Number of Computer Centre with capacity of each.**

Sl.No	Name of the Computer Centre	No. of Systems
1	BAY-1	66
2	BAY-2	66
3	BAY-3	66
4	BAY-4	66
5	BAY-5	66
6	BAY-6	66
7	BAY-7	30
8	BAY-8	66
9	BAY-9	70
10	BAY-10	78
<b>TOTAL</b>		<b>640</b>

**Central Examination Facility, Number of rooms and Capacity of each**

40 (Each 66 Sqmt) Examination halls are available to accommodate 1000 students at a time at the rate of 25 students per hall other than the 65 labs for the practical Examinations

**Barrier Free Built Environment for disabled and elderly persons**

- YES

**Occupancy Certificate**

- YES, Form-D License

## Fire and Safety Certificate

### FIRE LICENCE

Under Section 13 of the Tamil Nadu Fire Service Act No.40 - 1985 and With Tamil Nadu Fire Service Rules 1990-Appendix-111

Licence No.3941/B/2022

Date : 27-05-2022

Licence is hereby granted under section 13 of the Tamil Nadu Fire Service act 1985 for **To Run Educational Institution** (Mention whichever is applicable) Within the Jurisdiction of **Kavaraipettai Municipality / Panchayat / P.Union Township** at the Name of Company **M/s. R.M.D. Engineering College, R.S.M. Nagar, Kavaraipettai, Gummidipoondi Taluk, Thiruvallur District** Subject to the conditions noted thereon and such other conditions as may be prescribed (Inspected by District Officer, Thiruvallur on 26-05-2022)

#### CONDITIONS

As per Tamil Nadu Fire Service Act 1985 Section 13 of Chapter II and appendix V of this Act

1. This Licence is Valid for ONE YEAR from the date of Issue.
2. Regular Licence has to be obtained from the competent authority.
3. Fire & Life Safety Systems / Arrangements have to be provided in accordance with the requirements of NBC Part IV Fire & Life Safety 2016.
4. The First Aid Fire Fighting equipments should be maintained at all floors in accordance with the IS 2190:2010 requirements.
5. If there is any deviation from the Govt. Rules and Act the Licence issue will be stand cancelled.
6. Staffs should be trained in preliminary fire fighting as per G.O.No.713, Home(Police-17) Dated.17-08-2005 with Fire and Rescue Services Department.
7. Fire Drill should be conducted once in a six month with the local Fire & Rescue Service authorities and a register should be maintained in part-II.

(Office Seal with Date)



District Officer  
Fire & Rescue Services,  
Thiruvallur District,  
Thiruvallur.

To. M/s. R.M.D. Engineering College,  
R.S.M. Nagar, Kavaraipettai,  
Gummidipoondi Taluk,  
Thiruvallur District

## Hostel Facilities

Boys hostel details: (As per the academic Year 2022-23)

Block Name	Total Admitted Strength	Carpet area of room (sq.m.)	Number of rooms Available	Room capacity Available
BOYS HOSTEL I	366	9	366	3294
BOYS HOSTEL II	159	20	55	1100

**Girls hostel details:( As per the academic Year 2022-2023)**

<b>Block Name</b>	<b>Total Admitted Strength</b>	<b>Carpet area of room (sq.m.)</b>	<b>Number of rooms Available</b>	<b>Room capacity Available</b>
GIRLS HOSTEL I	104	20	50	1000
GIRLS HOSTEL 2	245	09	255	2295

**Library**

The college central library is in the new block with a carpet area of 1430 sq.mt. It contains 61278 volumes of books and 14596 titles. The Library subscribes to 36 national Journals, 6 leading international E-journals and E-books database, 40 Magazines, 16 News Papers, 1828 back volumes of journals and 6649 CD ROMs. Books are classified and arranged according to the Universal Decimal classification scheme. The value of books, periodicals and other materials in the library is around Rs.2.5 Crore

**Number of Library books/Titles/ Journals available (program wise)**

<b>S.No.</b>	<b>Course(s)</b>	<b>Number of Titles</b>	<b>Number of Volumes</b>	<b>Journals</b>
				<b>National</b>
01	Science and Humanities	3096	11576	06
02	Computer Science and Engineering	3240	13572	06
03	Electronics and Communication Engineering	2190	10423	06
04	Information Technology	2483	10943	06
05	Computer Science and Business Systems	177	517	06
06	Artificial Intelligence and Machine Learning	229	526	06

## E-LIBRARY FACILITY

### List of online National / International E-Journal subscribed

- IETE-Journals
- IEI-Journals
- Delnet (Developing Library Network)

PUBLISHERS	DEPARTMENT						
	CSE	IT	ECE	CSBS	AIML	S/H	TOTAL
DELNET - Journals	86	69	31	28	37	15	266
IEI-Journals	01	-	02	-	01	01	05
IETE-Journals	01	01	-	-	-	01	03
<b>TOTAL</b>	<b>88</b>	<b>70</b>	<b>33</b>	<b>28</b>	<b>28</b>	<b>17</b>	<b>274</b>

### 1.IETE LIFE MEMBERSHIP AND E-JOURNALS ACCESS(Subscribed since 2014 to 2023)

3 e-journals are available for access

The screenshot shows a web browser window with the URL <https://www.tandfonline.com/page/iete-journals>. The page header includes the Taylor & Francis Online logo and navigation links for Log in, Register, and Cart. The main heading is "Welcome, IETE members". Below this, a paragraph states: "Online access to the following journals is now available on Taylor & Francis Online for members and affiliates who have paid annual dues." Another paragraph provides instructions: "Please note that in order to have the full access to these journals that you are entitled to, you will first need to login at <http://www.iete.org/> and follow the link here. If you bookmark this page and attempt to access the content directly, 'Get Access' icons will appear by the journal articles instead of 'Full Access' icons and you will not have access." At the bottom, there is a cookie consent banner that says: "We use cookies to improve your website experience. To learn about our use of cookies and how you can manage your cookie settings, please see our Cookie Policy. By closing this message, you are consenting to our use of cookies." with an "Accept" button. The Windows taskbar at the bottom shows the Start button and several application icons, with the system clock displaying 11:15 on 04/03/2020.

### 2.INSTITUTION OF ENGINEER LIFE MEMBERSHIP AND E-JOURNALS (Subscribed since 2014 to

2023)

5 e-journals are available for access

The screenshot shows the IeI Dashboard for R.M.D ENGINEERING COLLEGE. The dashboard is divided into several sections:

- MAIN:** Includes links to Dashboard, Inbox, and ACCOUNT SETTINGS.
- IEI Publication:** Lists five publications: IEI-Springer Journal Series-A, IEI-Springer Journal Series-B, IEI-Springer Journal Series-C, IEI-Springer Journal Series-D, and IEI Epitome Monthly e-News Letter.
- ACTIVITY:** Lists various seminars and events, including ALL INDIA SEMINAR (D-1), ALL INDIA SEMINAR (D-2), and ALL INDIA SEMINAR (D-3).

### 3.DELNET E-JOURNALS ARE AVAILABLE FOR ACCESS

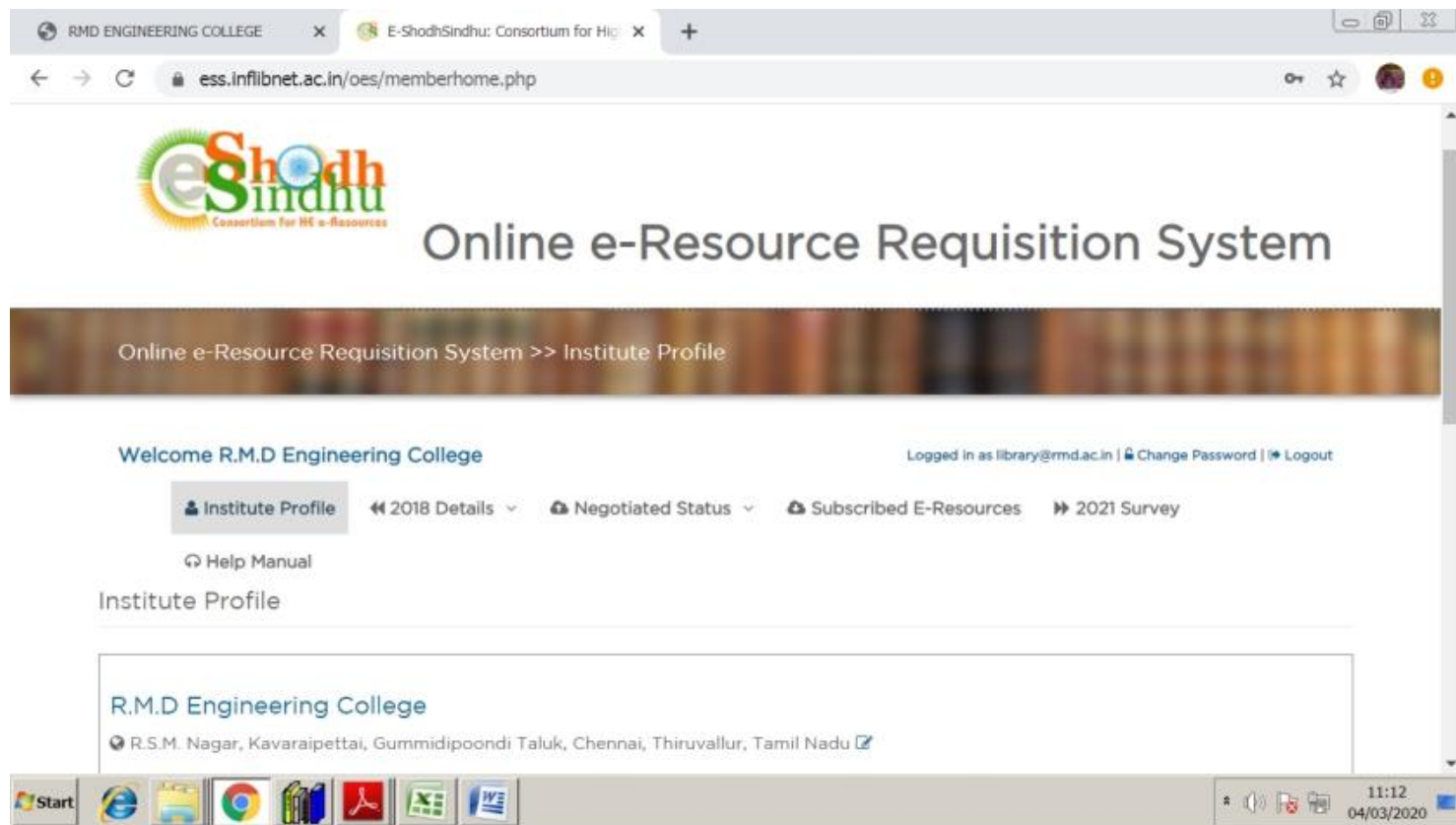
The screenshot shows the DELNET E-Journals page. The page features a banner for "NEW YEAR 2023" and a section titled "Access E-Journals". Below this section is a table listing various journals and their counts.

Engineering & Technology (860)	
<a href="#">Automobile Engineering (15)</a>	<a href="#">Chemical Engineering &amp; Technology (46)</a>
<a href="#">Computer Science (160)</a>	<a href="#">Construction &amp; Infrastructure (79)</a>
<a href="#">Electrical and Nuclear Engineering (70)</a>	<a href="#">Electronics &amp; Communication Engineering (41)</a>
<a href="#">General &amp; Civil Engineering (115)</a>	<a href="#">Hydraulic Engineering (44)</a>
<a href="#">Industrial Engineering (46)</a>	<a href="#">Manufacturing (25)</a>
<a href="#">Materials (36)</a>	<a href="#">Mechanical Engineering (40)</a>
<a href="#">Military Sciences (23)</a>	<a href="#">Mining and Metallurgy (20)</a>
<a href="#">Technology (General) (65)</a>	<a href="#">Transportation (35)</a>

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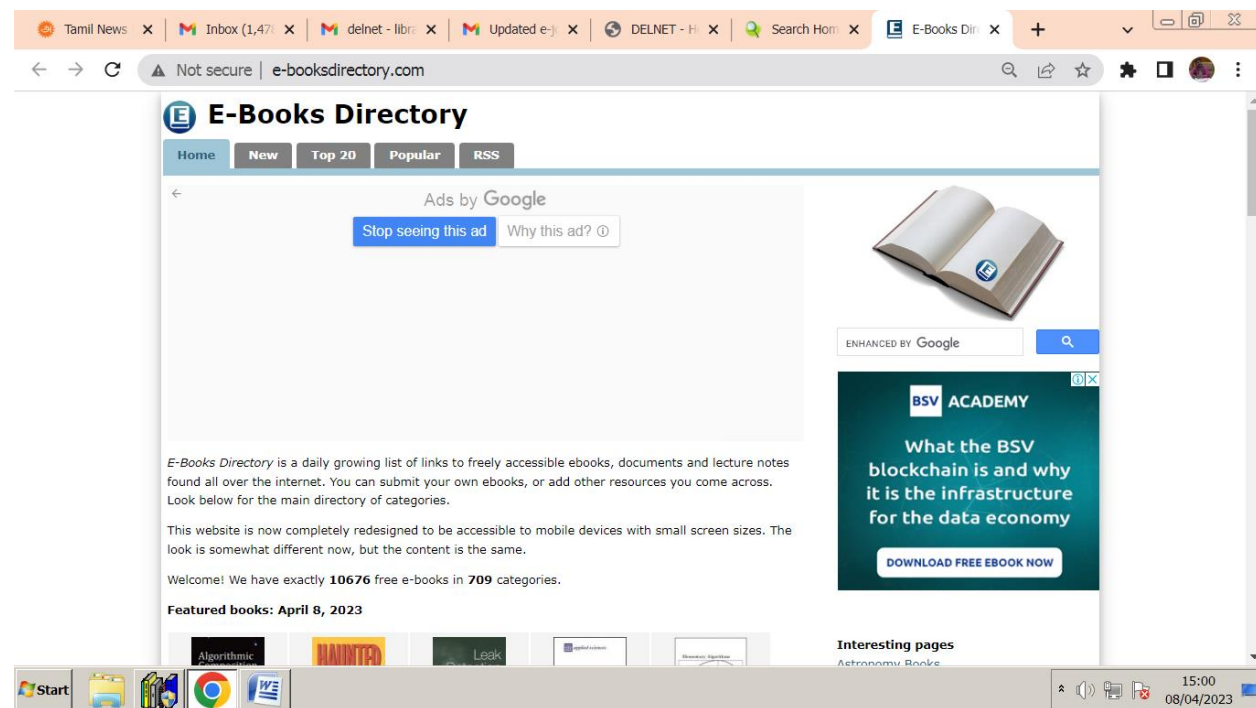
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#### 4.e-shodhsindhu(Membership e-resource Requisition)(Since 2019)



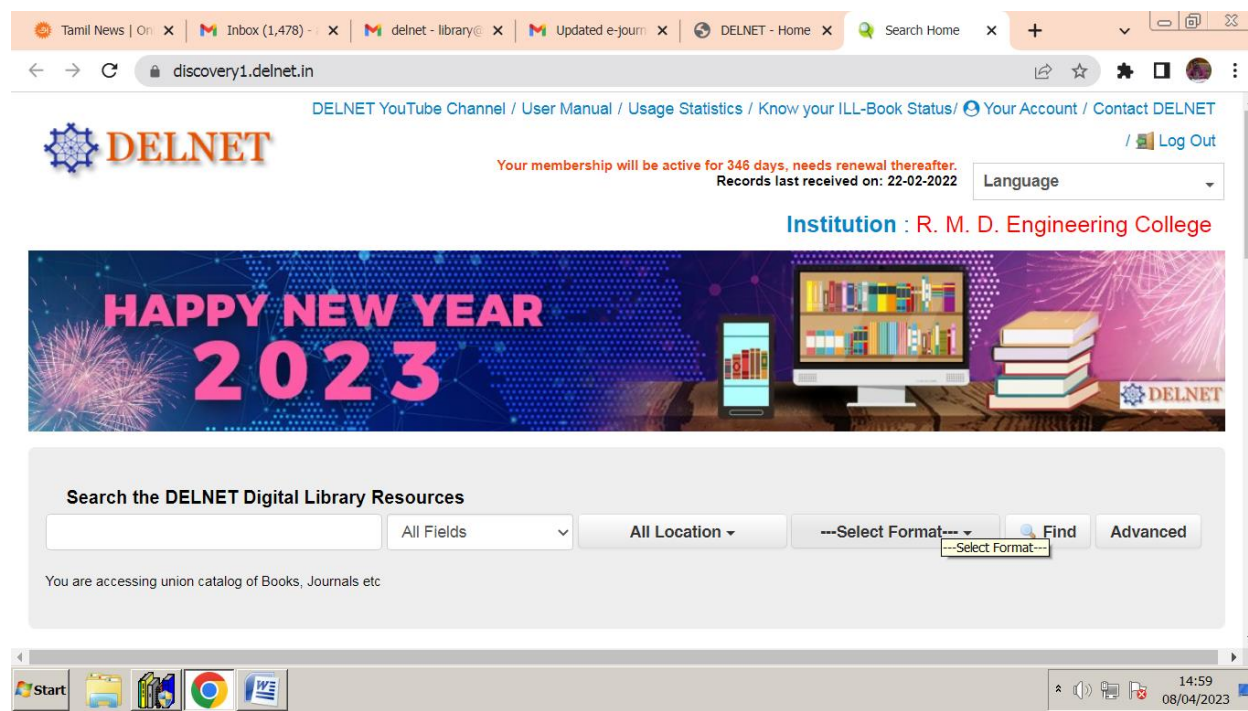
#### 5.E - BOOKS(Subscribed since 2014 to 2023)

10676 e-books are available for accessing

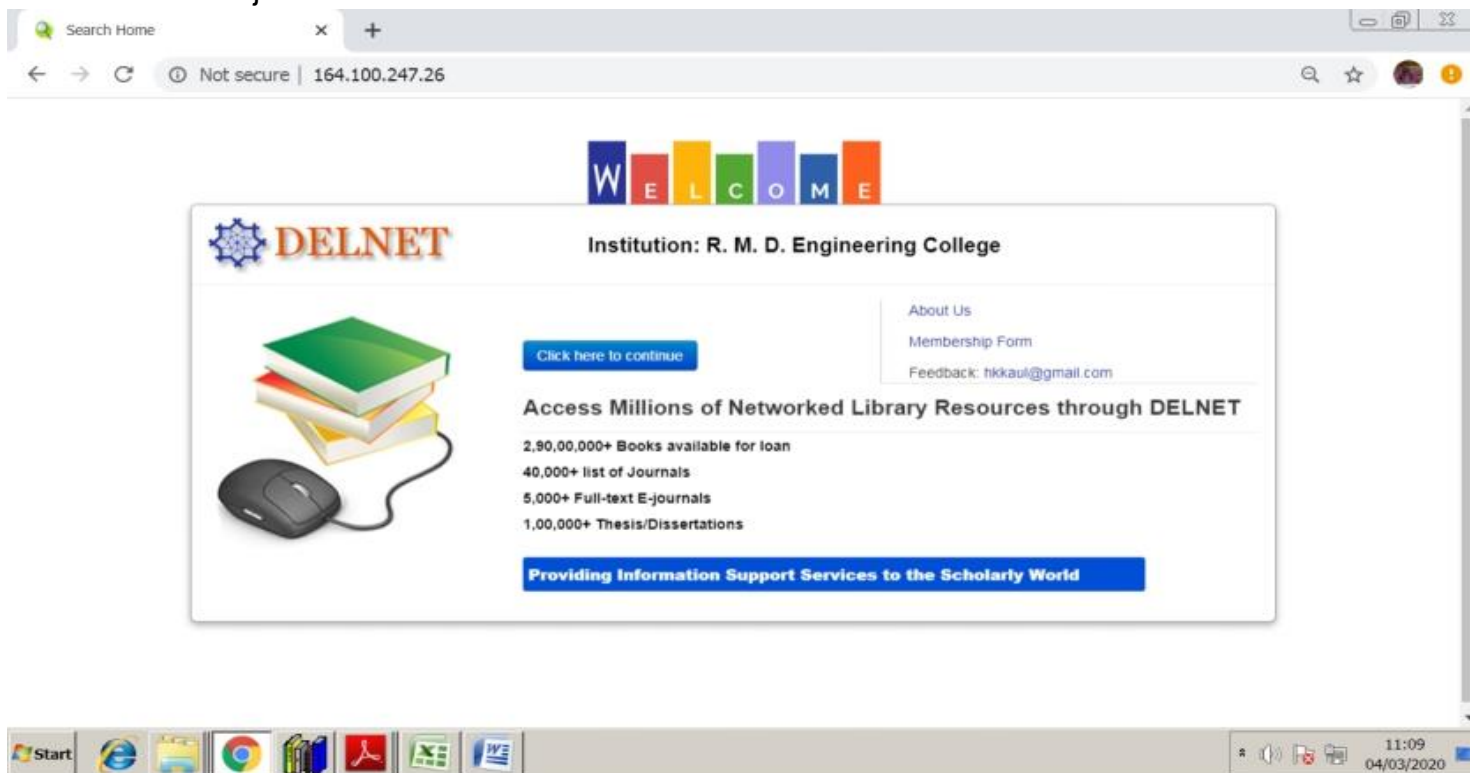




## 6. Database: DELNET (Developing Library Network): Subscribed since 2014 to 2023

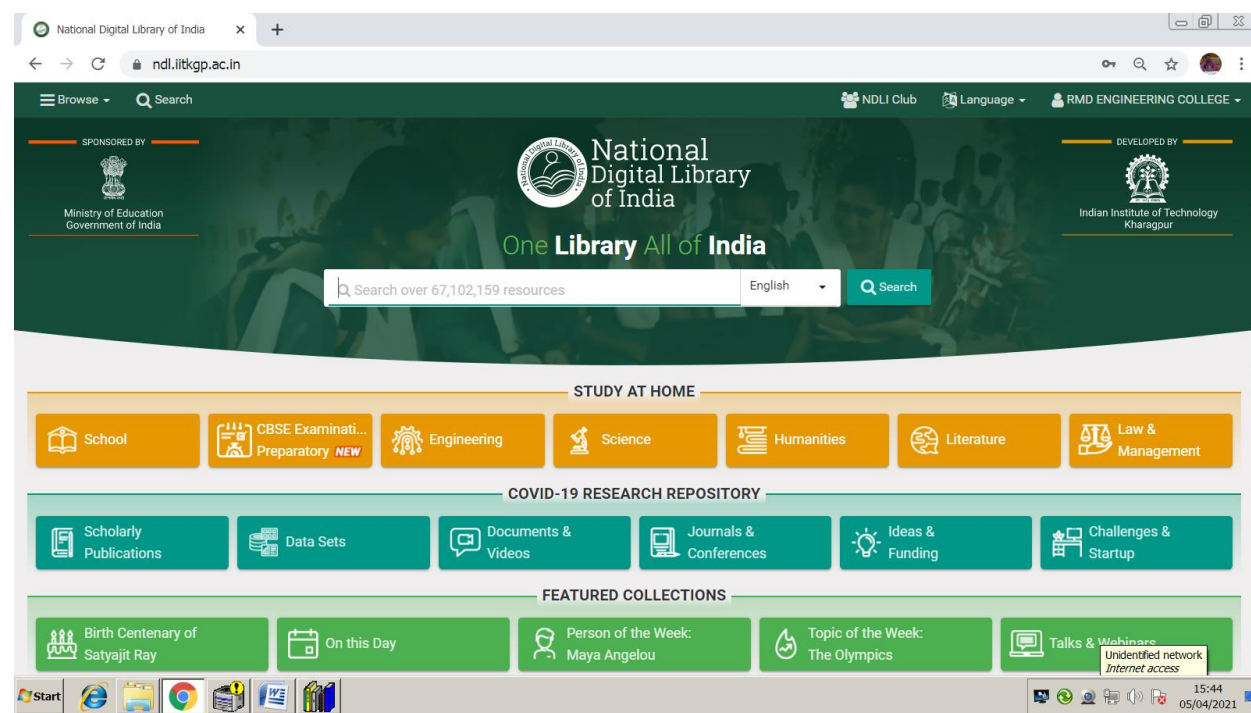
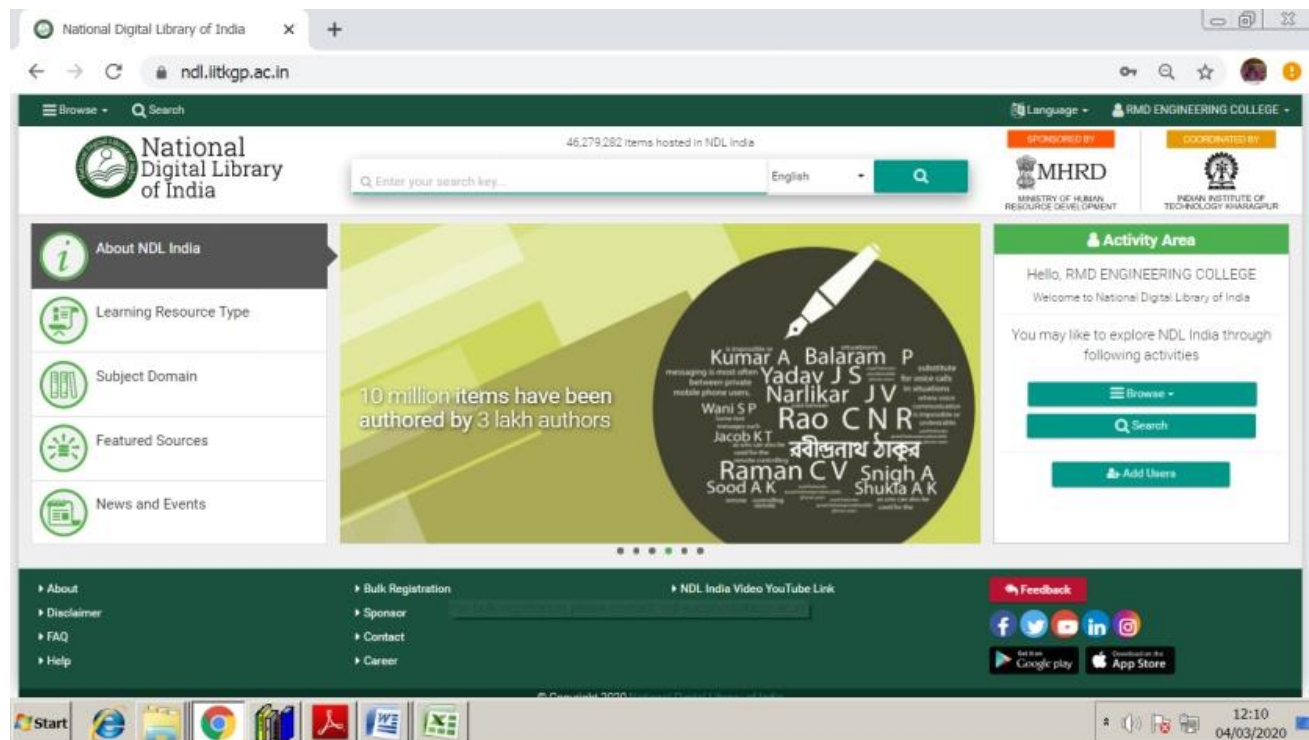


2.90,00,000 Books are available for interlibrary loan, 40,000 more list of journals, 5000 more are available full text journals and 100000 more Thesis and dissertation.



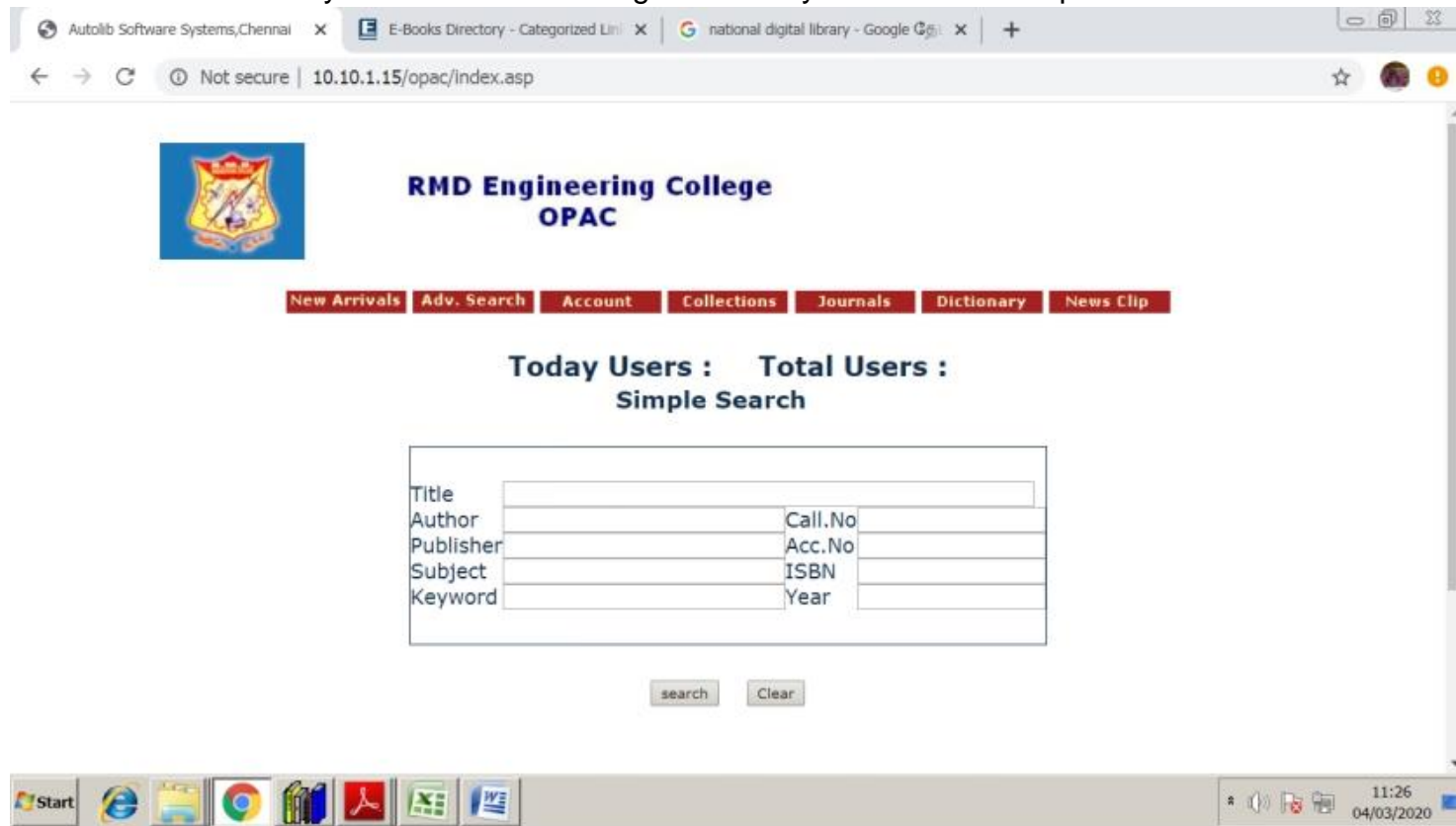
## 7. Database: National Digital Library

Use can access NCERT e-books, NPTEL video courses, audio lecture. INFLINET nadLibriVox audio.



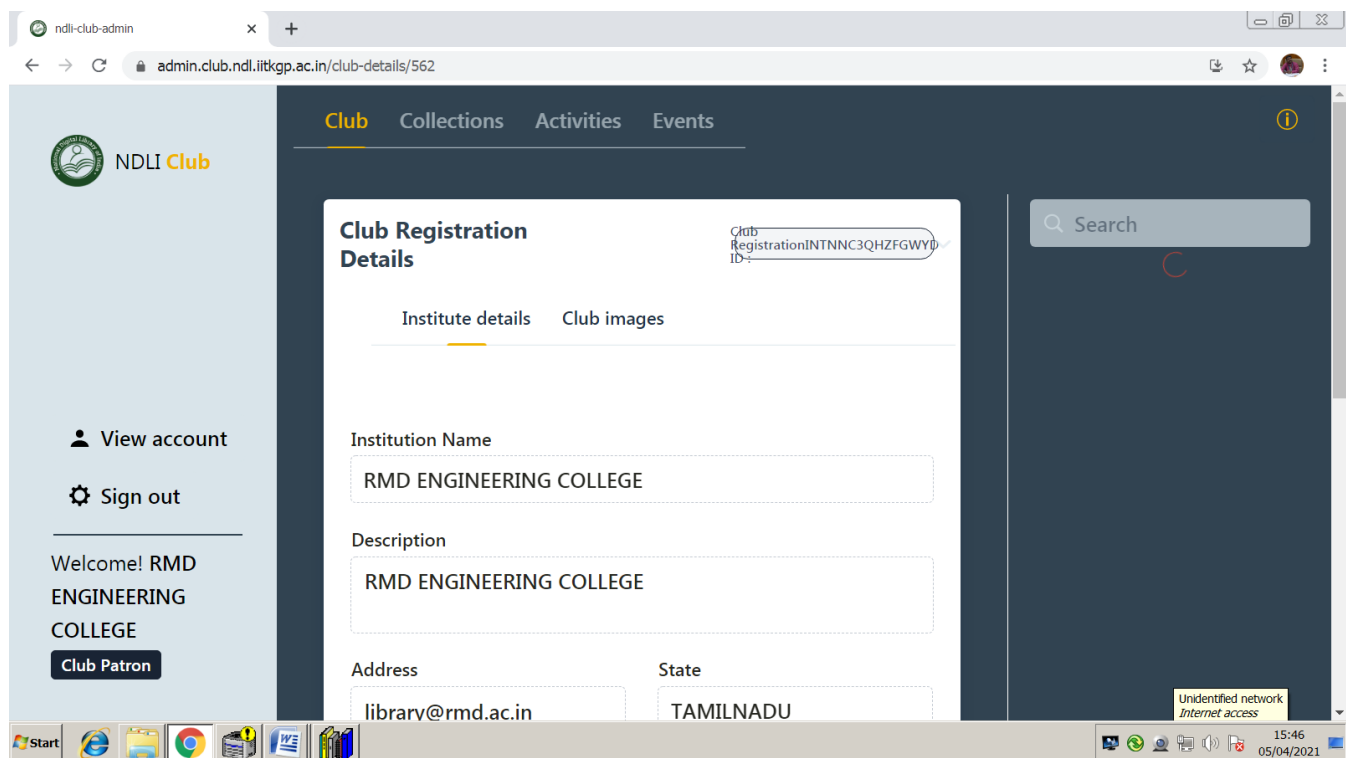
## 8. REMOTE ACCESS

User can access library online access catalogue from anywhere in our campus.



The screenshot shows a web browser window with the URL `10.10.1.15/opac/index.asp`. The page features the RMD Engineering College logo and the text "RMD Engineering College OPAC". Below this, there are navigation links: "New Arrivals", "Adv. Search", "Account", "Collections", "Journals", "Dictionary", and "News Clip". A status bar indicates "Today Users : Total Users : Simple Search". A search form is present with fields for Title, Author, Publisher, Subject, Keyword, Call.No, Acc.No, ISBN, and Year. The form includes "search" and "Clear" buttons. The Windows taskbar at the bottom shows the Start button and several application icons, with the system clock displaying 11:26 on 04/03/2020.

## 09.NATIONAL DIGITAL LIBRARY CLUB MEMBERSHIP(2021-2023)



The screenshot shows a web browser window with the URL `admin.club.ndli.iitkgp.ac.in/club-details/562`. The page displays the "Club Registration Details" for the "NDLI Club". The "Institute details" tab is active, showing the following information:

- Institution Name: RMD ENGINEERING COLLEGE
- Description: RMD ENGINEERING COLLEGE
- Address: library@rmd.ac.in
- State: TAMILNADU

The "Club images" tab is also visible. The page includes a search bar and a "View account" link. The Windows taskbar at the bottom shows the Start button and several application icons, with the system clock displaying 15:46 on 05/04/2021.

## Laboratory and Workshops

### List of Major Equipment/Facilities in each Laboratory/Workshop

*Requirements for a batch of 30 students*

#### Computer Science and Engineering:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	CS8261 C PROGRAMMING LABORATORY	Systems with Linux Operating System with gnu compiler	30	30
2	CS8381 DATA STRUCTURES LABORATORY	Systems with Linux Operating System with gnu compiler	30	30
3	CS8382 DIGITAL SYSTEM LABORATORY	Digital trainer kits	30	30
		Digital ICs	30	30
		Software: HDL simulator	30	30
4	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Netbeans or Eclipse	30	30
5	CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY	Systems with MySql	30	30
		Visual Studio	30	30
		Server	1	1
6	CS8461 OPERATING SYSTEMS LABORATORY	Systems with Linux OS and GNU Computer	30	30
7	CS8581 NETWORKS LABORATORY	Standalone Desktops	30	30
		C / C++ / Java / Python / Equivalent Compiler Network Simulator like NS2 / Glomosim / OPNET / Packet Tracer / Equivalent	30	30
8	EC8681 MICROPROCESSOR AND MICROCONTROLLER LABORATORY	8086 Microprocessor trainer kit with power supply	15	15
		8051 Microcontroller trainer kit	15	15
		Traffic light control interfacing card compatible with 8086 & 8051 kits	5	5
		Stepper motor control interfacing compatible with 8086 & 8051 kits	5	5
		Digital clock interfacing board compatible with 8086 & 8051 kits	5	5
		Keyboard & Display interface board compatible with 8086 & 8051 kits	5	5
		Printer interfacing card compatible with 8086 & 8051 kits	5	5
		A/D and D/A interfacing card compatible with 8086 & 8051 kits	5	5

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Serial and Parallel interfacing card compatible with 8086 & 8051 kits	5	5
9	CS8582 OBJECT ORIENTED ANALYSIS AND DESIGN LABORATORY	Rational Suite (User License)	30	30
		Open-Source Alternatives: ArgoUML, StarUML, Visual Paradigm (or) Equivalent Eclipse IDE and Junit	30	30
		PCs	30	30
10	CS8661 INTERNET PROGRAMMING LABORATORY	Systems	30	30
		Server (Web Server)	1	1
		Java/JSP/ISP Webserver/Apache Tomcat / MySQL / Dreamweaver or Equivalent, WAMP/XAMP	30	30
11	CS8662 MOBILE APPLICATION DEVELOPMENT LABORATORY	Standalone desktops with Windows or Android or iOS or Equivalent Mobile Application Development Tools with appropriate emulators and debuggers Tools with appropriate emulators and debuggers	30	30
12	IT8761 SECURITY LABORATORY	C / C++ / Java or equivalent compiler GnuPG, Snort, N-Stalker or Equivalent	30	30
		PCs	30	30
13	CS8711 CLOUD COMPUTING LABORATORY	Virtual box, VMware Workstation, Cloud Environment Creation, Openstack, Hadoop, Coludism, GAE Launcher	30	30

### Electrical and Electronics Engineering:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	EE8261 ELECTRIC CIRCUITS LABORATORY	RegulatedPowerSupply:0- 15V D.C	10	10
		Function Generator(1MHz)	10	10
		Single Phase Energy Meter	1	1
		Oscilloscope(20MHz).	10	10
		Digital Storage Oscilloscope(20MHz)	1	1
		PC With Circuit Simulation Software (10 Users)	10	10
		e-Sim/Scilab/Pspice / Matlab /otherEquivalentsoftwarePackage)	10	10
		Printer	1	1
		AC/DC-Voltmeters	10	10
		Ammeters	10	10
		Multi-meters	10	10
		Single Phase Watt meter	3	3
		Decade Resistance Box, Decade Inductance Box, Decade Capacitance Box (Each)	6	6
		Circuit Connection Boards	10	10
2	EC8311 ELECTRONICS LABORATORY	Semiconductor devices like Diode, ZenerDiode, NPN Transistors, JFET, UJT, Photo diode, Photo Transistor	10	10
		Resistors, Capacitors and inductors	10	10
		Necessary digitalIC8	10	10
		Function Generators	10	10
		Regulated 3 output Power Supply 5+_ 15V	10	10
		CRO	10	10
		Storage Oscilloscope	1	1
		Breadboards	10	10
3	EE8461 LINEAR AND DIGITAL INTEGRATED CIRCUITS LABORATORY	DC Shunt Motor with Loading Arrangement	3	3
		DC Shunt Motor Coupled With Three phase Alternator	1	1
		Single Phase Transformer	4	4
		DC Series Motor with Loading Arrangement	1	1
		DC Compound motor with loading arrangement	1	1
		Three Phase Induction Motor with Loading Arrangement	2	2

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Single Phase Induction Motor with Loading Arrangement	1	1
		DC Shunt Motor Coupled With DC Compound Generator	2	2
		DC Shunt Motor Coupled With DC Shunt Generator	1	1
		Tachometer-Digital/Analog	8	8
		Single Phase Auto Transformer	2	2
		Three Phase Auto Transformer	1	1
		Single Phase Resistive Loading Bank	2	2
		Three Phase Resistive Loading Bank	2	2
4	EE8461 LINEAR AND DIGITAL INTEGRATED CIRCUITS LABORATORY	Dual,(0-30V )variable Power Supply	10	10
		CRO(30MHz)	9	9
		Digital Multimeter	10	10
		Function Generator(1MHz)	8	8
		IC Tester(Analog)	2	2
		Breadboard	10	10
		Computer(PSPICE installed)	1	1
		IC741/ICNE555/566/565	10	10
		Digital IC types	10	10
		LED	10	10
		LM317	10	10
		LM723	10	10
		ICSG3524/ SG3525	10	10
		Transistor-2N3391	10	10
		Diodes,IN4001, BY126	10	10
		Zener diodes	10	10
		Potentiometer	10	10
		Step-downtransformer230V/12-0-12V	10	10
		Capacitor	10	10
		Resistors1/4WattAssorted	10	10
		Single Strand Wire	10	10
5	EE8411 ELECTRICAL	SynchronousInductionmotor3HP	1	1

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
	MACHINES LABORATORY II	DC Shunt Motor Coupled With Three phase Alternator	4	4
		DC Shunt Motor Coupled With Three phase Slipring Induction motor	1	1
		Three Phase Induction Motor with Loading Arrangement	2	2
		Single Phase Induction Motor with Loading Arrangement	2	2
		Tachometer-Digital/Analog	8	8
		Single Phase Auto Transformer	2	2
		Three Phase Auto Transformer	3	3
		Single Phase Resistive Loading Bank	2	2
		Three Phase Resistive Loading Bank	2	2
		Capacitor Bank	1	1
6	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Netbeans or Eclipse	30	30
7	EE8511 CONTROL AND INSTRUMENTATION LABORATORY	PID controller simulation and learner kit	1	1
		DSO for capturing transience	1	1
		Personal computers with contro lsystem Simulation packages	10	10
		DC motor- Generator test set-up for Evaluation of motor parameters	1	1
		CRO 30MHz	1	1
		2MHzFunctionGenerator	1	1
		Position Control Systems Kit(with manual)	1	1
		Tacho Generator Coupling set	1	1
		AC Synchro transmitter&receiver	1	1
		Digital multimeters, speed and torque sensors	10	10
		R,L,CBridgekit (withmanual)	1	1
		Electric heater	1	1
		Thermometer	1	1
		Thermistor(silicontype)RTDnickeltype	1	1
		30 psi Pressure chamber(complete set)	1	1
		Current generator(0-20mA)	1	1
		Air foot pump(with necessary connecting tubes)	1	1



Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		LVDT20mmcorelengthmovabletype	1	1
		CRO 30MHz	1	1
		Optical sensor	1	1
		Strain Gauge Kit with Handy lever beam	1	1
		100gmweights	10	10
		Flow measurement Trainer kit (1/2 HP Motor,Water tank,Digital Milli ammeter,complete set)	1	1
		Single phase Auto transformer	1	1
		Watt hour meter(energymeter)	1	1
		Voltmeter Rheostat Stopwatch Connecting wires	20	20
		ICtrainerkit	1	1
		InstrumentationAmplifierkit	1	1
		Analog-DigitalandDigital-Analog converters(ADCandDACs)	1	1
8	EE8661 POWER ELECTRONICS AND DRIVES LABORATORY	Device characteristics (for SCR,MOSFET,TRIAC,GTO,IGCTandIGBTkitwith builtin/discretepowersupplyandmeters)	2	2
		Single phase SCR based half controlled on verter and fullycontrolled converter along with built-in/separate/firingcircuit/moduleandmeter	2	2
		MOSFET based stepup and step down choppers(Builtin/ Discrete)	1	1
		IGBTbased single phase PWM inverter module/Discrete Component	2	2
		IGBTbased three phase PWM inverter module/Discrete Component	2	2
		Switched mode power converte rmodule/Discrete Component	2	2
		SCR &TRIAC based1 phaseAC controller along with lampor rheostatload	2	2
		Cyclo converter kit with firing module	1	1
		Dual regulated Dc power supply with common ground	5	5
		Cathode ray Oscilloscope	10	10
		IsolationTransformer	5	5
		Single phaseAutotransformer	3	3
		Components (Inductance,Capacitance)	3	3

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Multimeter	5	5
		LCRmeter	3	3
		Rheostats of various ranges	2	2
		Worktables	10	10
		DC and AC meters of required ranges	20	20
9	EE8681 MICROPROCESSORS AND MICROCONTROLLERS LABORATORY	8085 MicroprocessorTrainer with Power Supply	15	15
		8051MicroController Trainer Kit with power supply	15	15
		8255 Interface board	5	5
		8251Interface board	5	5
		8259Interface board	5	5
		8279 Keyboard/Display Interface board	5	5
		8254 timer counter	5	5
		ADC and DAC card	5	5
		AC & DC motor with Controller	5	5
		Traffic Light Control System	5	5
10	EE8711 POWER SYSTEM SIMULATION LABORATORY	Personal computers (Intel i3, 80GB,2GBRAM)	30	30
		Printerlaser	1	1
		Dotmatrix	1	1
		Server (Intel i5, 80GB, 2GBRAM) (HighSpeedProcessor)	1	1
		powersystem simulations software	5	5
		Compliers:C, C++,VB,VC++	30	30
11	EE8712 RENEWABLE ENERGY SYSTEMS LABORATORY	Personal computers (Intel i3, 80GB,2GBRAM)	15	15
		CRO 30MHz	9	9
		Digital Multimeter	10	10
		PV panels-100W,24V	1	1
		Battery storage system with charge and Discharge contro l40Ah	1	1
		PV Emulator	1	1
		Micro Wind Energy Generatormodule	1	1
		Potentiometer	5	5
		Step-downtransformer230V/12-0-12V	5	5

## Electronics and Communication Engineering:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	EC8261 CIRCUITS AND DEVICES LABORATORY	BC107,BC148,2N2646,BFW10	25	25
		IN4007,Zenerdiodes	25	25
		Resistors,Capacitors,Inductors-	100	100
		Bread Boards	15	15
		CRO(30MHz)	15	15
		Function Generators(3MHz)	10	10
		Dual Regulate dpower Supplies(0-30V)	10	10
2	EC8361 ANALOG AND DIGITAL CIRCUITS LABORATORY	CRO(30MHz)	15	15
		Signal Generator /Function Generators(3 MHz)	15	15
		Dual Regulated Power Supplies (0-30V)	15	15
		Standalone desktop PCs with SPICEsoftware	15	15
		Transistor/FET(BJT-NPN-PNPandNMOS/PMOS)	50	50
		Dual power supply/single mode powersupply	15	15
		Resistors,Capacitors, Inductors	50	50
		Diodes,Zenerdiode	10	10
		ICTrainer Kit	15	15
		Bread Boards	15	15
		ComputerwithHDLsoftware	15	15
		Sevenssegmentdisplay	15	15
		Multimeter	15	15
		ICs7400/7402/ 7404/ 7486/ 7408/ 7432/7483/74150/ 74151/74147/ 7445/7476/7491/555/7494 /7447/ 74180/7485/7473/74138 /7411/ 7474	50	50
3	EC8381 FUNDAMENTALS OF DATA STRUCTURES IN C LABORATORY	Standalone desktops (or) Server supporting with C compiler	30	<b>30</b>
4	EC8461 CIRCUITS DESIGN AND SIMULATION LABORATORY	CRO(Min30MHz)	15	15
		Signal Generator /Function Generators(2 MHz)	15	15
		Dua IRegulated Power Supplies (0-30V)	15	15
		Digital Multimeter	15	15

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Digital LCR Meter	2	2
		Standalone desktops PC	15	15
		Transistor/FET(BJT-NPN-PNPandNMOS/PMOS)	50	50
		Transistors, Resistors, Capacitors, Inductors, diodes, Zener Diodes, Bread Boards, Transformers	50	50
		SPICE Circuit Simulation Software (any public domain or commercial software)	15	15
5	EC8462 LINEAR INTEGRATED CIRCUITS LABORATORY	CRO /DSO(Min30MHz)	15	15
		Signal Generator /Function Generators (2 MHz)	15	15
		Dual Regulated Power Supplies (0-30V)	15	15
		DigitalMultimeter	15	15
		ICtester	5	5
		Standalone desktops PC	15	15
		Transistors, Resistors, Capacitors, diodes, Zener diodes, Bread Boards, Transformers, wires, Power transistors, Potentiometer, A/D and D/A converters, LEDs	50	50
6	EC8562 DIGITAL SIGNAL PROCESSING LABORATORY	PCs with Fixed / Floating point DSP Processors (Kit/Add-onCards)	15	15
		MATLAB with Simulink and Signal ProcessingToolBox or Equivalent Software in desktop systems	15	15
		Signal Generators(1MHz)	20	20
		CRO(20MHz)	20	20
7	EC8561 COMMUNICATION SYSTEMS LABORATORY	Kits for Signal Sampling, TDM, AM, FM, PCM, DM and Line Coding Schemes, Error control code	14	14
		CROs	15	15
		MATLAB/SCILA Bore equivalent software package for simulation experiments	20	20
		PCs	20	20
		Probes(CRO)	30	30
		Patchcords	100	100
		MSO	4	4
		DSO	4	4
8	EC8563 COMMUNICATION NETWORKS LABORATORY	C/ Python/Java/Equivalent Compiler	30	30
		Standard LAN Trainer Kits	4	4
		Qualnet/Optisim/Matlab/NS2/Netsim	30	30
		PCs	30	30
9	EC8681	8086 Microprocessor trainer kit with power supply	15	15

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
	MICROPROCESSOR AND MICROCONTROLLER LABORATORY	8051Microcontroller trainer kit	15	15
		Traffic light control interfacing card compatible with 8086&8051kits	5	5
		Stepper motor control interfacing compatible with 8086&8051kits	5	5
		Digital clock interfacing board compatible with 8086&8051kits	5	5
		Keyboard&Display interface board compatible with 8086&8051kits	5	5
		Printer interfacing card compatible with 8086 & 8051kits	5	5
		A/DandD/Ainterfacing card compatible with 8086 & 8051kits	5	5
		Serial and Parallel interfacing card compatiblewith 8086&8051kits	5	5
10	EC8661 VLSI DESIGN LABORATORY	XilinxISE/AlteraQuartus/equivalentEDATools	10	10
		Xilinx/Altera/equivalentFPGABoards	10	10
		Cadence/Synopsis/MentorGraphics/Tanner/equivalentE DATools	10	10
		PersonalComputer	30	30
12	EC8711 EMBEDDED LABORATORY	Embedded trainer kits with ARM board	10	10
		Embedded trainer kits suitable for wireless communication	10	10
		Adequate quantities of Hardware,software and consumables	10	10
	EC8761 ADVANCED COMMUNICATION LABORATORY	Trainer kit for carrying out LED and PIN diode characteristics,Digital multimeter,optical power meter	2	2
		Trainer kit for determining the mode characteristics,losses in optical fiber	2	2
		Trainer kit for analyzing Analog andDigitallinkperformance,2MbpsPRBSData source,10 MHzsignalgenerator, 20MHzDigitalstorageOscilloscope	2	2
		Kit for measuring Numerical apertureandAttenuation of fiber	2	2
		Advanced Optical fiber trainer kit for PC to PC communication, BER Measurement,Pulse broadening	2	2
		MM/SM Glass and plastic fiber patch chord swith ST/SC/E2000connectors	2	2
		LED swith ST/SC/ E2000 receptacles –650/850nm	2	2
		PINPDswithST/SC/E2000 receptacles–650/850nm	2	2
		Digital Communications Teaching Bundle (LabVIEW/MATLAB/Equivalent softwaretools)	10	10
		Software Define Radio Transceiver Platform with antennas and accessories	2	2

## Electronics and Instrumentation Engineering:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	EE8261 ELECTRIC CIRCUITS LABORATORY	Regulated Power Supply: 0 - 15 V D.C	10	10
		Function Generator (1 MHz)	10	10
		Single Phase Energy Meter	1	1
		Oscilloscope (20 MHz).	10	10
		Digital Storage Oscilloscope (20 MHz)	1	1
		PC with Circuit Simulation Software	10	10
		e-Sim / Scilab/ Pspice / Matlab /other Equivalent software Package)	10	10
		Printer	1	1
		AC/DC - Voltmeters	10	10
		Ammeters	10	10
		Multi-meters	10	10
		Single Phase Wattmeter	3	3
		Decade Resistance Box, Decade Inductance Box, Decade Capacitance Box (Each).	6	6
		Circuit Connection Boards	10	10
2	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Net beans or Eclipse	30	30
3	EI8361 MEASUREMENTS AND TRANSDUCERS LABORATORY	Measurement of Linear displacement using Potentiometer	1	1
		Strain gauge and Load cell Characterisation and application	1	1
		LVDT Characterisation and application	1	1
		Hall effect Characterisation and application	1	1
		Measurement of Angular displacement	1	1
		Muffle furnace	1	1
		Thermistor Characterisation and application	1	1
		Various types Thermocouple and RTD Characterisation and application	1	1
		Measurement of power and energy	1	1
		Sufficient number power supply, Galvanometer, Breadboard, Multimeter, Resistors, Decade	15	15
		Sufficient number Capacitance box, Decade resistance box, Decade Inductance box, CRO	15	15
4	EE8461 LINEAR AND	Dual, (0-30V) variable Power Supply	10	10

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
	DIGITAL INTEGRATED CIRCUITS LABORATORY	CRO(30MHz)	9	9
		DigitalMultimeter	10	10
		FunctionGenerator(1MHz)	8	8
		ICTester(Analog)	2	2
		Breadboard	10	10
		Computer(PSPICEinstalled)	1	1
		IC741/ICNE555/566/565	10	10
		DigitalICtypes	10	10
		LED	10	10
		LM317	10	10
		LM723	10	10
		ICSG3524/SG3525	10	10
		Transistor2N3391	10	10
		Diodes(IN4001,BY126)	10	10
		Zenerdiodes	10	10
		Potentiometer	10	10
		Step-downtransformer(230v/12-0-12v)	10	10
		Capacitor	10	10
		Resistors1/4WattAssorted	10	10
		SingleStrandWire	10	10
5	EI8461 DEVICES AND MACHINES LABORATORY	CircuitSimulationSoftware(5Users)	5	5
		(Pspice/Matlab/otherEquivalentsoftware Package) with PC	30	30
		Sufficient number of power supply, Galvanometer, Breadboard,Multimeter,	10	10
		Semiconductor devices like Diode, ZenerDiode,NPN Transistors,JFET, andUJT	10	10
		DC ShuntMotorwithLoading Arrangement	3	3
		SinglePhaseTransformer	3	3
		Single PhaseInductionMotorwithLoading Arrangement	1	1
		Single PhaseAuto Transformer	3	3
		Single PhaseResistiveLoadingBank	2	2
		Ammeters	2	2

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Voltmetersormultimeters	2	2
		Switches	2	2
		Tachometers	2	2
		Wattmeters	2	2
6	EE8681 MICROPROCESSORS AND MICROCONTROLLERS LABORATORY	8085MicroprocessorTrainerwithPowerSupply	15	15
		8051Micro ControllerTrainerKitwith powersupply	15	15
		8255Interfaceboard	5	5
		8251Interfaceboard	5	5
		8259Interfaceboard	5	5
		8279Keyboard/DisplayInterfaceboard	5	5
		8254timercounter	5	5
		ADC andDACcard	5	5
		AC &DC motorwithController	5	5
		TrafficLightControlSystem	5	5
7	EI8561 INDUSTRIAL INSTRUMENTATION LABORATORY	Orificeplate	1	1
		Deadweighttesterwith pressure gauge	1	1
		Torquetrainer	1	1
		SayboltViscometer	1	1
		Vacuum gauge	1	1
		DPtransmitter	1	1
		UVVisiblespectrophotometer	1	1
		pHmeter	1	1
		Conductivitymeter	1	1
		ECGtrainer	1	1
		Pulse ratetrainer	1	1
		tachometer	1	1
8	CS8381 DATA STRUCTURES LABORATORY	SystemswithLinuxOperatingSystemwithgnucompiler	30	30
9	EI8661 PROCESS CONTROL LABORATORY	Flowprocess stationwith allaccessories	1	1
		Analog/DigitalPIDcontroller	2	2



Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Control valve trainer (with position for varying pressure across the valve)	1	1
		Flowmeter	1	1
		Level process station with all accessories	1	1
		Temperature process station with all accessories	1	1
		Pressure process station with all accessories	1	1
		MATLAB software	Minimum 10 user license	10 User license
		Personal computer	15	15
10	EI8761 INDUSTRIAL AUTOMATION LABORATORY	Programmable Logic controller	5	5
		Programmable Logic controller Software	10	10
		DAQ card	2	2
		Filling/Draining System	1	1
		Traffic Light Controller	2	2
		DC Motor	5	5
		Personal computer	10	10
		DCS along with interface modules	1	1
		Thermal Process	1	1
		Level Process	1	1
		Flow Process stations	1	1
		Smart Transmitter	1	1
11	EI8762 INSTRUMENTATION SYSTEM DESIGN LABORATORY	Sufficient number of Monolithic Instrumentation amplifier, Operational amplifiers, IC7805 and resistors, diodes, capacitors	15	15
		Linear control valve, ON/OFF control valve, Air regulator, Rotameter, Pump	1 each	1
		Sufficient number of IC741, CRO, Breadboard, Signal generator (PID) Microprocessor kit with ADC and DAC section	15	15
		Any Process station (Temperature or Level) with Corresponding sensors, Data acquisition card, and Storage device (micro controller/microprocessor)	1	1
		Flow process station with DP transmitter	1	1
		Loop analyzer	1	1
		Thermocouple & RTD	Minimum 1	1
		Bonded strain gauge, Loads	Minimum 1	1
		Orifice plate	Minimum 1	1

### Information Technology:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	IT8211 Information Technology Essentials Laboratory	PCwithLinux/Windows/Solaris/MacOSXoperatingsystem	30	30
		XAMPPWebserver	1	1
		Mobile App Development tool(LikeapplInventor)	1	1
2	CS8261 C Programming Laboratory	Systems with Linux Operating System with gnu compiler	30	30
3	CS8382 DIGITAL SYSTEMS LABORATORY	Digitaltrainerkits	30	30
		DigitalIcs	30	30
		Software:HDL simulator	30	30
4	CS8381 DATA STRUCTURES LABORATORY	Systems with Linux Operating System with gnu compiler	30	30
5	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Netbeans or Eclipse	30	30
6	CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY	SystemswithMySQL	30	30
		VisualStudio	30	30
		Server	1	1
7	CS8461 OPERATING SYSTEMS LABORATORY	Systems with Linux OS and GNU Computer	30	30
8	EC8681 MICROPROCESSOR AND MICROCONTROLLER LABORATORY	8086Microprocessortrainerkitwithpower supply	15	15
		8051Microcontrollertrainerkit	15	15
		Traffic light control interfacing card compatible with 8086&8051kits	5	5
		Stepper motor control interfacing compatible with 8086&8051kits	5	5
		Digital clock interfacing board compatible with 8086&8051kits	5	5
		Keyboard&Display interface board compatible with 8086&8051kits	5	5
		Printer interfacing card compatible with 8086 & 8051kits	5	5
		A/DandD/A interfacing card compatible with 8086 & 8051kits	5	5
		Serial and Parallel interfacing cardcompatible with 8086&8051kits	5	5
9	CS8581 NETWORKS LABORATORY	StandaloneDesktops	30	30
		C / C++ / Java / Python / EquivalentCompilerNetwork SimulatorlikeNS2/Glomosim / OPNET / Packet Tracer /Equivalent	30	30
10	IT8511 WEB TECHNOLOGY	Dream Weaver or Equivalent, MySQL or Equivalent, Apache Server,WAMP/XAMPP	30	30

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
	LABORATORY	Standalonedesktops	30	30
11	CS8582 OBJECT ORIENTED ANALYSIS AND DESIGN LABORATORY	Rational Suite(UserLicense)	30	30
		ArgoUML,StarUML, Visual Paradigm(Or)Equivalent EclipseIDEand Junit	30	30
		PCs	30	30
12	CS8662 MOBILE APPLICATION DEVELOPMENT LABORATORY	Stand alone desktops with Windows or Android or iOS or Equivalent Mobile Application Development Tools with appropriate emulators and debuggers	30	30
13	IT8761 SECURITY LABORATORY	C / C++ / Java or equivalent compiler GnuPG, Snort, N-Stalkeror Equivalent	30	30
		Standalonedesktops	30	30
14	IT8711 FOSS AND CLOUD COMPUTING LABORATORY	PCwithlatest version	30	30
		Cloud tools from free of open sourcelike open nebula, open stack, Eucalyptus software	30	30

### Computer Science and Business Systems:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	AD8261 DATA STRUCTURES DESIGN LABORATORY	Python 3 interpreter for Windows/Linux	30	30
2	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Java Interpreter for Windows or Linux	30	30
3	CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY	System with MYSQL	30	30
4	CW8311 BUSINESS COMMUNICATION AND VALUE SCIENCE LABORATORY I	System with Windows 7	30	30
		Speaker	1	1
		Headphones	30	30
		Projector	1	1
5	CS 8461 OPERATING SYSTEMS LABORATORY	System with Linux OS	30	30
6	CW 8411 COMPUTATIONAL STATISTICS LABORATORY	R and R Studio for Windows or Linux	30	30
7	CW8412 BUSINESS COMMUNICATION AND VALUE SCIENCE LABORATORY II	System with Windows 7	30	30
		Speaker	1	1
		Headphones	30	30
		Projector	1	1
8	CW8512 SOFT SKILLS LABORATORY	System with Hi Class Software	30	30
9	CW8511 MINI PROJECT (SOFTWARE / SYSTEM DESIGN/ARCHITECTURE) END TO END	System with Windows	30	30
10	CW 8611 BUSINESS ANALYTICS LABORATORY (DESIGN/ARCHITECTURE) END TO END	R and R Studio for Windows or Linux	30	30
11	CW 8612 ARTIFICIAL INTELLIGENCE LABORATORY	System with Turbo C and LISP and PROLOG	30	30

### Artificial Intelligence and Machine Learning:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	21GE111 C PROGRAMMING LABORATORY	Systems with Linux Operating System and gnu compiler	30	30
2	21CS211 DATA STRUCTURES LABORATORY	Systems with Linux Operating System and gnu compiler	30	30
3	21CS202 PYTHON PROGRAMMING (LAB INTEGRATED)	Systems with Python Compiler	30	30
4	21IT412 DATABASE MANAGEMENT SYSTEMS LABORATORY	Systems with MySql	30	30
		Visual Studio / Eclipse	30	30
		Server	1	1
5	21CS311 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Netbeans or Eclipse	30	30
6	21AM302 PRINCIPLES OF ARTIFICIAL INTELLIGENCE	Machines with intel i5/i7 processor and minimum 8 GB RAM	30	30
		R/Python	30	30
		Keras/scikit-learn/Pytorch/Tensor Flow	30	30
7	21AM404 OPERATING SYSTEM FUNDAMENTALS (LAB INTEGRATED)	Systems with Linux OS and gnu compiler	30	30
8	21AM411 MACHINE LEARNING LABORATORY	Machines with intel i5/i7 processor and minimum 8 GB RAM	30	30
		R/Python	30	30
		Keras/scikit-learn/Pytorch/Tensor Flow	30	30
9	21AM412 DATA ANALYTICS LABORATORY	Machines with intel i5/i7 processor and minimum 8 GB RAM	30	30
		Hadoop Framework, Map Reduce Framework/ Spark/ Hive/ Pig	30	30
		Anaconda / R Studio or Equivalent	30	30

## List of Experimental Setup in each Laboratory/Workshop

### Computer Science and Engineering

#### 22CS101 PROBLEM SOLVING USING C++ ( Lab Integrated)

1. Write C/C++ programs for the following:
  - a. Find the sum of individual digits of a positive integer.
  - b. Compute the GCD of two numbers.
  - c. Find the roots of a number (Newton's method)
2. Write C/C++ programs using arrays:
  - a. Find the maximum of an array of numbers.
  - b. Remove duplicates from an array of numbers.
  - c. Print the numbers in an array after removing even numbers.
3. Write C/C++ programs using strings:
  - a. Checking for palindrome.
  - b. Count the occurrences of each character in a given word
4. Generate salary slip of employees using structures and pointers. Create a structure Employee with the following members: EID, Ename, Designation, DOB, DOJ, Basic pay  
Note that DOB and DOJ should be implemented using structure within structure.
5. Compute internal marks of students for five different subjects using structures and functions.
6. Write a program Illustrating Class Declarations, Definition, and Accessing Class Members.
7. Program to illustrate default constructor, parameterized constructor and copy constructors.
8. Write a Program to Demonstrate the i) Operator Overloading. ii) Function Overloading.
9. Write a Program to Demonstrate Friend Function and Friend Class.
10. Program to demonstrate inline functions.
11. Program for Overriding of member functions.
12. Write C++ programs that illustrate how the following forms of inheritance are supported:
  - a) Single inheritance b) Multiple inheritance c) Multi level inheritance d) Hierarchical inheritance.
13. Program to demonstrate pure virtual function implementation.
14. Count the number of account holders whose balance is less than the minimum balance using sequential access file.
15. Write a Program to Demonstrate the Catching of all Exceptions.
16. Mini project

## 22CS102 SOFTWARE DEVELOPMENT PRACTICES (Lab Integrated)

### List of Exercise/Experiments:

1. Form a Team, Decide on a project:
  - a) Create a repository in GitHub for the team.
  - b) Choose and follow a Git workflow.
    - Each team member can create a StudentName.txt file with contents about themselves and the team project.
    - Each team member can create a branch, commit the file with a proper commit message and push the branch to remote GitHub repository.
    - Team members can now create a Pull request to merge the branch to master branch or main development branch.
    - The Pull request can have two reviewers, one peer team member and one faculty. Reviewers can give at least one comment for Pull Request updating.
    - Once pull request is reviewed and merged, the master or main development branch will have files created by all team members.
2. Create a web page with at least three links to different web pages. Each of the web pages is to be designed by a team member. Follow Git workflow, pull request and peer reviews.
3. Form a Team, Decide on a project:
  - c) Create a repository in GitHub for the team.
  - d) Choose and follow a Git workflow.
    - Each team member can create a StudentName.txt file with contents about themselves and the team project
    - Each team member can create a branch, commit the file with a proper commit message and push the branch to remote GitHub repository.
    - Team members can now create a Pull request to merge the branch to master branch or main development branch.
    - The Pull request can have two reviewers, one peer team member and one faculty. Reviewers can give at least one comment for Pull Request updating.
    - Once pull request is reviewed and merged, the master or main development branch will have files created by all team members.
4. Create a web page with at least three links to different web pages. Each of the web pages is to be designed by a team member. Follow Git workflow, pull request and peer reviews.
5. Create web pages using the following:
  - Tables and Lists
  - Image map
  - Forms and Form elements
  - Frames
6. Apply Cascading style sheets for the web pages created.
7. Form Validation (Date, Email, User name, Password and Number validation) using JavaScript.
8. Implement Event Handling in the web pages.

### Mini Projects-

Develop any one of the following web applications (not limited to one) using above technologies.

- Online assessment system
- Ticket reservation system
- Online shopping

- Student management system
- Student result management system
- Library management
- Hospital management
- Attendance management system
- Examination automation system
- Web based chat application

## **22EC101 DIGITAL PRINCIPLES AND SYSTEMS DESIGN (Lab Integrated)**

1. Implementation of Boolean expression using logic gates
2. Design of adders
3. Design of subtractors.
4. Design of binary adder using IC7483
5. Design of Multiplexers & Demultiplexers.
6. Design of Encoders and Decoders.
7. Implementation of a Boolean function using a multiplexer
8. Design and implementation of 3-bit ripple counters.
9. Design and implementation of 3-bit synchronous counter
10. Design and implementation of shift registers

## **22CS201 DATA STRUCTURES (Lab Integrated)**

1. Array implementation of List ADTs.
2. Linked list implementation of List ADTs.
3. Array implementation of Stack and Queue ADTs.
4. Linked list implementation of Stack and Queue ADTs.
5. Applications of List – Polynomial manipulations
6. Applications of Stack – Infix to postfix conversion and expression evaluation.
7. Implementation of Binary Trees and operations of Binary Trees.
8. Implementation of Binary Search Trees.
9. Implementation of Heaps using Priority Queues
10. Graph representation and Traversal algorithms.
11. Implement searching and sorting algorithms.



## 22CS202 JAVA PROGRAMMING (Lab Integrated)

1. Develop a Java application to generate Electricity bill. You must use one super class called EB Bill and must have two sub classes namely Domestic Bill and Commercial Bill. Create a class with the following members: Consumer no., consumer name, previous month reading, current month reading, type of EB connection (i.e domestic or commercial). Compute the bill amount using the following tariff

If the type of the EB connection is domestic, calculate the amount to be paid as follows: First 100 units - Rs. 1 per unit

101-200 units - Rs. 2.50 per unit 201 -500 units - Rs. 4 per unit

> 501 units - Rs. 6 per unit

If the type of the EB connection is commercial, calculate the amount to be paid as follows: First 100 units - Rs. 2 per unit

101-200 units - Rs. 4.50 per unit 201 -500 units - Rs. 6 per unit

> 501 units - Rs. 7 per unit

2. Arrays Manipulations: (Use Methods for implementing these in a Class)

Find kth smallest element in an unsorted array

Find the sub array with given sum

Matrix manipulations – Addition, Subtraction, Multiplication

Remove duplicate elements in an Array

3. Accept an integer value N and print the Nth digit in the integer sequence 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15 and so on till infinity.

Example: The 11th digit in the sequence 12345678910111213.... is 0.

4. Develop a Java application to implement currency converter (Dollar to INR, EURO to INR, Yen to INR and vice versa), distance converter (meter to KM, milesto KM and vice versa), time converter (hours to minutes, seconds and vice versa)using packages.

5. Develop a Java application with Employee class with Emp\_name, Emp\_id, Address, Mail\_id, Mobile\_no as members. Inherit the classes, Programmer, Assistant Professor, Associate Professor and Professor from employee class. AddBasic Pay (BP) as the member of all the inherited classes with 97% of BP as DA, 10 % of BP as HRA, 12% of BP as PF, 0.1% of BP for staff club fund. Generate pay slips for the employees with their gross and net salary.

6.. Design a Java interface for ADT Stack. Implement this interface using array and built-in classes. Provide necessary exception handling in both the implementations.

7. Write a Java Program to create an abstract class named Shape that contains two integers and an empty method named print Area(). Provide three classes named Rectangle, Triangle and Circle such that each one of the classes extends the class Shape. Each one of the classes contains the methods print Area () that prints the area of the given shape and Numberofsides() that prints the number of sides of the given shape.

8. Write a Java program to apply built-in and user defined exceptions.

9. Write a Java program to read and copy the content of one file to other by handling all file related exceptions.

10.String Manipulation:

a. Reversing a set of words and count the frequency of each letter in the string.

b. Pattern Recognition - Find the number of patterns of form 1[0]1 where [0] represents any number of zeroes (minimum requirement is one 0) there shouldnot be any other character except 0 in the [0] sequence in a given binary string.

c. Remove all the occurrences of string S2 in string S1 and print the remaining.

- d. Find the longest repeating sequence in a string
  - e. Print the number of unique string values that can be formed by rearranging the letters in the string S.
11. Write a Java program that correctly implements producer consumer problem using the concept of inter thread communication.
12. Collections:
- a. Write a program to perform string operations using ArrayList. Write functions for the following
    - i. Append - add at end
    - ii. Insert – add at particular index
    - iii. Search
    - iv. List all string starts with given letter
  - b. Find the frequency of words in each text.

## **22IT202 DATABASE MANAGEMENT SYSTEM (Lab Integrated)**

1. Data Definition Commands, Data Manipulation Commands for inserting deleting, updating and retrieving Tables and Transaction Control statements Write programs using the following system calls of UNIX operating system fork, exec, getpid, exit, wait, close, stat, opendir, readdir
2. Database Querying – Simple queries, Nested queries, Sub queries and Joins
3. Views, Sequences, Synonyms
4. Database Programming: Implicit and Explicit Cursors.
5. Procedures and Functions
6. Triggers
7. Exception Handling
8. Database Design using ER modeling, normalization and Implementation for any application
9. Database Connectivity with Front End Tools
10. Case Study using real life database applications anyone from the following list
  - Inventory Management for a EMart Grocery Shop
  - Society Financial Management
  - Cop Friendly App – Eseva
  - Property Management – eMall
  - Star Small and Medium Banking and Finance
11. Build Entity Model diagram. The diagram should align with the business and functional goals stated in the application.
12. Apply Normalization rules in designing the tables in scope.
13. Prepared applicable views, triggers (for auditing purposes), functions for enabling enterprise grade features.
14. Build PL SQL / Stored Procedures for Complex Functionalities, ex EOD Batch Processing for calculating the EMI for Gold Loan for each eligible Customer.
15. Ability to showcase ACID Properties with sample queries with appropriate settings

## 22CS301 ADVANCE JAVA PROGRAMMING (Lab Integrated)

1. Write Java programs to implement Queue interface
  - Demonstrate the working of Dequeue.
  - Demonstrate the working of ArrayQueue.
2. Write Java programs using Utilities
  - String Processing.
  - Date Manipulation.
3. Write Java programs using Comparable & Observer
4. Write a Java program to display a string in a frame
5. Write a Java program to change the color, font name, style and size of a string
6. Write a Java program to demonstrate mouse events
7. Write a Java program to demonstrate adapter classes
8. Write a Java program to design a calculator
9. Write a Java program to demonstrate event handlingCount the occurrences of each character in a given word.
10. Write programs in Java using Servlets:
  - To invoke servlets from HTML forms
  - Session tracking.
11. Create a dynamic web application using Servlet/JSP with a facility to
  - Login to the application
  - Register a new user and
  - Change password for an existing user
12. i. Validate the form using PHP regular expression.
13. ii. PHP stores a form data into database.
14. Write a web service for finding public review about a consumer product.
15. Write a web service for finding what people think by asking 500 people\_ consumer product.
16. Mini Projects-Develop any one of the following web applications using above technologies.
  - Online assessment system
  - Ticket reservation system
  - Online shopping
17. Simulation of DNS using UDP sockets.
18. Write a code simulating ARP /RARP protocols.
19. Study of Network simulator (NS) and Simulation of Congestion Control Algorithms usingNS.
20. Study of TCP/UDP performance using Simulation tool.
21. .Simulation of Distance Vector/ Link State Routing algorithm.

22. Performance evaluation of Routing protocols using Simulation tool.

### **22CS303 DESIGN AND ANALYSIS OF ALGORITHMS (Lab Integrated)**

1. Perform the recursive algorithm analysis.
2. Perform the non-recursive algorithm analysis
3. Write a program to search an element using binary search
4. Write a program to sort the elements using merge sort and find time complexity.
5. Write a program to sort the elements using quick sort and find time complexity.
6. Write a program to sort the elements using heap sort
7. Solve Floyd's algorithm
8. Write a program to find optimal binary search tree for a given list of keys.
9. Solve the multi-stage graph to find shortest path using backward and forward approach
10. Write a program to find the longest common subsequence
11. Write a program to find minimum spanning tree using Prim's algorithm
12. Implement Kruskal's algorithm to find minimum spanning tree
13. Write a program to solve maximum flow problem
14. Write a program to implement sum of subset problem.
15. Write a program to solve N-Queen problem
16. Solve the assignment problem using branch and bound technique
17. Solve knapsack problem using branch and bound technique

### **22CS304 OPERATING SYSTEMS (Lab Integrated)**

1. Basic Unix file system commands such as ls, cd, mkdir, rmdir, cp, rm, mv, more, lpr, man, grep, sed, etc..
2. Programs using Shell Programming.
3. Implementation of Unix System Calls.
4. Implementation of IPC using message queue
  - a. Get the input data (integer value) from a process called sender
  - b. Use Message Queue to transfer this data from sender to receiver process
  - c. The receiver does the prime number checking on the received data
  - d. Communicate the verified/status result from receiver to sender process, this status should be displayed in the Sender process
5. Write a program to implement the following actions using pthreads
  - a. Create a thread in a program and called Parent thread, this parent thread creates another thread (Child thread) to print out the numbers from 1 to 20. The Parent thread waits till the child thread finishes
  - b. Create a thread in the main program, this program passes the 'count' as arguments to that thread function and this created thread function has to print your name 'count' times.
6. Write C programs to implement the various CPU Scheduling Algorithms
7. Process Synchronization using Semaphores. A shared data has to be accessed by two

categories of processes namely A and B. Satisfy the following constraints to access the data without any data loss.

- a. When a process A1 is accessing the database another process of the same category is permitted.
- b. When a process B1 is accessing the database neither process A1 nor another process B2 is permitted.
- c. When a process A1 is accessing the database process B1 should not be allowed to access the database. Write appropriate code for both A and B satisfying all the above constraints using semaphores.

Note: The time-stamp for accessing is approximately 10 sec.

8. Bankers Algorithm for Deadlock Avoidance
9. Analysis and Simulation of Memory Allocation and Management Techniques
  - i. First Fit ii. Best Fit iii. Worst Fit
10. Implementation of Page Replacement Techniques
  - i. FIFO ii. LRU iii. Optimal page replacement
11. Simulation of File Allocation Techniques
  - i. Sequential ii. Linked list iii. indexed
12. Implementation of File Organization Strategies
  - i. Single level directory ii. Two level directory iii. Hierarchical level directory.

## **22MA401 PROBABILITY AND STATISTICS (Lab Integrated)**

1. Finding conditional probability.  
Finding mean, variance and standard deviation.
2. Finding marginal density functions for discrete random variables.
3. Calculating correlation and regression.
4. Testing of hypothesis for given data using Z - test.
5. Testing of hypothesis for given data using t - test.
6. Perform one-way ANOVA test for the given data.
7. Perform two-way ANOVA test for the given data.
8. Interpret the results for  $\bar{X}$ -Chart for variable data.
9. Interpret the results for R-Chart for variable data.

## **22CS401 DISTRIBUTED AND CLOUD COMPUTING (Lab Integrated)**

1. Implement a simple distributed program that communicates between two nodes using Java's RMI (Remote Method Invocation) API.
2. Develop a distributed program that uses Java's messaging API (JMS) to communicate between nodes. Explore the different messaging paradigms (pub/sub, point-to-point) and evaluate their performance and scalability.
3. Develop a model of a distributed program using Java's concurrency and synchronization primitives.
4. Develop a program in Java that implements vector clocks to synchronize the order of events between nodes in a distributed system.
5. Implement a snapshot algorithm for recording the global state of the distributed system using vector clocks, for both FIFO and non-FIFO channels. Test the algorithm by recording snapshots at various points in the system's execution and analyzing the resulting global state.
6. Implement Lamport's algorithm for mutual exclusion in a distributed system using Java's RMI API.
7. Develop a program in Java that implements Maekawa's algorithm for mutual exclusion in a distributed system.
8. Implement Suzuki-Kasami's broadcast algorithm in Java to achieve reliable message delivery in a distributed system.
9. Set up a virtualized data center using a hypervisor like VMware or VirtualBox and create multiple virtual machines (VMs) on it. Configure the VMs with different operating systems, resources, and network configurations, and test their connectivity and performance.
10. Deploy a containerized application on a virtual machine using Docker or Kubernetes
11. Set up and configure a single-node Hadoop cluster.
12. Run the word count program in Hadoop.
13. Deploy a microservices architecture using a container orchestration tool like Kubernetes or Docker Swarm.

## **22CS402 WEB DEVELOPMENT FRAMEWORKS (Lab Integrated)**

1. Use Spring Boot to build a Web Application
2. Create REST Service for an Education Site.
3. Build Search filter in React
4. Display a list in React
5. Create Simple Login form in React
6. Write a node.js program for making external http calls
7. Write a program in node.js to parse the given url.
8. Create a Dropdown using Angular UI bootstrap
9. Modify existing components and generating new components using Angular
10. Launching your app with Angular root module

## **ARTIFICIAL INTELLIGENCE (Lab Integrated)**

1. Implement basic search strategies – 8-Puzzle, 8 - Queens problem.
2. Implement Breadth First Search & Depth first Search Algorithm
3. Implement Water Jug problem.
4. Solve Tic-Tac-Toe problem.
5. Implement A\* and memory bounded A\* algorithms.
6. Implement Minimax algorithm & Alpha-Beta pruning for game playing.
7. Constraint Satisfaction Problem
8. Mini Project – Chess. Sudoku.
9. Implement Unification algorithm for the given logic.
10. Implement forward chaining and backward chaining using Python.
11. Implementation of object detection.
12. Implement classical planning algorithms.
13. Develop an Expert system.
14. Mini-Project – Develop Machine Learning based classification Models.

## **21CS511 NETWORKS LABORATORY**

1. Practice different network commands available in Windows and Linux Operating Systems and troubleshoot the network.
2. Network configuration commands using Linux.
3. Error detection and correction mechanisms.
4. Flow control mechanisms.
5. Multi-client chatting in TCP and UDP using Socket programming ( C / Java)
6. Implementation of HTTP, Web Caching, FTP using socket programming.
7. Develop a DNS client server to resolve the given host name or IP address.

8. Simulation of unicast routing protocols.
9. Observing Packets across the network and Performance Analysis of various Routing protocols.
10. Simulation of Transport layer Protocols and analysis of congestion control techniques in the network.

## **21CS514 ARTIFICIAL INTELLIGENCE LABORATORY**

1. Implement basic search strategies – 8-Puzzle, 8 - Queens problem, Cryptarithmic
2. Implement Breadth First Search & Depth first Search for Water Jug problem
3. Implement A\* and memory bounded A\* algorithms
4. Implement Minimax algorithm for game playing (Alpha-Beta pruning)
5. Solve Tic-Tac-Toe using Python
6. Implement Unification algorithm using Python
7. Implement Hangman game using Python
8. Implement classical planning algorithms
9. Implement forward chaining and backward chaining using Python
10. Artificial Intelligence/Expert Systems in Health care
11. Mini-Project
  - a. Sudoku
  - b. Chess.

## **21CS601 COMPILER DESIGN (Lab Integrated)**

1. Develop a lexical analyzer to recognize a few patterns in C. (Ex. identifiers, constants, comments, operators etc.). Create a symbol table, while recognizing identifiers.
2. Design a lexical analyzer for the given language. The lexical analyzer should ignore redundant spaces, tabs and new lines, comments etc.
3. Implement a Lexical Analyzer using Lex Tool
4. Design Predictive Parser for the given language
5. Implement an Arithmetic Calculator using LEX and YACC
6. Generate three address code for a simple program using LEX and YACC.
7. Implement simple code optimization techniques (Constant folding, Strength reduction and Algebraic transformation)
8. Implement back-end of the compiler for which the three address code is given as input and the 8086 assembly language code is produced as output.



## **21CS612 SECURITY LABORATORY**

1. Perform encryption, decryption using the following substitution techniques
  - (i) Ceaser cipher, (ii) playfair cipher (iii) Hill Cipher (iv) Vigenere cipher
2. Perform encryption and decryption using following transposition techniques
  - i) Rail fence ii) row & Column Transformation
3. Apply DES algorithm for practical applications.
4. Apply AES algorithm for practical applications.
5. Implement RSA Algorithm using HTML and JavaScript.
6. Implement the Diffie-Hellman Key Exchange algorithm for a given problem.
7. Calculate the message digest of a text using the SHA-1 algorithm.
8. Implement the SIGNATURE SCHEME - Digital Signature Standard.
9. Demonstrate intrusion detection system (ids) using any tool eg. Snort or any other s/w
10. Automated Attack and Penetration Tools a.Exploring N-Stalker, a Vulnerability Assessment Tool
11. Defeating Malware
  - i) Building Trojans ii) Rootkit Hunter

## **Electronics and Communication Engineering**

### **22CS101 PROBLEM SOLVING USING C++ ( Lab Integrated)**

1. Write C/C++ programs for the following:
  - a. Find the sum of individual digits of a positive integer.
  - b. Compute the GCD of two numbers.
  - c. Find the roots of a number (Newton's method)
2. Write C/C++ programs using arrays:
  - a. Find the maximum of an array of numbers.
  - b. Remove duplicates from an array of numbers.
  - c. Print the numbers in an array after removing even numbers.
3. Write C/C++ programs using strings:
  - a. Checking for palindrome.
  - b. Count the occurrences of each character in a given word
4. Generate salary slip of employees using structures and pointers. Create a structure Employee with the following members: EID, Ename, Designation, DOB, DOJ, Basic pay  
Note that DOB and DOJ should be implemented using structure within structure.
5. Compute internal marks of students for five different subjects using structures and functions.

6. Write a program Illustrating Class Declarations, Definition, and Accessing Class Members.
7. Program to illustrate default constructor, parameterized constructor and copy constructors.
8. Write a Program to Demonstrate the i) Operator Overloading. ii) Function Overloading.
9. Write a Program to Demonstrate Friend Function and Friend Class.
10. Program to demonstrate inline functions.
11. Program for Overriding of member functions.
12. Write C++ programs that illustrate how the following forms of inheritance are supported:
  - b) Single inheritance
  - b) Multiple inheritance
  - c) Multi level inheritance
  - d) Hierarchical inheritance.
13. Program to demonstrate pure virtual function implementation.
14. Count the number of account holders whose balance is less than the minimum balance using sequential access file.
15. Write a Program to Demonstrate the Catching of all Exceptions.
16. Mini project

## **22CS102 SOFTWARE DEVELOPMENT PRACTICES (Lab Integrated)**

### **List of Exercise/Experiments:**

1. Form a Team, Decide on a project:
  - a) Create a repository in GitHub for the team.
  - b) Choose and follow a Git workflow.
    - Each team member can create a StudentName.txt file with contents about themselves and the team project.
    - Each team member can create a branch, commit the file with a proper commit message and push the branch to remote GitHub repository.
    - Team members can now create a Pull request to merge the branch to master branch or main development branch.
    - The Pull request can have two reviewers, one peer team member and one faculty. Reviewers can give at least one comment for Pull Request updating.
    - Once pull request is reviewed and merged, the master or main development branch will have files created by all team members.
2. Create a web page with at least three links to different web pages. Each of the web pages is to be designed by a team member. Follow Git workflow, pull request and peer reviews.
3. Form a Team, Decide on a project:
  - e) Create a repository in GitHub for the team.
  - f) Choose and follow a Git workflow.
    - Each team member can create a StudentName.txt file with contents about themselves and the team project
    - Each team member can create a branch, commit the file with a proper commit message and push the branch to remote GitHub repository.
    - Team members can now create a Pull request to merge the branch to master branch or main development branch.

- The Pull request can have two reviewers, one peer team member and one faculty. Reviewers can give at least one comment for Pull Request updation.
  - Once pull request is reviewed and merged, the master or main development branch will have files created by all team members.
- 4. Create a web page with at least three links to different web pages. Each of the web pages is to be designed by a team member. Follow Git workflow, pull request and peer reviews.
- 5. Create web pages using the following:
  - Tables and Lists
  - Image map
  - Forms and Form elements
  - Frames
- 6. Apply Cascading style sheets for the web pages created.
- 7. Form Validation (Date, Email, User name, Password and Number validation) using JavaScript.
- 8. Implement Event Handling in the web pages.

### **Mini Projects-**

Develop any one of the following web applications (not limited to one) using above technologies.

- Online assessment system
- Ticket reservation system
- Online shopping
- Student management system
- Student result management system
- Library management
- Hospital management
- Attendance management system
- Examination automation system
- Web based chat application

### **22EC101 DIGITAL PRINCIPLES AND SYSTEMS DESIGN (Lab Integrated)**

1. Implementation of Boolean expression using logic gates
2. Design of adders
3. Design of subtractors.
4. Design of binary adder using IC7483
5. Design of Multiplexers & Demultiplexers.
6. Design of Encoders and Decoders.
7. Implementation of a Boolean function using a multiplexer
8. Design and implementation of 3-bit ripple counters.
9. Design and implementation of 3-bit synchronous counter
10. Design and implementation of shift registers

## **22CS201 DATA STRUCTURES (Lab Integrated)**

1. Array implementation of List ADTs.
2. Linked list implementation of List ADTs.
3. Array implementation of Stack and Queue ADTs.
4. Linked list implementation of Stack and Queue ADTs.
5. Applications of List – Polynomial manipulations
6. Applications of Stack – Infix to postfix conversion and expression evaluation.
7. Implementation of Binary Trees and operations of Binary Trees.
8. Implementation of Binary Search Trees.
9. Implementation of Heaps using Priority Queues
10. Graph representation and Traversal algorithms.
11. Implement searching and sorting algorithms.

## **22CS202 JAVA PROGRAMMING (Lab Integrated)**

1. Develop a Java application to generate Electricity bill. You must use one super class called EB Bill and must have two sub classes namely Domestic Bill and Commercial Bill. Create a class with the following members: Consumer no., consumer name, previous month reading, current month reading, type of EB connection (i.e domestic or commercial). Compute the bill amount using the following tariff

If the type of the EB connection is domestic, calculate the amount to be paid as follows: First 100 units - Rs. 1 per unit

101-200 units - Rs. 2.50 per unit 201 -500 units - Rs. 4 per unit

> 501 units - Rs. 6 per unit

If the type of the EB connection is commercial, calculate the amount to be paid as follows: First 100 units - Rs. 2 per unit

101-200 units - Rs. 4.50 per unit 201 -500 units - Rs. 6 per unit

> 501 units - Rs. 7 per unit

2. Arrays Manipulations: (Use Methods for implementing these in a Class)

Find kth smallest element in an unsorted array

Find the sub array with given sum

Matrix manipulations – Addition, Subtraction, Multiplication

Remove duplicate elements in an Array

3. Accept an integer value N and print the Nth digit in the integer sequence 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15 and so on till infinity.

Example: The 11th digit in the sequence 12345678910111213.... is 0.

4. Develop a Java application to implement currency converter (Dollar to INR, EURO to INR, Yen to INR and vice versa), distance converter (meter to KM, milesto KM and vice versa), time converter (hours to minutes, seconds and vice versa)using packages.

5. Develop a Java application with Employee class with Emp\_name, Emp\_id, Address, Mail\_id, Mobile\_no as

members. Inherit the classes, Programmer, Assistant Professor, Associate Professor and Professor from employee class. Add Basic Pay (BP) as the member of all the inherited classes with 97% of BP as DA, 10 % of BP as HRA, 12% of BP as PF, 0.1% of BP for staff club fund. Generate pay slips for the employees with their gross and net salary.

- 6.. Design a Java interface for ADT Stack. Implement this interface using array and built-in classes. Provide necessary exception handling in both the implementations.
7. Write a Java Program to create an abstract class named Shape that contains two integers and an empty method named print Area(). Provide three classes named Rectangle, Triangle and Circle such that each one of the classes extends the class Shape. Each one of the classes contains the methods print Area () that prints the area of the given shape and Numberofsides() that prints the number of sides of the given shape.
8. Write a Java program to apply built-in and user defined exceptions.
9. Write a Java program to read and copy the content of one file to other by handling all file related exceptions.
- 10.String Manipulation:
  - a. Reversing a set of words and count the frequency of each letter in the string.
  - b. Pattern Recognition - Find the number of patterns of form 1[0]1 where [0] represents any number of zeroes (minimum requirement is one 0) there shouldnot be any other character except 0 in the [0] sequence in a given binary string.
  - c. Remove all the occurrences of string S2 in string S1 and print the remaining.
  - d. Find the longest repeating sequence in a string
  - e. Print the number of unique string values that can be formed by rearranging the letters in the string S.
11. Write a Java program that correctly implements producer consumer problem using the concept of inter thread communication.
12. Collections:
  - a. Write a program to perform string operations using ArrayList. Write functions forthe following
    - i. Append - add at end
    - ii. Insert – add at particular index
    - iii. Search
    - iv. List all string starts with given letter
  - b. Find the frequency of words in each text.

## **22EC201 ELECTRON DEVICES AND CIRCUIT THEORY**

1. VI characteristics of PN diode
2. VI characteristics of Zener diode.
3. Input and output characteristics of CE Configuration.
4. Characteristics of JFET.
5. VI characteristics of UJT.
6. VI characteristics of SCR
7. (a) Verification of Kirchhoff's current law. 7(b). Verification of Kirchhoff's voltage law..
8. Verification of superposition theorem.
9. Verification of Thevenin's theorem.

10. Verification of Norton's theorem.

### **22MA301 STATISTICS AND LINEAR ALGEBRA (Lab Integrated)**

1. Testing of hypothesis for given data using Z - test.
2. Testing of hypothesis for given data using t - test.
3. Perform one way ANOVA test for the given data.
4. Perform two way ANOVA test for the given data.
5. Interpret the results for  $\bar{X}$ -Chart for variable data.
6. Interpret the results for R-Chart for variable data.
7. Plot the vector subspace in 3-dimensional space.
8. Compute the null space of the matrix.
9. Write Matrix representation of linear transformations

### **22EC301 SIGNALS AND SYSTEMS (Lab Integrated)**

1. Generation of Continuous time and Discrete Time signals.
2. Perform amplitude-scaling and time-shifting on a given signal.
3. Compute the even and odd parts of a given signal
4. Compute the Fourier transform of CT signals.
5. Compute the Laplace transform of CT signals.
6. Perform convolution of signals using Fourier transform.
7. Compute the Z transform of causal signals.
8. Compute Linear convolution (Convolution Sum) of the given two sequences.
9. Simulate the impulse response of a system from its difference equation.
10. Find poles and zeros of Z domain signals and sketch the pole zero plot.

### **22CS301 PROBLEM SOLVING AND PYTHONPROGRAMMING ( Lab Integrated)**

1. Compute the GCD of two numbers.
2. Find the square root of a number (Newton's method)
3. First n prime numbers
4. String manipulation
  - a. Get a string from a given string where all occurrences of its first char have been changed to '\$', except the first char itself
  - b. Python function that takes a list of words and returns the length of the longest one
  - c. Python program to remove the characters which have odd index values of a given string
  - d. Python program to count the occurrences of each word in a given sentence.

- e. Python program that accepts a comma separated sequence of words as input and prints the unique words in sorted form
  - f. Python function to reverse a string if its length is a multiple of 4
5. Operations on Tuples:
- i. finding repeated elements
  - ii. slice a tuple
  - iii. reverse a tuple
  - iv. replace last value of a tuple
6. List operations
- a. Find the maximum of a list of numbers
  - b. Python program to remove duplicates from a list.
  - c. Python program to get the smallest number from a list.
  - d. Python program to print a specified list after removing the 0th, 4th and 5th elements.
  - e. Python program to print the numbers of a specified list after removing even numbers from it.
  - f. Python program to find the second smallest number in a list.
7. Linear search and Binary search
8. Selection sort, Insertion sort
9. Merge sort
10. Multiply matrices
11. Programs that take command line arguments (word count)
12. Find the most frequent words in a text read from a file
13. Simulate elliptical orbits in Pygame
14. Simulate bouncing ball using Pygame

## **22EC302 ANALOG ELECTRONICS ( Lab Integrated)**

- 1. Analysis of Fixed Bias and Self Bias circuit
- 2. Darlington Amplifier
- 3. BJT Cascode/Cascade amplifiers using PSPICE
- 4. MOSFET characteristics using PSPICE

5. Frequency response of CE amplifier
6. Frequency response of CS amplifier using PSPICE
7. Feedback Amplifier
8. Oscillator using PSPICE
9. Class B and Class C Tuned Amplifier
10. Class A Amplifier using PSPICE

## **22EC401 CONTROL ENGINEERING**

1. Determine the transfer function of the given closed loop system using MATLAB
2. Implement unity and non-unity feedback system using MATLAB.
3. Estimate the unit step response of the given transfer function and determine its time domain parameters using MATLAB.
4. Determine the steady state error of the given transfer function using MATLAB.
5. Simulate P, PD, PI, PID controller and verify by using hardware.
6. Perform stability analysis of a given transfer function using gain and phase margins estimated by the Bode plot using MATLAB.
7. Estimate the relative stability of a given transfer function using gain and phase margins estimated by the Polar plot using MATLAB.
8. Sketch the root locus of the given transfer function and locate the closed loop poles for different values of open loop gain (K) using MATLAB.
9. Construct the State space model for the classical transfer function using MATLAB.
10. Perform analytical study of water flow measurement using flow meter.

## **22MA401 PROBABILITY AND RANDOM PROCESSES**

1. Finding probability of DRV and CRV.
2. Finding mean, variance and MGF.
3. Using distributions to find probability value.
4. Determine mean values using regression.
5. Solving correlation problems
6. Finding covariance.
7. Determine asymptotic behaviour of Markov chain.
8. Solving Poisson process problems.
9. To test the stationary of a random process
10. Calculating auto correlation.
11. Finding PSD of a signal.
12. To estimate cross spectral density.



13. Construct linear time invariant models.
14. Problem with phase of a transfer function.
15. Create random input signal

## **22EC402 LINEAR INTEGRATED CIRCUITS (Lab Integrated)**

### **Design and Testing of**

1. Inverting, Non inverting amplifier, Differential amplifiers.
2. Integrator , Differentiator, Schmitt Trigger using Op-amp.
3. Instrumentation amplifier using Op-amp - PSPICE
4. Active low-pass, High-pass and band-pass filters – PSPICE
5. R-2R Ladder Type D- A Converter using Op-amp – PSPICE
6. PLL Characteristics IC565
7. Frequency Synthesizer using IC 565.
8. Phase shift and Wien bridge oscillators using Op-amp.
9. Voltage regulator-IC723
10. As table and Mono stable multi vibrators using NE555 Timer - PSPICE

## **22EC403 ANALOG AND DIGITAL COMMUNICATION (Lab Integrated)**

1. AM Modulator and Demodulator
2. FM Modulator and Demodulator.
3. Signal Sampling and reconstruction
4. Pulse Code Modulation and Demodulation
5. Delta Modulation and Demodulation
6. Simulation of ASK, FSK, and BPSK generation schemes
7. Simulation of ASK, FSK and BPSK detection schemes
8. Simulation of QPSK and QAM generation schemes
9. Simulation of signal constellations of BPSK, QPSK and QAM
10. Simulation of Linear Block

### **21EC503 VLSI DESIGN (LAB INTEGRATED)**

1. Basic CMOS gates
2. Half Adder, Full Adder, Half Subtractor & Full Subtractor using the Cells developed in Expt. No. 1
3. CMOS Latches & Flip Flops using the Cells developed in Expt. No. 1
4. Design of 4-Bit Synchronous Up/Down Counter & 4-Bit Ripple Counter
5. Design of 4-Bit Universal Shift Register
6. Design of Moore and Mealy FSMs
7. Design of 4-Bit Ripple Carry Adder & 4-Bit Carry Look Ahead Adder
8. Design of 4-Bit ALU
9. Design of 4-Bit Array Multiplier & 4-Bit Booth Multiplier
10. Bit Synchronous Up/Down Counter using the Cells developed in Expt. No. 3

### **21EC511 COMMUNICATION SYSTEMS LABORATORY**

1. Signal Sampling and reconstruction
2. Time Division Multiplexing
3. AM Modulator and Demodulator
4. FM Modulator and Demodulator
5. Pulse Code Modulation and Demodulation
6. Delta Modulation and Demodulation
7. Line coding schemes
8. Simulation of ASK, FSK, and BPSK generation and detection schemes
9. Simulation of DPSK, QPSK and QAM generation schemes
10. Simulation of signal constellations of BPSK, QPSK and QAM
11. Simulation of Linear Block and Cyclic error control coding schemes
12. Simulation of Convolutional coding scheme

### **21EC603 EMBEDDED SYSTEMS (LAB INTEGRATED)**

1. Study of ARM Microcontroller and Evaluation system.
2. Interfacing LEDs and stepper motor
3. Interfacing LED and PWM using ARM
4. Interfacing UART and I2C
5. Interfacing ADC using ARM.
6. Interfacing DAC using ARM.
7. Introduction to Free RTOS.org.

8. Creating multiple tasks and perform Scheduling algorithm using Free RTOS.
9. Interfacing real time clock and serial port.
10. Interfacing keyboard and LCD

## **21EC611 DIGITAL SIGNAL PROCESSING LABORATORY**

### **MATLAB / EQUIVALENT SOFTWARE PACKAGE**

1. Generation of elementary Discrete-Time sequences.
2. Linear and Circular convolutions.
3. Auto correlation and Cross Correlation
4. Frequency Analysis using DFT.
5. Design of FIR filters (LPF/HPF/BPF/BSF) and demonstrates the filtering operation.
6. Design of Butterworth and Chebyshev IIR filters (LPF/HPF/BPF/BSF) and demonstrate the filtering operations.

### **DSP PROCESSOR BASED IMPLEMENTATION**

7. Study of architecture of Digital Signal Processor.
8. Perform MAC operation using various addressing modes.
9. Generation of various signals and random noise.
10. Design and demonstration of FIR and IIR Filters.

## **21EC701 RF AND MICROWAVE ENGINEERING (Lab Integrated)**

1. Radiation Pattern of Horn Antenna
2. Study of Microwave Filter Characteristics
3. E Plane Tee
4. H Plane Tee
5. Magic Tee
6. Characterization of Directional Couplers
7. Gunn Diode Characteristics
8. Isolator
9. Circulators
10. VSWR and Impedance Measurement

## **21EC702 OPTICAL COMMUNICATION AND NETWORKS (Lab Integrated)**

1. Mode characteristics of fibers.
2. DC characteristics of LED and PIN photo diode.
3. Measurement of connector loss
4. Bending loss
5. Numerical aperture
6. Fiber optic analog frequency response (analog)

7. Fiber optic digital link characterization
8. Attenuation Loss
9. Eye Diagram
10. Measurement of BER

## **21EC712 DESIGN THINKING LABORATORY**

1. Design a mind map of design thinking process
2. Construct empathy maps for a given case study-1
3. Develop customer journey map for a given case-1
4. Construct empathy maps for a given case study-2
5. Develop customer journey map for a given case -2
6. Ideation Technique Set 1 - Thirty circle Exercise and others
7. Ideation Technique Set 2 – Brainstorming and others
8. Prepare a prototype for the case-1, a toothpick bridge (mock-up model)
9. Test the prototype of the case-1, the toothpick bridge
10. Prepare a prototype for the case-2, a marble maze (mock up model)
11. Test the prototype of the case-2, the marble maze
12. Prepare a prototype for the case-3, an electronic system (mock up model)
13. Test the prototype of the case-3, the electronic system
14. Design thinking using sprintbase software - I
15. Design thinking using sprintbase software - II

# Information Technology

## 22CS101 PROBLEM SOLVING USING C++ ( Lab Integrated)

1. Write C/C++ programs for the following:
  - a. Find the sum of individual digits of a positive integer.
  - b. Compute the GCD of two numbers.
  - c. Find the roots of a number (Newton's method)
2. Write C/C++ programs using arrays:
  - a. Find the maximum of an array of numbers.
  - b. Remove duplicates from an array of numbers.
  - c. Print the numbers in an array after removing even numbers.
3. Write C/C++ programs using strings:
  - a. Checking for palindrome.
  - b. Count the occurrences of each character in a given word
4. Generate salary slip of employees using structures and pointers. Create a structure Employee with the following members: EID, Ename, Designation, DOB, DOJ, Basic pay  
  
Note that DOB and DOJ should be implemented using structure within structure.
5. Compute internal marks of students for five different subjects using structures and functions.
6. Write a program Illustrating Class Declarations, Definition, and Accessing Class Members.
7. Program to illustrate default constructor, parameterized constructor and copy constructors.
8. Write a Program to Demonstrate the i) Operator Overloading. ii) Function Overloading.
9. Write a Program to Demonstrate Friend Function and Friend Class.
10. Program to demonstrate inline functions.
11. Program for Overriding of member functions.
12. Write C++ programs that illustrate how the following forms of inheritance are supported:
  - c) Single inheritance b) Multiple inheritance c) Multi level inheritance d) Hierarchical inheritance.
13. Program to demonstrate pure virtual function implementation.
14. Count the number of account holders whose balance is less than the minimum balance using sequential access file.
15. Write a Program to Demonstrate the Catching of all Exceptions.
16. Mini project

## 22CS102 SOFTWARE DEVELOPMENT PRACTICES (Lab Integrated)

### List of Exercise/Experiments:

1. Form a Team, Decide on a project:
  - c) Create a repository in GitHub for the team.
  - d) Choose and follow a Git workflow.
    - Each team member can create a StudentName.txt file with contents about themselves and the team project.
    - Each team member can create a branch, commit the file with a proper commit message and push the branch to remote GitHub repository.
    - Team members can now create a Pull request to merge the branch to master branch or main development branch.
    - The Pull request can have two reviewers, one peer team member and one faculty. Reviewers can give at least one comment for Pull Request updating.
    - Once pull request is reviewed and merged, the master or main development branch will have files created by all team members.
2. Create a web page with at least three links to different web pages. Each of the web pages is to be designed by a team member. Follow Git workflow, pull request and peer reviews.
3. Form a Team, Decide on a project:
  - g) Create a repository in GitHub for the team.
  - h) Choose and follow a Git workflow.
    - Each team member can create a StudentName.txt file with contents about themselves and the team project
    - Each team member can create a branch, commit the file with a proper commit message and push the branch to remote GitHub repository.
    - Team members can now create a Pull request to merge the branch to master branch or main development branch.
    - The Pull request can have two reviewers, one peer team member and one faculty. Reviewers can give at least one comment for Pull Request updating.
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4. Create a web page with at least three links to different web pages. Each of the web pages is to be designed by a team member. Follow Git workflow, pull request and peer reviews.
5. Create web pages using the following:
  - Tables and Lists
  - Image map
  - Forms and Form elements
  - Frames
6. Apply Cascading style sheets for the web pages created.
7. Form Validation (Date, Email, User name, Password and Number validation) using JavaScript.
8. Implement Event Handling in the web pages.

### Mini Projects-

Develop any one of the following web applications (not limited to one) using above technologies.

- Online assessment system
- Ticket reservation system
- Online shopping
- Student management system
- Student result management system

- Library management
- Hospital management
- Attendance management system
- Examination automation system
- Web based chat application

## **22EC101 DIGITAL PRINCIPLES AND SYSTEMS DESIGN (Lab Integrated)**

1. Implementation of Boolean expression using logic gates
2. Design of adders
3. Design of subtractors.
4. Design of binary adder using IC7483
5. Design of Multiplexers & Demultiplexers.
6. Design of Encoders and Decoders.
7. Implementation of a Boolean function using a multiplexer
8. Design and implementation of 3-bit ripple counters.
9. Design and implementation of 3-bit synchronous counter
10. Design and implementation of shift registers

## **22CS201 DATA STRUCTURES (Lab Integrated)**

1. Array implementation of List ADTs.
2. Linked list implementation of List ADTs.
3. Array implementation of Stack and Queue ADTs.
4. Linked list implementation of Stack and Queue ADTs.
5. Applications of List – Polynomial manipulations
6. Applications of Stack – Infix to postfix conversion and expression evaluation.
7. Implementation of Binary Trees and operations of Binary Trees.
8. Implementation of Binary Search Trees.
9. Implementation of Heaps using Priority Queues
10. Graph representation and Traversal algorithms.
11. Implement searching and sorting algorithms.

## 22CS202 JAVA PROGRAMMING (Lab Integrated)

1. Develop a Java application to generate Electricity bill. You must use one super class called EB Bill and must have two sub classes namely Domestic Bill and Commercial Bill. Create a class with the following members: Consumer no., consumer name, previous month reading, current month reading, type of EB connection (i.e domestic or commercial). Compute the bill amount using the following tariff

If the type of the EB connection is domestic, calculate the amount to be paid as follows: First 100 units - Rs. 1 per unit

101-200 units - Rs. 2.50 per unit 201 -500 units - Rs. 4 per unit

> 501 units - Rs. 6 per unit

If the type of the EB connection is commercial, calculate the amount to be paid as follows: First 100 units - Rs. 2 per unit

101-200 units - Rs. 4.50 per unit 201 -500 units - Rs. 6 per unit

> 501 units - Rs. 7 per unit

2. Arrays Manipulations: (Use Methods for implementing these in a Class)

Find kth smallest element in an unsorted array

Find the sub array with given sum

Matrix manipulations – Addition, Subtraction, Multiplication

Remove duplicate elements in an Array

3. Accept an integer value N and print the Nth digit in the integer sequence 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15 and so on till infinity.

Example: The 11th digit in the sequence 12345678910111213.... is 0.

4. Develop a Java application to implement currency converter (Dollar to INR, EURO to INR, Yen to INR and vice versa), distance converter (meter to KM, milesto KM and vice versa), time converter (hours to minutes, seconds and vice versa)using packages.

5. Develop a Java application with Employee class with Emp\_name, Emp\_id, Address, Mail\_id, Mobile\_no as members. Inherit the classes, Programmer, Assistant Professor, Associate Professor and Professor from employee class. AddBasic Pay (BP) as the member of all the inherited classes with 97% of BP as DA, 10 % of BP as HRA, 12% of BP as PF, 0.1% of BP for staff club fund. Generate pay slips for the employees with their gross and net salary.

6.. Design a Java interface for ADT Stack. Implement this interface using array and built-in classes. Provide necessary exception handling in both the implementations.

7. Write a Java Program to create an abstract class named Shape that contains two integers and an empty method named print Area(). Provide three classes named Rectangle, Triangle and Circle such that each one of the classes extends the class Shape. Each one of the classes contains the methods print Area () that prints the area of the given shape and Numberofsides() that prints the number of sides of the given shape.

8. Write a Java program to apply built-in and user defined exceptions.

9. Write a Java program to read and copy the content of one file to other by handling all file related exceptions.

10.String Manipulation:

a. Reversing a set of words and count the frequency of each letter in the string.

b. Pattern Recognition - Find the number of patterns of form 1[0]1 where [0] represents any number of zeroes (minimum requirement is one 0) there shouldnot be any other character except 0 in the [0] sequence in a given binary string.

c. Remove all the occurrences of string S2 in string S1 and print the remaining.



- d. Find the longest repeating sequence in a string
  - e. Print the number of unique string values that can be formed by rearranging the letters in the string S.
11. Write a Java program that correctly implements producer consumer problem using the concept of inter thread communication.
12. Collections:
- a. Write a program to perform string operations using ArrayList. Write functions for the following
    - i. Append - add at end
    - ii. Insert – add at particular index
    - iii. Search
    - iv. List all string starts with given letter
  - b. Find the frequency of words in each text.

## **22IT202 DATABASE MANAGEMENT SYSTEM (Lab Integrated)**

1. Data Definition Commands, Data Manipulation Commands for inserting deleting, updating and retrieving Tables and Transaction Control statements Write programs using the following system calls of UNIX operating system fork, exec, getpid, exit, wait, close, stat, opendir, readdir
2. Database Querying – Simple queries, Nested queries, Sub queries and Joins
3. Views, Sequences, Synonyms
4. Database Programming: Implicit and Explicit Cursors.
5. Procedures and Functions
6. Triggers
7. Exception Handling
8. Database Design using ER modeling, normalization and Implementation for any application
9. Database Connectivity with Front End Tools
10. Case Study using real life database applications anyone from the following list
  - Inventory Management for a EMart Grocery Shop
  - Society Financial Management
  - Cop Friendly App – Eseva
  - Property Management – eMall
  - Star Small and Medium Banking and Finance
11. Build Entity Model diagram. The diagram should align with the business and functional goals stated in the application.
12. Apply Normalization rules in designing the tables in scope.
13. Prepared applicable views, triggers (for auditing purposes), functions for enabling enterprise grade features.
14. Build PL SQL / Stored Procedures for Complex Functionalities, ex EOD Batch Processing for calculating the EMI for Gold Loan for each eligible Customer.
15. Ability to showcase ACID Properties with sample queries with appropriate settings

## 22CS301 ADVANCE JAVA PROGRAMMING (Lab Integrated)

1. Write Java programs to implement Queue interface
  - Demonstrate the working of Dequeue.
  - Demonstrate the working of ArrayQueue.
2. Write Java programs using Utilities
  - String Processing.
  - Date Manipulation.
3. Write Java programs using Comparable & Observer
4. Write a Java program to display a string in a frame
5. Write a Java program to change the color, font name, style and size of a string
6. Write a Java program to demonstrate mouse events
7. Write a Java program to demonstrate adapter classes
8. Write a Java program to design a calculator
9. Write a Java program to demonstrate event handlingCount the occurrences of each character in a given word.
10. Write programs in Java using Servlets:
  - To invoke servlets from HTML forms
  - Session tracking.
11. Create a dynamic web application using Servlet/JSP with a facility to
  - Login to the application
  - Register a new user and
  - Change password for an existing user
12. i. Validate the form using PHP regular expression.
13. ii. PHP stores a form data into database.
14. Write a web service for finding public review about a consumer product.
15. Write a web service for finding what people think by asking 500 people\_ consumer product.
16. Mini Projects-Develop any one of the following web applications using above technologies.
  - Online assessment system
  - Ticket reservation system
  - Online shopping
17. Simulation of DNS using UDP sockets.
18. Write a code simulating ARP /RARP protocols.
19. Study of Network simulator (NS) and Simulation of Congestion Control Algorithms using NS.
20. Study of TCP/UDP performance using Simulation tool.
21. .Simulation of Distance Vector/ Link State Routing algorithm.

22. Performance evaluation of Routing protocols using Simulation tool.

### **22CS303 DESIGN AND ANALYSIS OF ALGORITHMS (Lab Integrated)**

1. Perform the recursive algorithm analysis.
2. Perform the non-recursive algorithm analysis
3. Write a program to search an element using binary search
4. Write a program to sort the elements using merge sort and find time complexity.
5. Write a program to sort the elements using quick sort and find time complexity.
6. Write a program to sort the elements using heap sort
7. Solve Floyd's algorithm
8. Write a program to find optimal binary search tree for a given list of keys.
9. Solve the multi-stage graph to find shortest path using backward and forward approach
10. Write a program to find the longest common subsequence
11. Write a program to find minimum spanning tree using Prim's algorithm
12. Implement Kruskal's algorithm to find minimum spanning tree
13. Write a program to solve maximum flow problem
14. Write a program to implement sum of subset problem.
15. Write a program to solve N-Queen problem
16. Solve the assignment problem using branch and bound technique
17. Solve knapsack problem using branch and bound technique

### **22CS304 OPERATING SYSTEMS (Lab Integrated)**

1. Basic Unix file system commands such as ls, cd, mkdir, rmdir, cp, rm, mv, more, lpr, man, grep, sed, etc..
2. Programs using Shell Programming.
3. Implementation of Unix System Calls.
4. Implementation of IPC using message queue
  - e. Get the input data (integer value) from a process called sender
  - f. Use Message Queue to transfer this data from sender to receiver process
  - g. The receiver does the prime number checking on the received data
  - h. Communicate the verified/status result from receiver to sender process, this status should be displayed in the Sender process
5. Write a program to implement the following actions using pthreads
  - c. Create a thread in a program and called Parent thread, this parent thread creates another thread (Child thread) to print out the numbers from 1 to 20. The Parent thread waits till the child thread finishes
  - d. Create a thread in the main program, this program passes the 'count' as arguments to that thread function and this created thread function has to print your name 'count' times.
6. Write C programs to implement the various CPU Scheduling Algorithms
7. Process Synchronization using Semaphores. A shared data has to be accessed by two

categories of processes namely A and B. Satisfy the following constraints to access the data without any data loss.

- d. When a process A1 is accessing the database another process of the same category is permitted.
- e. When a process B1 is accessing the database neither process A1 nor another process B2 is permitted.
- f. When a process A1 is accessing the database process B1 should not be allowed to access the database. Write appropriate code for both A and B satisfying all the above constraints using semaphores.

Note: The time-stamp for accessing is approximately 10 sec.

- 8. Bankers Algorithm for Deadlock Avoidance
- 9. Analysis and Simulation of Memory Allocation and Management Techniques
  - i. First Fit ii. Best Fit iii. Worst Fit
- 10. Implementation of Page Replacement Techniques
  - i. FIFO ii. LRU iii. Optimal page replacement
- 11. Simulation of File Allocation Techniques
  - i. Sequential ii. Linked list iii. indexed
- 12. Implementation of File Organization Strategies
  - i. Single level directory ii. Two level directory iii. Hierarchical level directory

## **22MA401 PROBABILITY AND STATISTICS (Lab Integrated)**

- 1. Finding conditional probability.
  - Finding mean, variance and standard deviation.
- 2. Finding marginal density functions for discrete random variables.
- 3. Calculating correlation and regression.
- 4. Testing of hypothesis for given data using Z - test.
- 5. Testing of hypothesis for given data using t - test.
- 6. Perform one-way ANOVA test for the given data.
- 7. Perform two-way ANOVA test for the given data.
- 8. Interpret the results for  $\bar{X}$ -Chart for variable data.
- 9. Interpret the results for R-Chart for variable data.

## **22IT401 ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING ( Lab Integrated)**

1. Implementation of uninformed search algorithm (BFS and DFS).
2. Implementation of Informed Search algorithm (A\* and Hill Climbing Algorithm)
3. Implementation of forward and backward chaining.
4. Implementation of unification algorithms.
5. Numpy Operations
6. NumPy arrays
7. NumPy Indexing and Selection
8. NumPy Exercise:
  - (i) Write code to create a 4x3 matrix with values ranging from 2 to 13.
  - (ii) Write code to replace the odd numbers by -1 in the following array.
  - (iii) Perform the following operations on an array of mobile phones prices 6999, 7500, 11999, 27899, 14999, 9999.
    - a) Create a 1d-array of mobile phones prices
    - b) Convert this array to float type
    - c) Append a new mobile having price of 13999 Rs. to this array
    - d) Reverse this array of mobile phones prices
    - e) Apply GST of 18% on mobile phones prices and update this array.
    - f) Sort the array in descending order of price
    - g) What is the average mobile phone price.
9. Build linear regression models to predict housing prices using python , using data set available Google colabs.
10. Stock Ensemble-based Neural Network for Stock Market Prediction using Historical Stock Data and Sentiment Analysis.

### **Use Cases**

Case Study 1: Churn Analysis and Prediction (Survival Modelling) Cox-proportional models

Churn Prediction

Case Study 2: Credit card Fraud Analysis Imbalanced Data

Neural Network.

Case study 3: Sentiment Analysis or Topic Mining from New York Times Similarity measures (Cosine Similarity, Chi-Square, N Grams)

Part-of-Speech Tagging Stemming and Chunking

Case Study 4: Sales Funnel Analysis A/B testing

Campaign effectiveness, Web page layout effectiveness Scoring and Ranking

Case Study 5: Recommendation Systems and Collaborative filtering User based

Item Based

Singular value decomposition–based recommenders Case Study 6: Customer Segmentation and

Value Segmentation Strategies

Lifetime Value

Case Study 7: Portfolio Risk Conformance Risk Profiling

Portfolio Optimization

Case Study 8: Uber Alternative Routing Graph Construction

Route Optimization

## **22IT402 COMPUTER ARCHITECTURE AND MICROPROCESSORS ( Lab Integrated)**

1. To familiarize the use of QtSPIM simulator
2. To use basic instructions of MIPS to understand various addressing modes using QtSPIM simulator.
3. To perform basic addition, subtraction, multiplication, and division programs in QtSPIM simulator using MIPS instructions.
4. To perform floating point addition and multiplication in QtSPIM simulator using MIPS instructions
5. Design an 8-bit ALU using MODELSIM
6. To implement Verilog code for 16-bit Single-Cycle MIPS processor
7. Simulating cache read/write using Paracache simulator.
8. Learning address translation in virtual memory system using Paracache simulator.
9. Write and execute 8086 ALP for performing Addition, Subtraction, Multiplication and division of two 8-bit numbers using 8086 Emulator.
10. Write and execute 8086 ALP for reversing the given number using 8086 Emulator

## **22IT403 WEB DEVELOPMENT FRAMEWORKS ( Lab Integrated)**

1. Create a JS Object for Bank Account (w attributes like à customer name, account type, balance, data of creation, bank name, branch name, pan card number). Using JS Object keyword, try to perform following activities
  - a. List down all the entries of the bank object
  - b. Check the existence of a key
  - c. If key found, get the value for the key
2. Spread Operator
  - a. Merge Customer and Account Arrays
  - b. Update the Customer Object with the new values
  - c. Develop a function that takes an Spread Argument and calculates total balance.
3. 1Create a list of Bank Objects (same kind of object you used in above lab, but in a array format)
  - ☐ Display the banks where balance is greater than 200
  - ☐ deduct 10% of the Bank account balance, as part of monthly service fees
  - ☐ Display the banks where balance is greater than 200 and branch code is “Chennai”

- ☐ Add a new Bank to the given array
  - ☐ Delete a bank from the array (use splice operator)
  - ☐ Calculate the total balance of all bank accounts
4. Develop a Scientific calculator that does following operations
- ☐ Rounded Value
  - ☐ Area of Circle
  - ☐ Calculating of Sin, Cos and Tan functions
  - ☐ Perimeter of a Rectangle
  - ☐ Employ Arrow functions
  - ☐ Employ HOC
5. Create a collection of Customer by using
- i. Weak Map and Map Collection in JS
  - ii. Show Case the different feature set of the same.
6. Add Login Page, Dash Board Page, Admin Page
- i. Enable React Routing
  - ii. Add React Protected Route, for authorization
7. Develop a React application that has User Registration Form w field level validations, data submission to a rest api end point, bootstrap for responsive.
- Use YUP or Formik to implement the same
8. Employ back end api for Login Page functionality (authentication). Post login, store the user context (received from the back end server) in browser's session storage as objects. And use the same as creds during protected route verification
- On the dashboard page, have a grid of Students. The data has to be fetched from back end API
  - Employ useRef, useEffect & useState, and useHistory
  - Enable Exception Handling
  - Enable HOC and Aux Components
  - Implement React-Testing Library

#### Business Use Case Implementations

- Student Management System
- Retail Bank System
- eCommerce System
- Student LMS Management System

## **22IT404 APPLICATION SYSTEM DESIGN WITH UML ( lab Integrated)**

1. Develop Problem statement for software System
2. Document the Software Requirements Specification (SRS) for the identified system
3. Identify use cases and develop the Use Case model.
4. Identify the conceptual classes and develop Class Diagram
5. Using the identified scenarios, find the interaction between objects and represent them using UML Sequence Diagram
6. . Draw relevant State Chart and Activity Diagrams for the same system.
7. Develop UML Component and Deployment diagram
8. Evaluate the different pattern interactions between various physical components and the user, managing a design solution through visual representations

### **To develop a mini-project by using the following Use Cases listed below:**

#### Use Case 1

POS (Point of Sale) Terminal Features to be handled:-

1. Order Entry,
2. Item Management and Categorization,
3. Tax Calculation,
4. Payment Mode, Payment Status, User Management

#### Use Case 2

Hotel Room Management Features to be handled:-

1. Rooms type and Category
2. Check in and Check Out
3. Room occupation Status
4. Room Service Request
5. Guests Management and allocation Room
6. Billing Calculation, User management

#### Use Case 3 Banking Portal

1. Funds Transfer within Same Bank, Intra Bank
2. Forex Conversion
3. Bene Management
4. Customer and Accounts Management
5. Funds Transfer Transaction Status

#### Use Case 4

Mobile Phone Service Center

1. Mobile Phone Parts Management



2. Mobile Phone Models
  3. Service Request Registration
  4. Service Request Status Check
  5. Service Request Engineer Allocation
  6. Payment
- Customer Management

## **21EC441 MICROPROCESSORS AND INTERFACING (LAB INTEGRATED)**

### **. 8086 Programs**

1. Basic arithmetic and Logical operations
2. Move a data block without overlap
3. Code conversion and decimal arithmetic.
4. Sorting and searching

### **8051 Experiments**

5. Basic arithmetic and Logical operations
6. Square and Cube program
7. Find 2's complement of a number
8. Unpacked BCD to ASCII

### **Interfacing Experiments of 8086 and 8051**

9. Traffic light controller
10. Key board and Display - 8279
11. Programmable Timer - 8253/8254
12. Programmable peripheral Interface - 8255
13. A/D and D/A interface

Stepper motor control

## **21CS511 NETWORKS LABORATORY**

1. Practice different network commands available in Windows and Linux Operating Systems and troubleshoot the network.
2. Network configuration commands using Linux.
3. Error detection and correction mechanisms.
4. Flow control mechanisms.
5. Multi-client chatting in TCP and UDP using Socket programming ( C / Java)
6. Implementation of HTTP, Web Caching, FTP using socket programming.
7. Develop a DNS client server to resolve the given host name or IP address.
8. Simulation of unicast routing protocols.

9. Observing Packets across the network and Performance Analysis of various Routing protocols.
10. Simulation of Transport layer Protocols and analysis of congestion control techniques in the network.

## **21IT511 OBJECT ORIENTED SYSTEMS DESIGN LABORATORY**

1. To develop a mini-project by using the following Use Cases listed below.
2. Use Case 1
3. POS (Point of Sale) Terminal
4. Features to be handled:-
5. Order Entry,
6. Item Management and Categorization,
7. Tax Calculation,
8. Payment Mode, Payment Status, User Management
9. Use Case 2
10. Hotel Room Management
11. Features to be handled:-
12. Rooms type and Category
13. Check in and Check Out
14. Room occupation Status
15. Room Service Request
16. Guests Management and allocation Room
17. Billing Calculation, User management
18. Use Case 3
19. Banking Portal
20. Funds Transfer within Same Bank, Intra Bank
21. Forex Conversion
22. Bene Management
23. Customer and Accounts Management
24. Funds Transfer Transaction Status
25. Use Case 4
26. Mobile Phone Service Center
27. Mobile Phone Parts Management
28. Mobile Phone Models
29. Service Request Registration
30. Service Request Status Check
31. Service Request Engineer Allocation

32. Payment

33. Customer Management

## **21CS611 MOBILE APPLICATION DEVELOPMENT LABORATORY**

1. Simulate Mobile Routing Protocols using Network simulators.
2. Develop an application that uses the following features:
3. GUI components, Font and Colours
4. Layout Managers and event listeners.
5. Graphical primitives on the screen.
6. Develop an application that makes use of databases.
7. Develop an application that makes use of Notification Manager
8. Implement an application that uses Multi-threading.
9. Develop a native application that uses GPS location information
10. Implement an application that writes data to the SD card.
11. Implement an application that creates an alert upon receiving a message
12. Write a mobile application that makes use of RSS feed
13. Develop a mobile application to send an email.
14. Develop a simple Mobile application that uses data from sensors like GPS, proximity, bluetooth, etc. (Mini Project).

## **21CS701 CLOUD COMPUTING LABORATORY**

1. Install Virtualbox/VMware Workstation with different flavours of linux or windows OS on top of windows7 or 8.
2. Install a C compiler in the virtual machine created using virtual box and execute Simple Programs
3. Install Google App Engine. Create hello world app and other simple web applications using python/java.
4. Use GAE launcher to launch the web applications.
5. Simulate a cloud scenario using CloudSim and run a scheduling algorithm that is not present in CloudSim.
6. Find a procedure to transfer the files from one virtual machine to another virtual machine.
7. Find a procedure to launch virtual machine using trystack (Online Openstack Demo Version)
8. Install Hadoop single node cluster and run simple applications like wordcount..

# **Computer Science and Business Systems**

## **GE8161 PROBLEM SOLVING AND PYTHON PROGRAMMING LABORATORY**

9. Compute the GCD of two numbers.
10. Find the square root of a number (Newton's method)
11. Exponentiation (power of a number)
12. Find the maximum of a list of numbers
13. Linear search and Binary search
14. Selection sort, Insertion sort
15. Merge sort
16. First n prime numbers
17. Multiply matrices
18. Programs that take command line arguments (word count)
19. Find the most frequent words in a text read from a file
20. Simulate elliptical orbits in Pygame
21. Simulate bouncing ball using Pygame

## **AD8261 DATA STRUCTURES DESIGN LABORATORY**

1. Implement simple ADTs as Python classes
2. Implement recursive algorithms in Python
3. Implement List ADT using Python arrays
4. Linked list implementations of List
5. Implementation of Stack and Queue ADTs
6. Applications of List, Stack and Queue ADTs
7. Implementation of sorting and searching algorithms
8. Implementation of Hash tables
9. Tree representation and traversal algorithms
10. Implementation of Binary Search Trees
11. Implementation of Heaps
12. Graph representation and Traversal algorithms
13. Implementation of single source shortest path algorithm
14. Implementation of minimum spanning tree algorithms

## **CW8311 BUSINESS COMMUNICATION AND VALUE SCIENCE LAB-1**

1. Different forms of words
2. Technical terminology
3. Interpersonal Skills: Dialogue & Conversation

4. Job Application
5. Letters & Reports
6. SWOT analysis
7. Socio cultural & Cross-cultural understanding
8. Women in all spheres
9. Team vs Group
10. Conflict management
11. Acquiring Leadership traits
12. Human values and Corporate culture

## CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY

1. Develop a Java application to generate Electricity bill. Create a class with the following members:

Consumer no., consumer name, previous month reading, current month reading, type of EB connection (i.e domestic or commercial). Compute the bill amount using the following tariff.

If the type of the EB connection is domestic, calculate the amount to be paid as follows:

□□ First 100 units - Rs. 1 per unit

□□ 101-200 units - Rs. 2.50 per unit

□□ 201 -500 units - Rs. 4 per unit

□□ > 501 units - Rs. 6 per unit

If the type of the EB connection is commercial, calculate the amount to be paid as follows:

□□ First 100 units - Rs. 2 per unit

□□ 101-200 units - Rs. 4.50 per unit

□□ 201 -500 units - Rs. 6 per unit

□□ > 501 units - Rs. 7 per unit

2. Develop a java application to implement currency converter (Dollar to INR, EURO to INR, Yen to INR and vice versa), distance converter (meter to KM, miles to KM and vice versa) , time conerter (hours to minutes, seconds and vice versa) using packages.

3. Develop a java application with Employee class with Emp\_name, Emp\_id, Address, Mail\_id, Mobile\_no as members. Inherit the classes, Programmer, Assistant Professor, Associate Professor and Professor from employee class. Add Basic Pay (BP) as the member of all the inherited classes with 97% of BP as DA, 10 % of BP as HRA, 12% of BP as PF, 0.1% of BP for staff club fund. Generate pay slips for the employees with their gross and net salary.

4. Design a Java interface for ADT Stack. Implement this interface using array. Provide

necessary exception handling in both the implementations

5. Write a program to perform string operations using ArrayList. Write functions for the following
  - a. Append - add at end
  - b. Insert – add at particular index
  - c. Search
  - d. List all string starts with given letter
6. Write a Java Program to create an abstract class named Shape that contains two integers and an empty method named print Area(). Provide three classes named Rectangle, Triangle and Circle such that each one of the classes extends the class Shape. Each one of the classes contains only the method print Area () that prints the area of the given shape.
7. Write a Java program to implement user defined exception handling.
8. Write a Java program that reads a file name from the user, displays information about whether the file exists, whether the file is readable, or writable, the type of file and the length of the file in bytes.
9. Write a java program that implements a multi-threaded application that has three threads. First thread generates a random integer every 1 second and if the value is even, second thread computes the square of the number and prints. If the value is odd, the third thread will print the value of cube of the number.
10. Write a java program to find the maximum value from the given type of elements using a generic function.
11. Design a calculator using event-driven programming paradigm of Java with the following options.
  - a) Decimal manipulations
  - b) Scientific manipulations
12. Develop a mini project for any application using Java concepts.

## **CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY**

1. Data Definition Commands, Data Manipulation Commands for inserting, deleting, updating and retrieving Tables and Transaction Control statements
2. Database Querying – Simple queries, Nested queries, Sub queries and Joins
3. Views, Sequences, Synonyms
4. Database Programming: Implicit and Explicit Cursors
5. Procedures and Functions
6. Triggers
7. Exception Handling
8. Database Design using ER modeling, normalization and Implementation for any application
9. Database Connectivity with Front End Tools
10. Case Study using real life database applications

## **CS8461 OPERATING SYSTEMS LABORATORY**

1. Basics of UNIX commands
2. Write programs using the following system calls of UNIX operating system  
fork, exec, getpid, exit, wait, close, stat, opendir, readdir
3. Write C programs to simulate UNIX commands like cp, ls, grep, etc.
4. Shell Programming
5. Write C programs to implement the various CPU Scheduling Algorithms
6. Implementation of Semaphores
7. Implementation of Shared memory and IPC
8. Bankers Algorithm for Deadlock Avoidance
9. Implementation of Deadlock Detection Algorithm
10. Write C program to implement Threading & Synchronization Applications
11. Implementation of the following Memory Allocation Methods for fixed partition
  - a) First Fit b) Worst Fit c) Best Fit
12. Implementation of Paging Technique of Memory Management
13. Implementation of the following Page Replacement Algorithms
  - a) FIFO b) LRU c) LFU
14. Implementation of the various File Organization Techniques
15. Implementation of the following File Allocation Strategies
  - a) Sequential b) Indexed c) Linked



## **CW8411 COMPUTATIONAL STATISTICS LABORATORY**

1. Install R and R Studio
2. Creation and manipulation of Vectors, Matrices, Arrays, Lists, Factors and Data Frames
3. Install of Packages and scripts for Importing and Exporting Data
4. Implement Control structures and Functions
5. Visualize Statistical Graphs using Scatter Plots, Box Plots, Whisker Plot, Histograms
6. Perform Data exploration and visualization techniques over a dataset.
7. Perform Data Query using SQL and R.
8. Create a data set and do statistical analysis on the data

## **Artificial Intelligence and Machine Learning**

### **21GE111 C PROGRAMMING LABORATORY**

1. Constructing flow charts using RAPTOR tools.
2. Programs using I/O statements and expression
3. Write a program to find whether the given line is horizontal or vertical.
4. Write a program to calculate the distance between two points  $p1(x1,y1)$ ,  $p2(x2,y2)$ .
5. Write a program to calculate the force for the given mass and acceleration.
6. Write a program to calculate the Young's modulus.
7. Write a program to calculate the type of solution based on its pH value.
8. Write a program to temperature conversion (Fahrenheit to Celsius and vice versa)
9. Programs using decision-making constructs.
10. Write a program to find whether the given year is leap year or Not? (Hint: not everycenturion year is a leap. For example 1700, 1800 and 1900 is not a leap year)
11. Design a calculator to perform the operations, namely, addition, subtraction, multiplication, division and square of a number.
12. Check whether a given number is Armstrong number or not?
13. Given a set of numbers like, find sum of weights based on the following conditions.
  - 5 if it is a perfect cube.
  - 4 if it is a multiple of 4 and divisible by 6.
  - 3 if it is a prime number.

Sort the numbers based on the weight in the increasing order as shown-below

<10,its weight>, <36,its weight>, <89,its weight>

14. Populate an array with height of persons and find how many persons are above the average height.
15. Populate a two dimensional array with height and weight of persons and compute the Body Mass Index of the individuals. Given a string —a\$bcd./fgl find its reverse without changing the position of special characters. (Example input: a@gh%;j and output: j@hg%;a)
16. Convert the given decimal number into binary, octal and hexadecimal numbers using user defined functions.
17. From a given paragraph perform the following using built-in functions:
  - a. Find the total number of words.
  - b. Capitalize the first word of each sentence.
  - c. Replace a given word with another word.
18. Solve towers of Hanoi using recursion.
19. Sort the list of numbers using pass by reference.
20. Generate salary slip of employees using structures and pointers. Create a structure Employee with the following members: EID, Ename, Designation, DOB, DOJ, Basic pay. Note that DOB and DOJ should be implemented using structure within structure.
21. Compute internal marks of students for five different subjects using structures and functions.
22. Insert, update, delete and append telephone details of an individual or a company into a telephone directory using random access file.
23. Count the number of account holders whose balance is less than the minimum balance using sequential access file.
24. Mini project: Create a —Railway reservation system with the following modules
  - Booking
  - Availability checking
  - Cancellation
  - Prepare chart

1. Array Manipulation
  - a. Find kth smallest element in an unsorted array
  - b. Find the sub array with given sum
  - c. Matrix manipulations – Addition, Subtraction, Multiplication
  - d. Job Sequencing: Given an array of jobs where every job has a deadline and a profit. Profit can be earned only if the job is finished before the deadline. It is also given that every job takes a single unit of time, so the minimum possible deadline for any job is 1. How to maximize total profit if only one job can be scheduled at a time. Print the sequence of jobID order to maximize total profit.
2. String manipulations:
  - a. Reversing a set of words and count the frequency of each letter in the string.
  - b. Pattern Recognition - Find the number of patterns of form 1[0]1 where [0] represents any number of zeroes (minimum requirement is one 0) there should not be any other character except 0 in the [0] sequence in a given binary string.
  - c. Remove all the occurrences of string S2 in string S1 and print the remaining.
3. Pointers
  - a. Manipulating two dimensional arrays using pointers.
  - b. Print all permutations of a given string using pointers.
4. Dynamic Memory Allocation
  - a. Find Largest Number.
  - b. Print the list in reverse order.
5. Array implementation of List, Stack and Queue ADTs.
6. Linked list implementation of List, Stack and Queue ADTs.
7. Applications of List, Stack and Queue ADTs.
8. Implementation of Binary Trees and operations of Binary Trees.
9. Implementation of Binary Search Trees.
10. Implementation of AVL Trees.
11. Implementation of Heaps using Priority Queues.
12. Graph representation and Traversal algorithms.
13. Implement searching and sorting algorithms. Analyze and compare the time taken for various algorithms with best, average and worst case inputs.

## Computing Facilities

### Internet Bandwidth

The Computer Centre is connected to leased line internet connection of 520 Mbps (500 Mbps leased line internet connection from AIRTEL and 20 Mbps leased line from TATA TELE Business Services). The Computer Centre provides internet services to all the systems in the Computer Centre and all the departments in campus. The Computer Centre provides Wi-Fi facility to all the departments in campus and provides 24 Hrs Wi-Fi facility to all the Hostel students.

### Number and Configuration of Systems

Particulars	No. of Systems
Dell Power Edge T620 Server	1
Dell Power Edge T420 Server	2
Apple imac Quad-Core-i5 Systems	30
Desktops with i9 Processor	30
Desktops with i7 Processor	445
Desktops with i3 and i5 Processors	135
iPad	4

### Total number of systems connected by LAN / WAN

All the Systems in the Computer Centre are connected by LAN and WAN

Sl.No.	Course Type	Total Student	Number of Terminals On LAN / WAN
1	B.E.	1440	460
2	B.Tech.	540	180

## Major software packages available

### System Software

#### **1. Windows Server:**

**Windows Server** R2 64-bit OS, power edge T420 Model from DELL , Intel Xeon Processor, Intel® Xeon(r) CPU E-5 2630 Mother Board, 2.3 GHZ Processor Speed, 24 GB DDR3 RAM, 2400 GB HDD

#### **2. Linux Operating System**

Ubuntu OS

#### **3. Windows Operating System**

Windows 11 and Windows 10

#### **4. Fortinet Firewall**

### Application Software

1. Office 2016 Professional plus with Core CAL
2. Visual Studio Pro 2016
3. SQL Client Access License
4. Rational Rose Software
5. Java Development Kit(JDK 1.5)
6. Turbo C and C ++ / GCC and G++ for Ubuntu
7. My-SQL
8. iOS – X Code
9. Android for Mobile App Development
10. NetBeans or Eclipse
11. Hi Class software Ver 4.2 / Business Management skill / Manage Stress Focus
12. OPNET / NS2 Simulator
13. LEX / YACC Tool
14. KF Sensor Tool / Tensor Flow / Knime Tool / Net Stumbler / Open Nebula Tool / Open Stack
15. Python and R-Tool
16. Apache / Tomcat Server
17. WAMP / XAMP
18. GnuPG / Snort / N-Stalker
19. Virtual box / Openstack / Hadoop / Coludism / GAE launcher
20. Argo UML / Eclipse IDE

## Innovation Cell

RMDEC IIC objective is to create a vibrant innovation ecosystem and Start-up supporting Mechanism and inculcate Ideas and Pre-incubation of Ideas. Develop better Cognitive Ability for RMDEC Engineering Students. To Prepare RMDEC for better for Atal Ranking

RMDEC IIC is headed by Principal as President and council of Staff members and student members

in the areas of Startup ,IPR,NIRF,ARIIA,Innovation,Incubation

### **Functions of RMDEC IICs**

- *To conduct various innovation and entrepreneurship-related activities prescribed by Central MIC in time bound fashion.*
- *Identify and reward innovations and share success stories.*
- *Organize periodic workshops/ seminars/ interactions with entrepreneurs, investors, professionals and create a mentor pool for student innovators.*
- *Network with peers and national entrepreneurship development organizations.*
- *Organize Hackathons, idea competition, mini-challenges etc. with the involvement of industries.*

### **Social Media Cell**

R.M.D. Engineering College has established an official presence on Facebook, Instagram, LinkedIn and Twitter. These social media accounts are all maintained by the Social Media Committee of the college.

With majority of student crowd present on social media, it provides us a platform to promote activities, receive feedback and start conversations. It provides a better way to connect with parents and community and keep them up to date. This is a very effective tool for Alumni Engagement. Thus, Connecting Students, Teachers, Parents, Alumni and other stakeholders, social media plays an important medium of communication.

Below is the link for the social media accounts.

Facebook:

<https://www.facebook.com/rmdecprincipal>

Instagram:

<https://www.instagram.com/rmdecprincipal>

LinkedIn:

<https://www.linkedin.com/in/rmdecprincipal>

Twitter:

<https://twitter.com/rmdecprincipal>

YouTube:

<https://www.youtube.com/RMDEnggCollege>

## List of Facilities available

### Games and Sports Facilities

Outdoor Games Play Field For MEN				
Sl.No..	NAME OF PLAY GROUND	Length	Width	Total Area
		(meters)		
1	FOOTBALL, HAND BALL, KABADDI & CRICKET	146.6	124	18178.40
2	VOLLEY BALLCOURT - I	20.49	10.6	217.19
3	VOLLEY BALLCOURT - II	20.49	10.6	217.19
4	VOLLEY BALLCOURT - III	27.52	12.86	353.91
5	VOLLEY BALL COURT - IV	27.52	12.86	353.91
6	BASKET BALLCOURT	35.3	21.9	773.07
7	TENNIS COURT – I	35.77	16.6	593.78
8	TENNIS COURT - II	35.77	16.6	593.78
9	BALL BADMINTION COURT	27.52	12.86	353.91
10	NON STANDARD TRACK	146.6	124	18178.40
Indoor Games Play Field For MEN				
Sl.No.	NAME OF PLAY GROUND	Length	Width	Total Area
		(meters)		
1	TABLE TENNIS	15	10	150.00
2	BADMINTON COURT – 1 ( II Yr. Hostel)	18.11	9.19	166.43
3	BADMINTON COURT – 1 ( I Yr. Hostel)	17.3	8.12	140.48
4	BADMINTON COURT – 2 (I Yr. Hostel)	17.3	8.12	140.48
5	CHESS AND CARROM	4	3	12.00
6	MULTI HYDRAULIC GYM	20.2	5.03	101.61

Outdoor Games Play Field For WOMEN				
Sl.No.	NAME OF PLAY GROUND	Length (meters)	Width	Total Area
1	VOLLEY BALL COURT - I	24.4	12.85	313.54
2	VOLLEY BALL COURT - II	24.4	12.85	313.54
3	BADMITON-I AND TENNIKOIT	27.14	9.87	267.87
4	TENNIS COURT	27.14	15.87	430.71
5	BADMINTON COURT III & FITNESS ZONE	23	23.60	542.80
Indoor Games Play Field For WOMEN				
Sl.No.	NAME OF PLAY GROUND	Length (meters)	Width	Total Area
1	TABLE TENNIS	15	10	150.00
2	CHESS AND CARROM	4	3	12.00
3	MULTI HYDRAULIC GYM	9.85	5	49.25

### List of sports Equipment

S.No	Name of the equipment	Numbers
1	Ball Badminton Ball	20 Nos
2	Ball Badminton Bat	20 Nos
3	Ball Badminton Net	01 No
4	Basket Ball	08 Nos
5	Basket Ball Board	01 Set
6	Basket ball Net	02 Nos
7	Carrom Board & coins	10 Box
8	Carrom Board Striker	10 Nos
9	Chess Board	10 Nos
10	Chess Clock	04 Nos
11	Cone	35 Nos
12	Cricket Abdomen Guard	12 Nos
13	Cricket Arm Guard	06 Nos
14	Cricket Balls (cork ball)	25 Nos
15	Cricket Bat - English willow	08 Nos
16	Cricket Bat - Kashmir willow	06 Nos
17	Cricket Batting Gloves	08 Pairs
18	Cricket Batting Inner Gloves	08 Pairs
19	Cricket Batting pads	08 Pairs

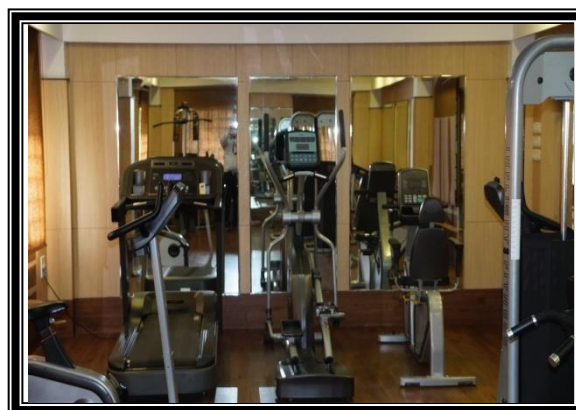


S.No	Name of the equipment	Numbers
20	Cricket Helmet	06 Nos
21	Cricket Mat	02 Nos
22	Cricket Kit Bag	04 Nos
23	Cricket seasoning hammer	02 Nos
24	Cricket Stumps	20 Nos
25	Cricket Thigh Pads	06 Nos
26	Cricket W. Keeper Gloves	04 Pairs
27	Cricket W. Keeper Inner Gloves	04 Pairs
28	Cricket W. Keeper Pads	02 Sets
29	Discuss 1 KG (Nelco)Women	02 Nos
30	Discuss 2 KG (Nelco)Men	02 Nos
31	Foot Ball	08 Nos
32	Foot Ball Gloves	03 Nos
33	Foot ball Post	01 Pair
34	Foot ball net	01 No
35	Foot Pump	01 Nos
36	Hammer throw - Men	01 No
37	Hammer throw - Women	01 No
38	Hand ball	04 Nos
39	Hand ball Glove	02 Nos
40	Hand ball Post	01 Pair
41	Hand ball Net	01 No
42	Hurdles	12 Nos
43	Javelin (600 gm) Nelco	02 Nos
44	Javelin (800 gm) Nelco	02 Nos
45	Measuring Tapes	01 No
46	Medicine ball	05 Nos
47	Racer Stop Watch	02 Nos
48	Relay Baton Nalco	08 Nos
49	Shot put 4 kg Nelco	01 No
50	Shot Put 7.26 kg Nelco	01 No
51	Shuttle Cock feather	02 Box
52	Shuttle Net	01 No
53	Shuttle Post Iron	02 Nos
54	Shuttle Racket	05 Nos
55	Skipping Rope	06 Nos

S.No	Name of the equipment	Numbers
56	Table Tennis Ball	20 Nos
57	Table Tennis Bat	10 Nos
58	Table Tennis Board	01 No
59	Table Tennis Net	01 No
60	Tennikoit ring ball	04 Nos
61	Tennis Ball	06 Nos
62	Tennis Ball for Cricket	20 Nos
63	Tennis Iron Rope	02 Nos
64	Tennis Net	02 Nos
65	Tennis Post	04 Nos
66	Tennis Racket	06 Nos
67	Throw Ball	03 Nos
68	Volley Ball (Cosco)	05 Nos
69	Volley Ball Net (Iron Rope)	04 Nos
70	Volley Ball Net (wire)	04 Nos
71	Volley Ball Post	06 Nos
72	Weighing Machine	01 No
73	Whistle fox 40	03 Nos

### **GYMNASIUM**

#### **MULTI - GYM**



We have well equipped power gym and hydraulic gym (Air Conditioned) with latest multiple machines. These gyms could accommodate 100 Students per session. Each session is engaged by experienced trainers.

### **Teaching Learning Process**

## Internal Continuous Evaluation System and place

Academic calendar is prepared matching the prescribed number of working days specified by Anna University. Academic calendar contains total number of working days month wise, schedule of University theory and practical examination, schedule for two internal evaluation tests for each of the courses and model examination and symposium schedule. The academic calendar is prepared by the academic co-ordinator in consultation with the heads of the departments and due approval of the principal. The institution strictly adheres to the academic calendar including for the conduct of continuous internal evaluation (CIE).

The academic calendar is issued well in advance of the ensuing semester to all the department faculty members and students. This enables effective planning by the faculty for each course and all curricular, co-curricular and extracurricular activities. Based on the academic calendar, lesson plan is prepared by the course coordinator, which includes unit wise topics with content beyond the syllabus, teaching aids used for effective course delivery.

A committee nominated by the head of the department of each programme frames academic timetable adhering to the academic calendar to ensure effective curriculum delivery both theory and laboratory courses.

The institution academic calendar provides the information and scheduled time table for continuous internal evaluation (CIE) and model examinations to enable the students to frame their action plan for the entire semester.

### Continuous Internal Evaluation process (CIE)

The subject wise Continuous Internal Evaluation process (CIE) timetable matching with the schedule given in the academic calendar is announced to the students one week prior to the commencement of the internal evaluation test schedule.

### Continuous internal evaluation (CIE) in theory courses:

**Table 1: Evaluation Tools and content**

Evaluation Tools	content	Marks	Duration
Internal Assessment I	Units I and II	100	Three Hours
Internal Assessment II	Units III and IV	100	
Model Examination	All Five Units	100	

- ❖ The Internal evaluation tests are conducted after the completion of every 2 units. The subject wise Internal evaluation test schedule and staff invigilation schedule for the test are prepared by the Exam-cell and circulated to all the departments.

### Continuous internal evaluation (CIE) in laboratory courses:

- ❖ Regular monitoring of the student's performance and ability to conduct the experiment during lab sessions.
- ❖ Evaluation of the student's observation book and record notebook.

- ❖ Evaluation by oral examination on the student's knowledge of the experiment
- ❖ Model practical examination.
- ❖ Internal mark is awarded to each student based on performance in the evaluation process.

### **Teaching Learning Process**

The primary focus at RMDEC is to move away from lecture-based classroom to more interactive teaching facilitated using ICT enabled tools. The teaching-learning strategy adopted is to create opportunity for multidisciplinary activities and for students' interaction with teachers, with each other, and with the industry. The objective is to embrace more interactive teaching and create a more supportive and challenging classroom setting for the students to learn actively.

For efficient implementation of interactive teaching techniques, the faculty are encouraged to use ICT enabled tools such as digital and online technology. Power-point presentations and animations, smart classrooms, interactive white-boards, guided web tours, learning Online resources, virtual labs, educational interactive audio-video chats, webinars, Course material and YouTube videos posted by the faculty of RMDEC, etc. are regularly used by the faculty.

The students are constantly encouraged to access E-Resources such as E-Books, digital libraries, online free simulation tools, and MOOC (massive open online courses such as NPTEL). The college provides facilities to use online platforms such as Skill Rack, AMCAT for the students to improve their programming skills. Quiz questions are also posted by the faculty on Skill Rack platform, for the students to practice online.

The teaching resource material and YouTube videos published online by the faculty of RMDEC has transformed the teaching- learning process from confined classroom delivery to open, flexible delivery for the benefit of students to learn independently by adopting ICT enabled tools for effective teaching-learning. The necessary links for the teaching resources and YouTube videos are provided on the college website.

The use of ICT tools for teaching-learning at RMDEC provides an affable atmosphere, inspiration, and impact on the process of learning.

Institution provides complete infrastructure for ICT enabled teaching-learning processes and for content development by the faculty. Facilities such as seminar halls, smart classrooms, equipped with smart boards, LCD projectors, video cameras, Internet, and Wi-Fi etc.

### **Mechanism of internal assessment is transparent and robust in terms of frequency and mode.**

Robust and Transparent system of internal assessment, the frequency and mode are very clearly stipulated in Anna University regulation 2017 (section 11 & 12, Page 8,9) and it is mandatory for the college to follow the system. The regulation is published for transparency on the University website under the head Academic, for public viewing.

### **Transparency initiatives at college level**

RMDEC on its part, to ensure awareness and transparency among students conducts special orientation programme on the examination system both university and internal assessment and award of internal marks, frequency and mode to all the first-year students at entry level itself.

The controller of examinations Anna University notifies the academic schedule for each semester to the college. Based on this schedule, an academic calendar is prepared by the college which includes schedules for two Internal Assessment Tests, Model Examination, and tentative date of commencement of university examinations. Hard copy

of the academic calendar is provided to each student and faculty of the college to ensure transparency. The Internal Assessment Tests and Model Examination are conducted as scheduled in the academic calendar.

The timetable for the Assessment test is prepared by the Exam cell and displayed on the department Notice Board one week in advance. It is also circulated to students. Proper Seating plan is followed for internal assessment tests, and it is displayed on the notice board as well as on the respective classrooms. Changes in schedules, patterns, methods if any, are immediately notified to the students through notice boards and through classroom briefing by the concerned subject teacher.

Two internal assessment tests and one model exam are conducted per semester. Exam cell allocates experienced faculty as overall course coordinator. Detailed instructions about the format of the question paper are given by notification from the principal.

#### **SMART PICK SYSTEM OF QUESTION PAPER SETTING:**

Question bank is prepared for each course by an experienced course coordinator nominated for each course by extracting and consolidating the questions prepared by different faculty handling the same course. The question bank covers Part – A, Part – B and Part C questions. The final question paper is generated using Smart Pick Soft Ware to assures transparency.

Internal assessment test answer papers are evaluated by the concerned course teachers whereas model exam answer papers are shuffled and evaluated by different teachers handling the same course thereby eliminating any bias. It is the practice of the college, to give the internal examination answer books after evaluation, to the students in the class for self-evaluation.

The Internal Assessment marks are entered periodically in Anna University Web Portal in four scheduled phases in a semester specified by the University. The students can view their internal assessment marks in the web portal through Anna University Student login.

Thus the mechanism of internal assessment system followed by the college guarantees transparency and is robust in terms of frequency and mode.

#### **Student's assessment of Faculty, System in place**

A class committee comprises of six student representatives, a faculty as Chairperson; branch coordinator, all faculty handling courses for the class & the Head of the Department as the special invitee. The committee is convened thrice in a semester. Quality of content delivery by faculty, syllabus completion etc are discussed in the committee as well as any welfare points raised by the students are recorded and appropriate remedial actions are initiated. Online feedback system is used to assess the effectiveness of curriculum delivery and are documented.

## Academic Time Table (2021-22)

### EVEN SEM

#### CSE

Day	Hour	CSE									
		IV SEM				VI SEM			VIII SEM		
		A	B	C	D	A	B	C	A	B	C
TUE	1	21CS402	21CS402	21CS401	21CS403	CS8603	CS8691	CS8602	GE8076	GE8076	GE8076
	2	21MA301	21CS401	21CS402	21CS404	CS8602	GE8076	GE8076	CS8080	CS8080	CS8080
	3	21CS404	21MA301	21CS403	21CS401	CS8691	CS8601	HS8581	Library	CS8811	GE8076
	4	21CS401	21CS403	21CS402	21MA301	CS8651	CS8602		GE8076		CS8080
	5	21CS412	21CS411	21EC441	21CS402	CS8661	CS8611	CS8603	CS8811		CS8811
	6			21MA301	21EC441			CS8601			
	7			21CS404			IT8076	CS8651	GE8076		
WED	1	21EC441	21CS404	21CS403	21CS402	GE8076	CS8651	CS8691	CS8080	CS8811	CS8080
	2	21CS403	21CS412	21CS411	21EC441	CS8601	CS8661	CS8603	GE8076		GE8076
	3	21CS401			21MA301	CS8611		CS8611	CS8811		Library
	4	21CS402			21CS404					GE8076	CS8811
	5	21MA301	21CS401	21MA301	21CS401	HS8581	CS8602	CS8602	CS8080	CS8811	
	6	21CS404	21EC441	21CS401	21MA301		CS8603	IT8076	CS8080		GE8076
	7	Sports	Sports	Sports	Sports	Sports	Sports	Sports	Sports	Sports	Sports
THU	1	21CS404	21CS403	21EC441	21MA301	CS8601	IT8076	GE8076	GE8076	CS8080	CS8080
	2	21CS402	21CS401	21MA301	21CS402	CS8662	CS8603	CS8602	CS8811	Library	GE8076
	3	21MA301	21CS403	21CS401	21EC441		HS8581	IT8076		CS8811	CS8811
	4	21CS401	21MA301	21CS404	21CS403			CS8651			
	5	21EC441	21CS402	21CS412	21CS411	IT8076	CS8602	CS8661	CS8080	CS8080	CS8811
	6	21MA301	21EC441			CS8602	CS8691				
	7	21CS403				CS8603	CS8651		GE8076	GE8076	
FRI	1	21CS403	21MA301	21CS402	21CS404	CS8651	GE8076	IT8076	CS8811	CS8811	CS8811
	2	21CS411	21CS402	21CS403	21CS412	CS8603	IT8076	CS8601			
	3		21CS401	21MA301		CS8602	CS8602	Library			
	4		21MA301	21CS401		GE8076	CS8601	CS8603			
	5	21CS402	21EC441	Library/ Mini Project	Library/ Mini Project	CS8691	CS8662	CS8691			
	6	21EC441	21CS404	21EC441	21CS403	IT8076		CS8602			
	7	Library / Mini Project	Library/ Mini Project		21CS401	CS8602		GE8076			
SAT	1	21CS414	21CS414	21CS414	21CS414	CS8691	CS8601	CS8651	CS8811	CS8811	CS8811
	2					CS8651	GE8076	CS8691			
	3	21CS403	21CS404	21EC441	21CS403	CS8601	CS8651	CS8602			
	4	21CS401	21CS402	21CS402	21MA301	GE8076	CS8602	CS8601			
	5	21MA301	21EC441	21CS404	21CS401	Library	CS8603	CS8662			
	6	21EC441	21MA301	21CS403	21EC441	CS8602	Library				
	7		21CS403	21MA301	21CS402	IT8076	CS8691				

Day	Hour	ECE									
		IV SEM				VI SEM			VIII SEM		
		A	B	C	D	A	B	C	A	B	C
TUE	1	21EC404	21EC403	21MA401	21EC404	EC8004	MG8591	EC8691	GE8076	GE8076	GE8076
	2	21EC401	21MA401	21EC411	21EC412	EC8691	EC8651	EC8681 / EC8661	EC8094	EC8094	EC8094
	3	21EC403	LIBRARY			GE8076	EC8651		GE8076	GE8076	GE8076
	4	21MA401	21EC402			EC8095	LIBRARY		EC8094	GE8076	EC8094
	5	21GE301	21EC404	21EC402	21EC402	HS8581	EC8004	EC8004	GE8076	EC8094	GE8076
	6	21EC402	21MA401	21EC404	21MA401		EC8652	EC8095	EC8811	EC8811	EC8811
	7	SPORTS	SPORTS	SPORTS	SPORTS	SPORTS	SPORTS	SPORTS			
WED	1	21EC403	21MA401	21EC401	21GE301	MG8591	EC8652	MG8591	EC8094	EC8094	EC8094
	2	21EC411	21EC412	21EC403	21EC401	EC8651	EC8095	EC8652	GE8076	GE8076	GE8076
	3			21MA401	21EC402	LIBRARY	HS8581	EC8651	EC8094	EC8094	EC8094
	4			21EC402	21MA401	EC8691		LIBRARY	GE8076	GE8076	GE8076
	5	21MA401	21EC404	21EC412	LIBRARY	EC8681 / EC8661	GE8076	EC8095	EC8094	EC8094	EC8094
	6	21EC402	21EC401		21EC403		EC8691	EC8691	EC8811	EC8811	EC8811
	7	21GE301	21GE301		21MA401		MG8591	GE8076			
THU	1	21EC401	21EC404	21EC404	21MA401	EC8651	GE8076	EC8651	EC8811	EC8811	EC8811
	2	21EC403	21EC402	21MA401	21EC403	EC8095	EC8651	EC8095			
	3	21EC404	21EC401	21EC402	21EC404	EC8691	EC8652	EC8652			
	4	21EC402	21EC403	21EC401	21EC401	MG8591	EC8095	EC8691			
	5	21EC404	21MA401	LIBRARY	21EC411	EC8652	EC8681 / EC8661	EC8004			
	6	LIBRARY	21EC413	21EC403		EC8004		MG8591			
	7	21MA401		21GE301		GE8076		GE8076			
FRI	1	21EC402	21EC403	21EC403	21EC401	GE8076	EC8691	GE8076	EC8811	EC8811	EC8811
	2	21MA401	21EC404	21GE301	21MA401	EC8652	EC8661/ EC8681	EC8652			
	3	21EC401	21MA401	21EC413	21EC402	MG8591		HS8581			
	4	21EC403	21EC402		21EC403	EC8095					
	5	21EC412	21EC411	21EC402	21EC404	EC8661 / EC8681	EC8004	EC8651			
	6			21MA401	21EC413		EC8095	EC8611			
	7			21EC404			MG8591				
SAT	1	21MA401	21EC401	21MA401	21EC403	EC8652	EC8004	EC8004	EC8811	EC8811	EC8811
	2	21EC404	21EC402	21EC401	21EC404	EC8691	EC8095	EC8691			
	3	21EC414	21EC414	21EC414	21EC414	EC8004	GE8076	MG8591			
	4					EC8095	EC8651	EC8095			
	5	21GE301	21EC403	21EC403	21EC401	EC8651	EC8691	EC8661 / EC8681			
	6	21EC413	21GE301	21EC404	21GE301	EC8611	EC8691				
	7			21GE301							

## EEE

Day	Hour	EEE	EEE
		VI SEM	VIII SEM
TUE	1	EE8602	EE8018
	2	EE8002	MG8591
	3	EE8691	EE8018
	4	EE8681	MG8591
	5		MG8591
	6		MG8591
	7		MG8591
WED	1	EE8691	MG8591
	2	EE8005	EE8018
	3	LIB	MG8591
	4	MG8591	EE8018
	5	EE8602	EE8018
	6	EE8601	EE8018
	7	EE8002	MG8591
THU	1	EE8005	EE8018
	2	EE8601	MG8591
	3	EE8602	EE8018
	4	EE8661	MG8591
	5		EE8018
	6		EE8018
	7		EE8018
FRI	1	MG8591	EE8811
	2	EE8691	
	3	EE8002	
	4	EE8611	
	5		
	6		
	7		
SAT	1	EE8002	EE8811
	2	MG8591	
	3	EE8601	
	4	EE8602	
	5	EE8691	
	6	EE8005	
	7	SPO/INT	

## EIE

Day	Hour	EIE
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		<b>VIII SEM</b>
TUE	1	GE8076
	2	EI8078
	3	GE8076
	4	EI8078
	5	GE8076
	6	EI8078
	7	GE8076
WED	1	EI8078
	2	GE8076
	3	EI8078
	4	GE8076
	5	EI8078
	6	GE8076
	7	EI8078
THU	1	GE8076
	2	EI8078
	3	GE8076
	4	EI8078
	5	GE8076
	6	EI8078
	7	GE8076
FRI	1	EI6811 - Project Work
	2	
	3	
	4	
	5	
	6	
	7	
SAT	1	EI6811 - Project Work
	2	
	3	
	4	
	5	
	6	
	7	

# IT

Day	Hour	IT			
		IV SEM		VI SEM A	VIII SEM A
		A	B		
TUE	1	21CS401	21MA303	CS8091	IT8005
	2	21MA303	21IT401	CS8092	GE8076
	3	21IT402	21CS401	IT8602	IT8005
	4	21GE301	21IT402	IT8601	GE8076
	5	21MA303	21IT403	CS8662	IT8811
	6	21IT401	21CS401		
	7	21IT403	21GE301		
WED	1	21IT401	21IT403	CS8092	GE8076
	2	21CS401	21MA303	CS8091	IT8005
	3	21IT403	21IT402	IT8601	GE8076
	4	21MA303	21IT401	IT8076	IT8005
	5	21IT411	21IT411	CS8582	IT8811
	6				
	7				
THU	1	21IT403	21GE301	IT8602	IT8005
	2	21IT402	21CS401	GE8076	GE8076
	3	21GE301	21MA303	CS8592	IT8005
	4	21MA303	21IT403	IT8076	GE8076
	5	21IT401	21IT401	HS8581	IT8811
	6				
	7	Mini project/SPORTS	Mini project/SPORTS	Mini project/SPORTS	
FRI	1	21IT402	21IT402	IT8076	GE8076
	2	21MA303	21IT403	IT8601	IT8005
	3	21CS401	21CS401	GE8076	GE8076
	4	21IT403	21MA303	CS8091	IT8005
	5	21IT412	21IT412	CS8592	IT8811
	6			IT8602	
	7			CS8092	
SAT	1	21GE301	21MA303	IT8611	IT8005
	2	21IT402	21IT401		GE8076
	3	21CS414	21CS414	LIB	IT8005
	4			CS8592	GE8076
	5	21IT401	21IT402	CS8091	IT8811
	6	21CS401	21GE301	GE8076	
	7	LIB	LIB	IT8601	

## CSBS

Day	Hour	CSBS		
		IV SEM		VI SEM A
		A	B	
TUE	1	21CB407	21CB407	CW8601
	2	21CB407	21CB407	CS8691
	3	21CB405	21CB406	CS8791
	4	21GE301	21GE301	AD8551
	5	21GE301(T)	21GE301(T)	CS8602
	6	21GE301(T)	21GE301(T)	CW8016
	7	Mini Project	Mini Project	AD8551
WED	1	21CB404	21CB403	CW8612
	2	21CB406	21CB401	CW8612
	3	21CB401	21CB401	CW8612
	4	21CB401	21CB401	CW8612
	5	21CB403	21EL401	CS8602
	6	21CB401	21CB404	CS8791
	7	21EL401	21CB406	CW8601
THU	1	21CB405	21CB401	CW8016
	2	21CB403	21CB405	CS8791
	3	21CB402	21CB402	CS8602
	4	21CB404	21CB403	CS8602
	5	21CB402	21CB402	CW8691
	6	21CB402	21CB402	AD8551
	7	21CB406	21EL401	CW8691
FRI	1	21EL401	21CB402	CW8691
	2	21CB405	21CB403	CS8602
	3	21CB403	21CB403	AD8551
	4	21CB403	21CB403	Library
	5	21CB402	21CB404	CW8601
	6	21CB401	21CB405	CS8691
	7	Library	Library	CW8016
SAT	1	21CB401	21CB404	CS8691
	2	21CB402	21CB401	CS8602
	3	21CB402	21CB405	CS8791
	4	21CB406	21CB406	CW8601
	5	21CB405	21CB405	CW8691
	6	21CB405	21CB405	CW8691
	7	Sports/ Technical seminar	Sports/ Technical seminar	Sports/ Technical seminar

## AIML

Day	Hour	AIML	
		IV SEM	
		A	B
TUE	1	21AM411	21AM411
	2		
	3		
	4		
	5	21MA402	21AM404
	6	21CS402	21MA402
	7	21AM401	21AM402
WED	1	21MA402	21AM401
	2	21AM404	21AM402
	3	21AM403	21AM404
	4	21MA402	21CS402
	5	21CS402	21AM403
	6	Mni Project/Library	Mni Project/Library
	7	21AM402	21AM401
THU	1	21AM412	21AM412
	2		
	3		
	4		
	5	21AM403	21MA402
	6	21AM401	21CS402
	7	21MA402	21AM403
FRI	1	21AM402	21MA402
	2	21AM404	21AM403
	3	21MA402	21AM404
	4	21AM403	21AM401
	5	21AM402	21CS402
	6	21AM401	21AM402
	7	21CS402	21MA402
SAT	1	21CS414	21CS414
	2		
	3	21AM404	21CS402
	4	21CS402	21MA402
	5	21AM404	21AM404
	6		
	7	Mini Project/Sports	Mini Project/Sports

## ODD SEM

### CSE

Da y	Ho ur	CSE										
		III SEM				V SEM			VII SEM			
		A	B	C	D	A	B	C	A	B	C	
TU E	1	21CS311	21CS301	21MA302	21MA302	OCE552	OCE552	CS8501	MG8591	OME752	CS8792	
	2		21MA302	21CS301	21IT403	CS8591	EC8691	EC8691	T & P	CS8791	CS8791	
	3		21GE301	21CS302	21CS303	CS8592	MA8551	CS8591	OME752	CS8079	OME752	
	4	21IT412	21IT403	21GE301-T	MA8551	CS8501	MA8551	CS8711	IT8761	CS8079		
	5			21GE301	21CS302	EC8691	CS8592			OCE552	OME752	
	6		21CS303	21CS301	CS8501	CS8591	CS8591			MG8591		
	7		21MA302		21GE301-T	CS8592	MA8551			CS8592	T & P	
WE D	1	21CS303	21CS311	21IT412	21CS301	CS8581	CS8582	EC8681	CS8791	CS8079	OME752	
	2	21MA302			21CS302				CS8079	T & P	CS8792	
	3	21CS302			21MA302				CS8791	MG8591	CS8791	
	4	21CS301			21GE301							CS8792
	5	21MA302	21CS302	21CS303	MA8551	CS8501	MA8551	MG8591	CS8711	IT8761		
	6	21GE301-T	21CS303	21MA302	21IT403	CS8592	CS8591	OCE552			CS8792	
	7	Sports/Net/21CS312	Sports/Net/21CS312	Sports/Net/21CS312	Sports/Net/21CS312	Internet/Sports/Mini project	Internet/Sports/Mini project	Internet/Sports/Mini project	OME752			
TH U	1	21CS301	21IT403	21CS311	21MA302	MA8551	EC8691	EC8691	HX8001	HX8001	HX8001	
	2	21GE301	21MA302		21GE301	CS8591	OCE552	CS8592				
	3	21MA302	21CS301		21CS303	OCE552	MA8551	MA8551				
	4	21GE301	21IT412	21CS301	EC8681	CS8581	CS8582	CS8791	CS8791	CS8792		
	5	21CS303						21CS302	21MA302	MG8591	CS8079	MG8591
	6	21IT403						21GE301-T	CS8792	CS8792	CS8079	
	7	21CS302						21MA302	21CS302	CS8792	CS8792	CS8079
FRI	1	21IT412	21CS303	21CS303	21CS311	CS8501	OCE552	MA8551	CS8079	MG8591	CS8791	
	2		21CS302	21GE301		EC8691	CS8591	CS8591	MG8591	Library	MG8591	
	3		21CS301	21MA302		CS8591	CS8592	Library/Miniproject	CS8792	OME752	CS8791	
	4		21MA302	21IT403		CS8582	EC8681	CS8581	CS8079	CS8792	Library	
	5	21CS301	21GE301	Lib/21CS312	Lib/21CS312				OME752	CS8791	CS8079	

	6	21MA302	21IT403	21CS301	21MA302				Library	MG8591	CS8792
	7	Lib/21CS312	Lib/21CS312		21CS301				Sports / T & P	Sports / T & P	Sports / T & P
S A T	1	21IT403	21CS303	21IT403	21CS303	MA8551	CS8592	CS8592	OME752	CS8792	CS8079
	2	21CS303	21IT403	21CS303	21IT403	CS8592	CS8591	EC8691	CS8791	CS8791	OME752
	3	21CS301	21CS302	21CS301	21CS301	CS8501	EC8691	CS8501	CS8079	OME752	MG8591
	4	21IT403	21CS303	21CS303	21IT403	CS8591	CS8501	CS8592	IT8761	MG8591	CS8711
	5	21CS302	21CS301	21MA302	21CS302	Library/Mini project	MA8551	EC8691		CS8792	
	6	21MA302	21GE301-T	21GE301-T	21MA302	EC8691	Library/Miniproject	CS8501		CS8079	
	7	21GE301-T	21IT403	21IT403	21GE301-T	OCE552	CS8592	OCE552		OME752	

## ECE

D a y	H o u r	ECE									
		III SEM				V SEM			VII SEM		
		A	B	C	D	A	B	C	A	B	C
T U E	1	21EC303	21EC303	21EC304	21EC302	OMD551	EC8501	OMD551	EC8751	OIC751	EC8702
	2	21MA303	21EC301	21EC303	21EC301	EC8563	EC8561 / EC8562	EC8073	PROFES SIONAL READIN ESS	PROFES SIONAL READIN ESS	PROFES SIONAL READIN ESS
	3	21CS202	LIBRARY	21MA303	21EC303			EC8552			
	4	21EC304	21MA303	21EC302	21MA303			LIBRARY			
	5	21CS313	21CS202	21CS202	21CS202	EC8551	EC8073	EC8553	EC8702	LIBRARY	OIC751
	6					EC8552	EC8553	EC8501	EC8701	EC8751	LIBRARY
	7	SPORTS/INTERNET	SPORTS/INTERNET	SPORTS/INTERNET	SPORTS/INTERNET	SPORTS/INTERNET	SPORTS/INTERNET	SPORTS/INTERNET	EC8791	EC8702	EC8701
W E D	1	21EC302	21CS202	21MA303	21CS202	EC8553	EC8073	EC8553	EC8791	EC8751	OIC751
	2	21EC311	21EC304	21EC304	21EC301	EC8501	EC8552	EC8561 / EC8562	EC8711 / EC8761	EC8701	EC8751
	3		21EC301	21CS202	21EC303	LIBRARY	EC8551			EC8791	EC8702
	4		21MA303	21EC301	21EC304	OMD551	LIBRARY			OIC751	EC8751
	5	21EC301	21EC304	21EC311	LIBRARY	EC8561 / EC8562	EC8553	EC8501	OIC751	EC8702	EC8791
	6	21MA303	21EC303		21EC301		OMD551	EC8073	EC8751	EC8791	OIC751
	7	21EC304	21EC302		21MA303		EC8501	EC8551	EC8702	EC8701	EC8791
	1	21MA303	21EC312	21CS202	21EC301	EC8551	EC8552	EC8501	OIC751	EC8702	EC8751

T H U	2	21EC302		21EC302	21EC304	EC8553	EC8563	OMD551	LIBRARY	EC8711 / EC8761	EC8702
	3	21EC303	21EC301	21EC303	21EC312	EC8073		EC8501	EC8701		OIC751
	4	21EC301	21MA303	21MA303		EC8553		EC8552	EC8791		EC8791
	5	21EC304	21EC311	LIBRARY	21EC302	EC8552	OMD551	EC8562/E C8561	EC8751	EC8701	EC8711 / EC8761
	6	LIBRARY		21EC301	21EC304	EC8501	EC8551		EC8702	EC8791	
	7	21CS202		21EC302	21MA303	OMD551	EC8552		OIC751	EC8751	
F R I	1	21EC312	21MA303	21EC301	21EC302	EC8552	OMD551	EC8551	EC8702	EC8701	EC8751
	2		21EC302	21MA303	21CS202	EC8562 / EC8561	EC8553	OMD551	EC8761 / EC8711	OIC751	EC8701
	3	21EC303	21EC301	21EC312	21MA303		EC8551	EC8552		EC8791	EC8702
	4	21EC301	21EC303		21EC302		EC8553	EC8553		EC8702	OIC751
	5	21CS202	21EC302	21EC304	21EC311	EC8501	EC8552	EC8563	EC8751	EC8761 / EC8711	EC8791
	6	21MA303	21EC304	21EC301		EC8073	EC8501		EC8701		EC8701
	7	21EC302	21CS202	21CS202		EC8553	EC8073		EC8791		EC8791
S A T	1	21EC304	21EC302	21EC303	21MA303	EC8552	EC8553	EC8073	OIC751	EC8791	EC8701
	2	21EC301	21EC301	21MA303	21EC303	EC8553	EC8501	EC8553	EC8701	EC8751	EC8761 / EC8711
	3	21CS202	21CS313	21CS313	21CS313	SKILL RACK	SKILL RACK	SKILL RACK	EC8702	EC8701	
	4								EC8701	EC8751	
	5	21MA303	21EC304	21EC304	21EC301	EC8073	EC8562 / EC8561	EC8552	EC8791	OIC751	EC8702
	6	21EC302	21CS202	21EC302	21CS202	EC8501		EC8553	OIC751	EC8702	EC8751
	7	21EC301	21MA303	21EC301	21EC304	EC8551		EC8551	SPORTS	SPORTS	SPORTS

EEE

Day	Hour	EEE	
		VI SEM	VIII SEM
		A	A
TUE	1	EE8511	EE8703
	2		EE8010
	3		EE8703
	4		EE8702
	5	EE8551	OCS752
	6	EE8552	EE8701
	7	CS8392	EE8010
WED	1	EE8591	GE8074
	2	OAN551	OCS752
	3	Library/Mini Project	EE8010
	4	EE8551	EE8701
	5	EE8501	GE8074
	6	OAN551	EE8702
	7	EE8591	Library
THU	1	EE8501	EE8701
	2	EE8591	GE8074
	3	EE8552	OCS752
	4	CS8383	EE8711(A1) / EE8712 (A2)
	5		
	6		
	7		
FRI	1	CS8392	EE8010
	2	EE8551	EE8702
	3	EE8552	EE8701
	4	EE8501	EE8703
	5	EE8591	PROJECT
	6	HS8581	
	7		Internet / Sports
SAT	1	Aptitude Training	OCS752
	2		EE8702
	3	EE8501	EE8703
	4	OAN551	EE8711 (A2) / EE8712 (A1)



	5	CS8392	
	6	EE8551	
	7	Internet / Sports/ Mini Project	

## EIE

Day	Hour	EIE
		VII SEM
TUE	1	EC8093
	2	EI8751
	3	OBT751
	4	EI8762
	5	
	6	
	7	
WED	1	OBT751
	2	EE8691
	3	EI8751
	4	EI8075
	5	Library
	6	GE8077
	7	EC8093
THU	1	GE8077
	2	EE8691
	3	EC8093
	4	OBT751
	5	EI8075
	6	Project
	7	
FRI	1	EI8751
	2	EE8691
	3	GE8077
	4	EI8751
	5	OBT751
	6	EC8093

	7	EI8075
SAT	1	EI8075
	2	EE8691
	3	EI8761
	4	
	5	
	6	
	7	Sports/Internet

## IT

Day	Hour	IT			
		III SEM		V SEM A	VII SEM A
		A	B		
TUE	1	21MA302	21IT301	EC8691	GE8077
	2	21CS402	21CS301	MA8551	CS8792
	3	21IT301	21EC341	CS8591	MG8591
	4	21CS301	21CS402	OCE552	CS8791
	5	21EC341	21MA302	EC8681	IT8761
	6	21IT312	21IT312		
	7				
WED	1	21CS402	21CS301	MA8551	CS8791
	2	21CS412	21CS412	EC8691	OME752
	3			CS8591	CS8792
	4			IT8501	MG8591
	5	21MA302	21EC341	IT8511	IT8711
	6	21CS301	21CS402		
	7		21IT301		
THU	1	21CS404	21CS301	CS8494	MG8591
	2	21CS402		IT8501	CS8792
	3	21CS301	21MA302	CS8591	GE8077
	4	21EC341	21IT301	EC8691	CS8791
	5	21MA302	21CS402	OCE552	GE8077
	6	21IT301	21CS404	MA8551	MG8591
	7	INT/SPORTS	INT/SPORTS	INT/SPORTS	INT/SPORTS
FRI	1	21IT301	21EC341	OCE552	OME752
	2	21MA302	21CS301	CS8494	EEC
	3	21CS404	21CS404	CS8591	

	4	21CS301	21MA302	MA8551	
	5	21IT311	21IT311	IT8501	GE8077
	6			EC8691	OME752
	7			CS8494	CS8791
SAT	1	21CS313	21CS313	CS8494	CS8792
	2			IT8501	MG8591
	3	21CS404	21CS402	LIB	GE8077
	4	21IT301	21CS404	OCE552	LIB
	5	21CS402	21MA302	CS8581	CS8791
	6	21EC341	21IT301		CS8792
	7	LIB	LIB		OME752

## CSBS

Day	Hour	CSBS		
		III SEM		V SEM A
		A	B	
TUE	1	21MA301 LAB	21MA301 LAB	CW8501 LAB
	2			
	3	21CB301 LAB	21CB301 LAB	CW8503 LAB
	4			
	5	21CB305	21MA301	CS8501
	6	21CB304	21CB301	CW8591
	7	21MA301	21CB304	CW8502
WED	1	21CB305	21MA301	CW8501
	2	21MA301	21CB301	CW8503
	3	21CB303	21CB303	OMD551
	4	21CB301	21CB304	CW8502
	5	21CB302 LAB	21CB302 LAB	CW8502 LAB
	6			
	7	21CB304	21CB305	CW8591
THU	1	21CB304	21CB301	CW8502
	2	21MA301	21CB302	CS8501
	3	Library	Library	CW8503
	4	21CB301	21CB303	CW8591
	5	21CB302	21CB304	OMD551
	6	21MA301	21CB305	CW8501

	7	21CB303	21CB303	Library
FRI	1	21CB302	21CB302	CW8511
	2	21CB301	21MA301	
	3	21CB303      LAB	21CB303      LAB	
	4			
	5	21CB304   LAB	21CB304   LAB	CW8501
	6			CW8502
	7	Sports/ Mini Project	Sports/ Mini Project	Sports/ Technical Seminar
SAT	1	21CB303	21MA301	CW8503
	2	21CB304	21CB301	CS8501
	3	21CB306 /21CB313	21CB306 /21CB313	Aptitude and Coding Skills – I
	4			
	5	21MA301	21CB303	CW8512    LAB
	6	21CB305	21CB304	
	7	21CB302	21CB302	OMD551

## AIML

Day	Hour	AIML	
		III SEM	
		A	B
TUE	1	21MA303	21AM301
	2	21CS302	21MA303
	3	21AM301	21GE301
	4	21AM302	21CS302
	5	21IT403	21AM302
	6	21GE301	21MA303
	7	21GE301(T)	21IT403
WED	1	21CS311	21CS311
	2		
	3		
	4		
	5	21GE301	21CS301
	6	21AM311/Lib	21AM311/Lib
	7	21MA303	21AM301
THU	1	21AM301	21MA303

	2	21MA303	21AM302
	3	21CS302	21AM301
	4	21IT412	21IT412
	5		
	6		
	7		
FRI	1	21AM302	21AM302
	2		
	3	21AM302	21CS302
	4	21GE301(T)	21IT403
	5	21MA303	21GE301(T)
	6	21IT403	21AM302
	7	21AM301	21MA303
SAT	1	21CS313	21CS313
	2		
	3	21CS302	21MA303
	4	21MA303	21IT403
	5	21IT403	21GE301
	6	21AM302	21GE301(T)
	7	21AM311/Sports/Internet	21AM311/Sports/Internet

## 16. Enrollment of the Students in last 3 years

Dept	Academic year 2022-23				Academic year 2021-22				Academic year 2020-21			
	Sanctioned	Admitted			Sanctioned	Admitted			Sanctioned	Admitted		
		CAT-I	CAT-II	Total		CAT-I	CAT-II	Total		CAT-I	CAT-II	Total
CSE	180	83	104	187	180	69	107	176	180	65	115	180
ECE	180	79	104	183	180	71	105	176	180	72	108	180
EEE	-	-	-	-	-	-	-	-	60	20	8	28
IT	60	29	34	63	60	27	32	59	60	23	37	60
CSBS*	60	20	40	60	60	25	33	58	60	21	34	55
AIML**	60	28	33	61	60	25	33	58	-	-	-	-
TOTAL	540	239	315	554	540	217	310	527	540	201	302	503

\* CSBS started from 2020-21

\*\* AIML started from 2021-22

**Campus placement in last three years with minimum salary, maximum salary and average salary**

Batch	No. of Students Eligible	No. of Students Placed	Placement %	Min. Salary (Lakhs)	Max. Salary (Lakhs)	Avg. Salary (Lakhs)
2019-23	415	380	91.57	2.50 Lakhs	8.40 Lakhs	4.81 Lakhs
2018-22	391	376	96.16	1.90 Lakhs	8.40 Lakhs	4.50 Lakhs
2017-21	399	350	87.72	1.80 Lakhs	10.00 Lakhs	4.46 Lakhs
2016-20	355	322	90.70	1.44 Lakhs	14.00 Lakhs	4.32 Lakhs

\*Placement in Progress

**17. List of Research Projects / Consultancy Works**

**Number of Projects carried out, funding agency, Grant received.**

**From 2020 to 2022**

S. No	Department	No. of research projects	Funding Agency	Scheme	Amount Received in Rs	Total Amount in Rs
1	CSE	2	AICTE	Skill and Personality Development Centre for SC/ST Students	2,50,000	3,43,000
			AICTE-ISTE	AICTE-ISTE Induction/Refresher	93,000	
2	ECE	2	AICTE	MODROB	2,04,000	7,56,333
			AICTE	Skill and Personality Development Centre for SC/ST Students	5,48,333	
				TNSCST Student Project	4000	
3	IT	1	AICTE	MODROB	10,03,922	10,03,922
4	E&I	1	AICTE	AICTE-ATAL FDP	93,000	93,000
<b>TOTAL</b>		<b>06</b>				<b>21,96,255</b>

Click the below link for more details on Research / Consultancy projects Completed/Applied/ Ongoing

<https://rmd.ac.in/research/fp.html>

### Publications (if any) out of research in last three years

S.No	Department	No. of Research publications				
		2022	2021	2020	2019	2018
1	CSE	71	70	19	25	12
2	ECE	54	66	37	21	25
3	EEE	5	11	8	13	23
4	EIE	9	03	4	06	11
5	IT	19	30	8	14	18
	CSBS	7				
	AIML	9				
6	S&H	41	24	14	11	19
<b>TOTAL</b>		<b>215</b>	<b>204</b>	<b>90</b>	<b>90</b>	<b>90</b>

Click the below link for the details of the publications

<https://rmd.ac.in/research/rp.html>

### Industry linkage

The following Centre of Excellence (CoE) are established in our institution in association with industries in the relevant field.

Sl. No.	Name of the CoE	Industries Associated
1	Artificial Intelligence (AI CoE)	Cognizant and Virtusa
2	Automotive Electronics (AE CoE)	Virtusa
3	Cloud Computing (CC CoE)	Virtusa
4	Cyber Security (CS CoE)	Tata Consultancy Services
5	Embedded SystemS (ES CoE)	HCL
6	Factory Automation (FA CoE)	Mitsubishi Electric
7	Full Stack Engineering (FS CoE)	Virtusa
8	Front End Engineering (FEE CoE)	Virtusa
9	Internet of Things (IOT CoE)	Tata Consultancy Services
10	Robotic Process Automation (RPA)	Ui Path
11	Telecom (TCoe)	Wipro

## MoUs with Industries

Sl. No.	Name Of the Industry
1.	Bosch Ltd
2.	Hcl Technologies Ltd
3.	Infosys Ltd.
4.	Johnson Controls
5.	Kamachi Industries
6.	Kpit Technologies Ltd.
7.	Microsoft Dynamic
8.	Mitsubishi Electric India Private Limited
9.	Nucleus Satellite Communications Madras Pvt. Ltd.
10.	Soliton Technologies Private Limited
11.	Tata Consultancy Services
12.	Thejo Engineering
13.	VIRTUSA CONSULTING SERVICES PRIVATE LIMITED (Datascience,Full Stack,Cloud Computing,Front End Technology)
14.	Western Thomson
15.	Mitsuba India Pvt Ltd.
16.	Tata Elxsi
17.	Kobelco
18.	Nec Corporation India Private Limited
19.	Ntt Data
20.	Bosch Ltd